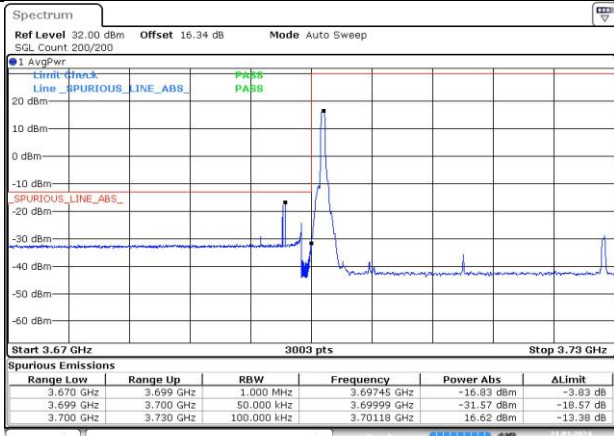


DC\_7A\_n77A / 30KHz / 30MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



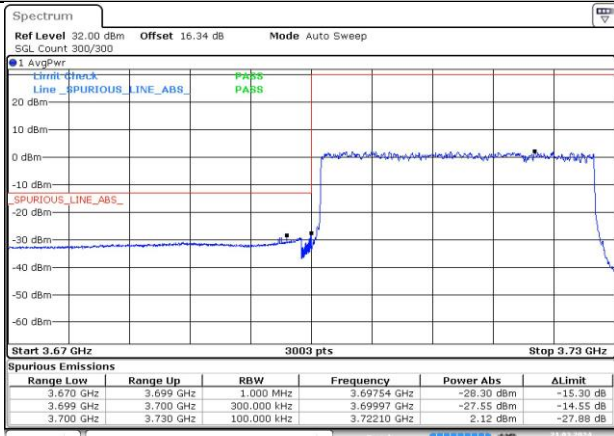
Date: 21.MAR.2024 11:30:39

HCH / DFT-Pi2BPSK / Edge\_1RB\_Right



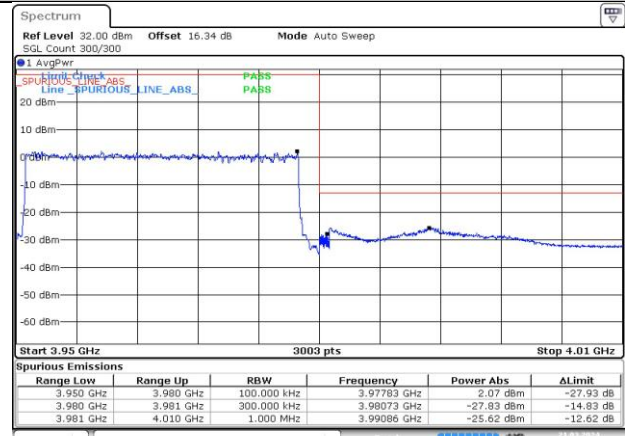
Date: 21.MAR.2024 11:32:24

LCH / DFT-Pi2BPSK / Outer\_Full



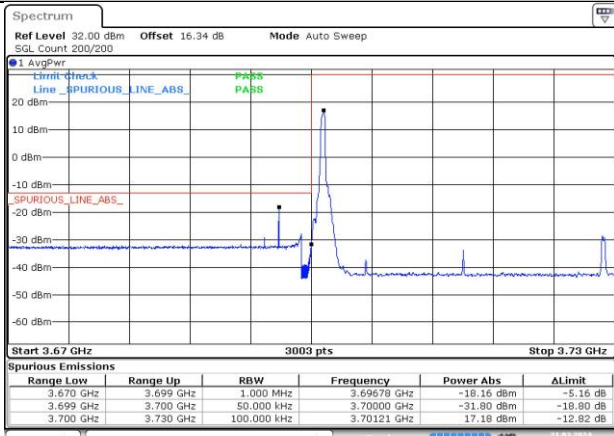
Date: 21.MAR.2024 11:31:00

HCH / DFT-Pi2BPSK / Outer\_Full



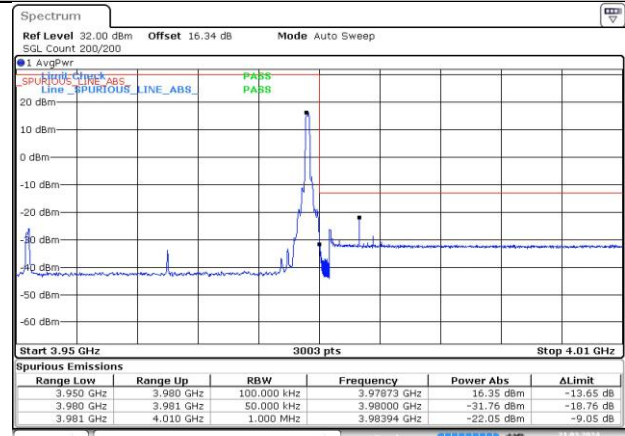
Date: 21.MAR.2024 11:32:40

LCH / DFT-QPSK / Edge\_1RB\_Left

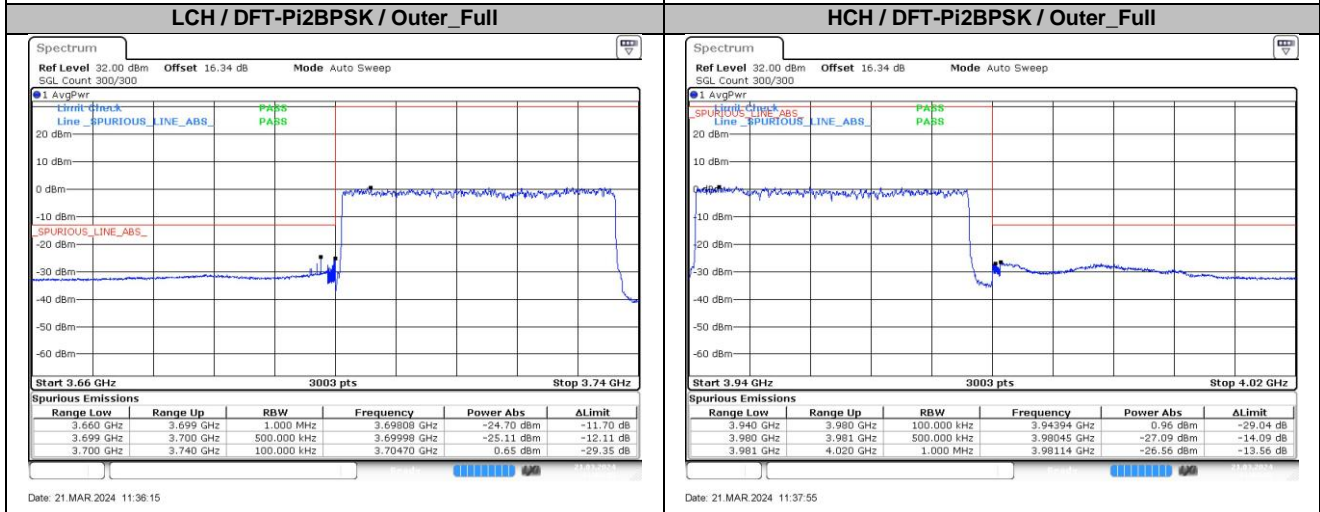
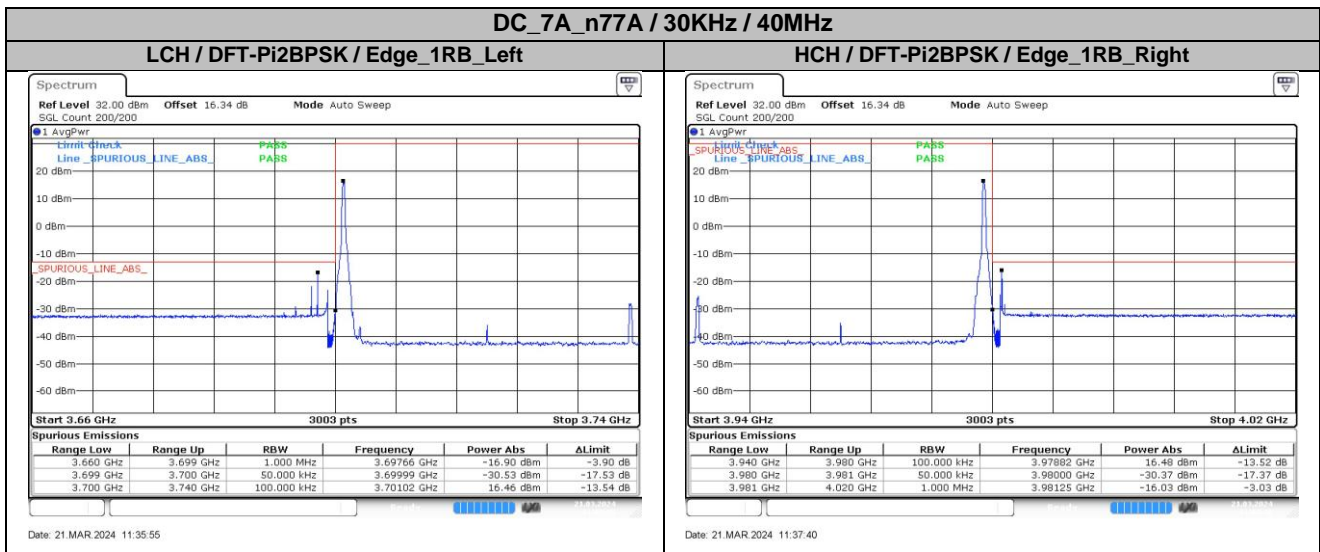
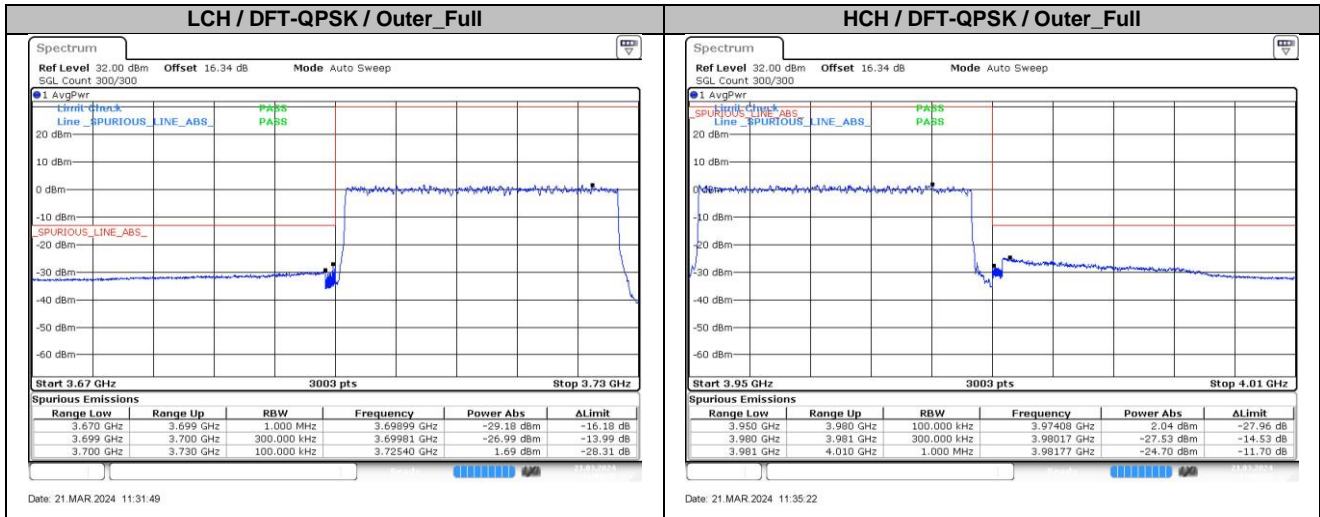


Date: 21.MAR.2024 11:31:30

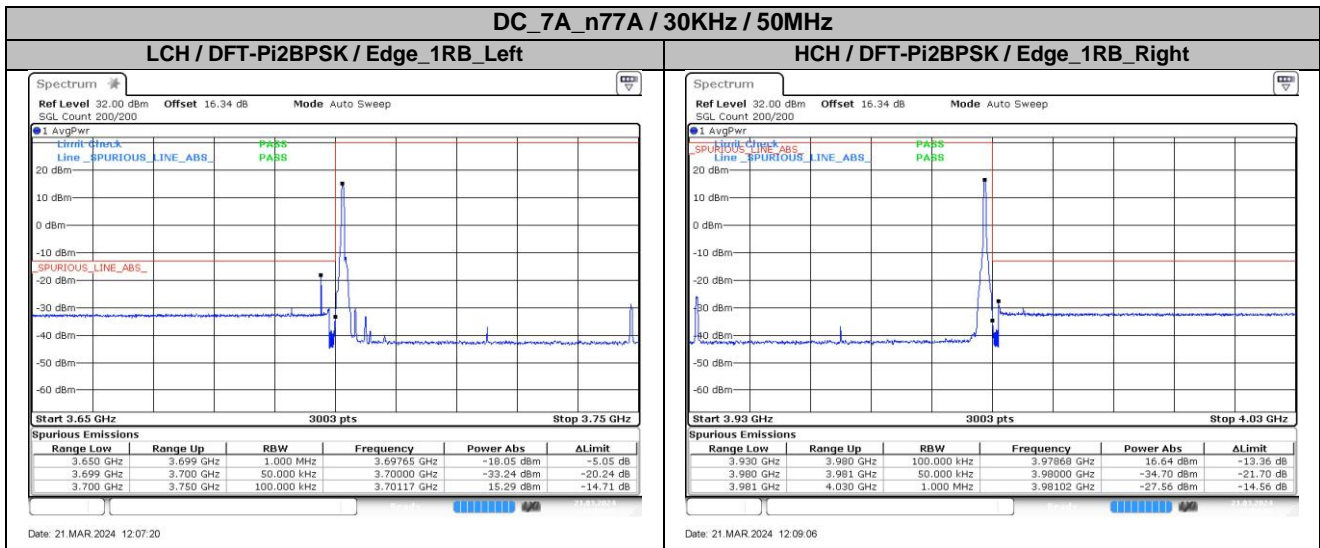
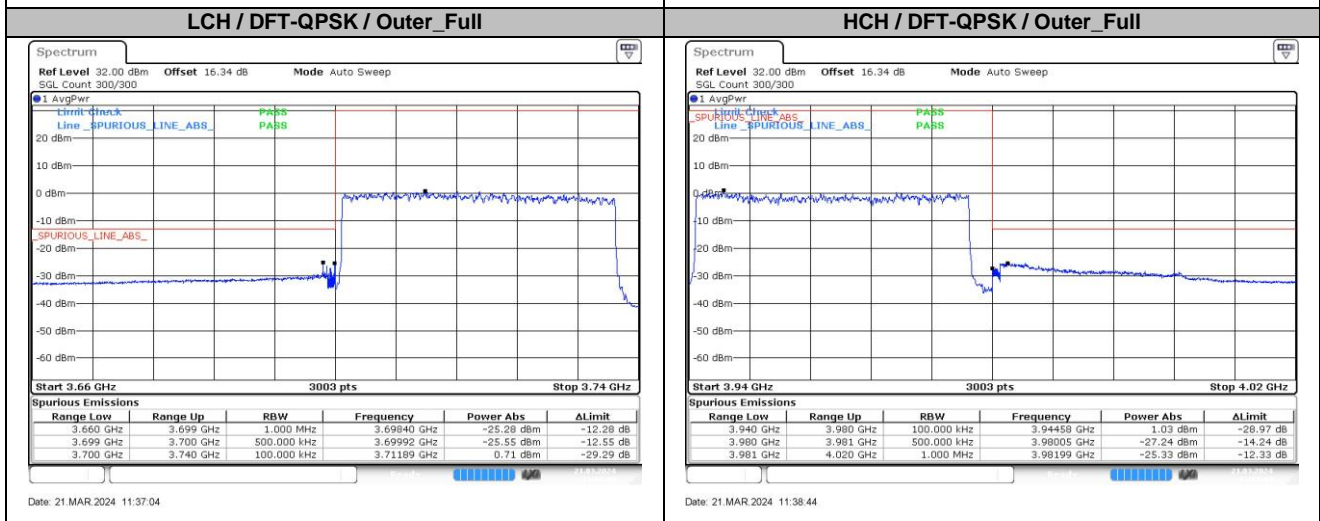
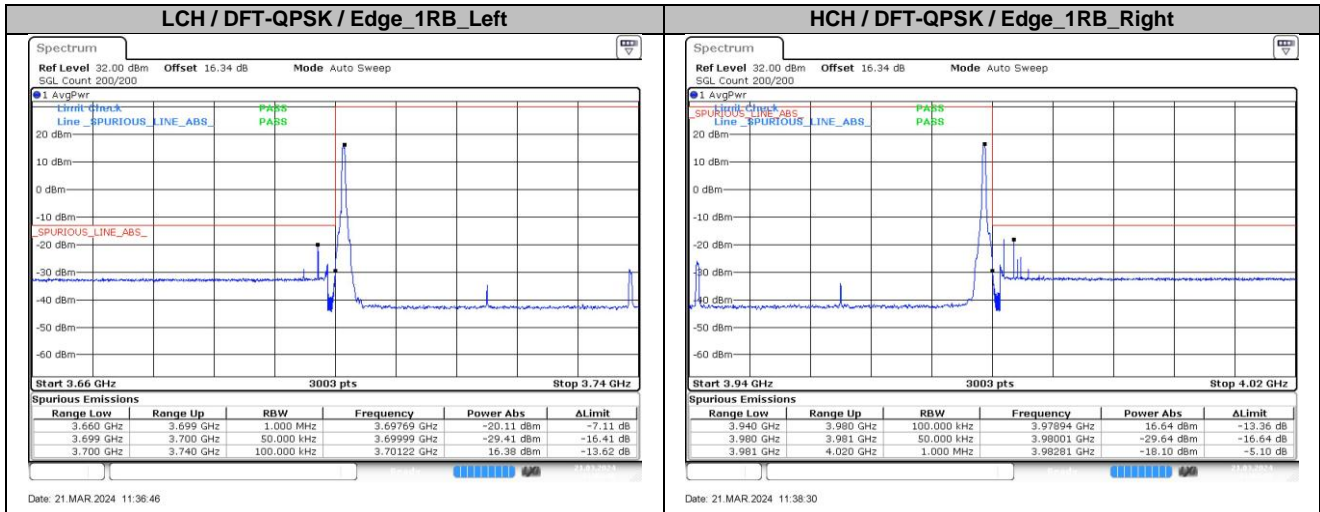
HCH / DFT-QPSK / Edge\_1RB\_Right

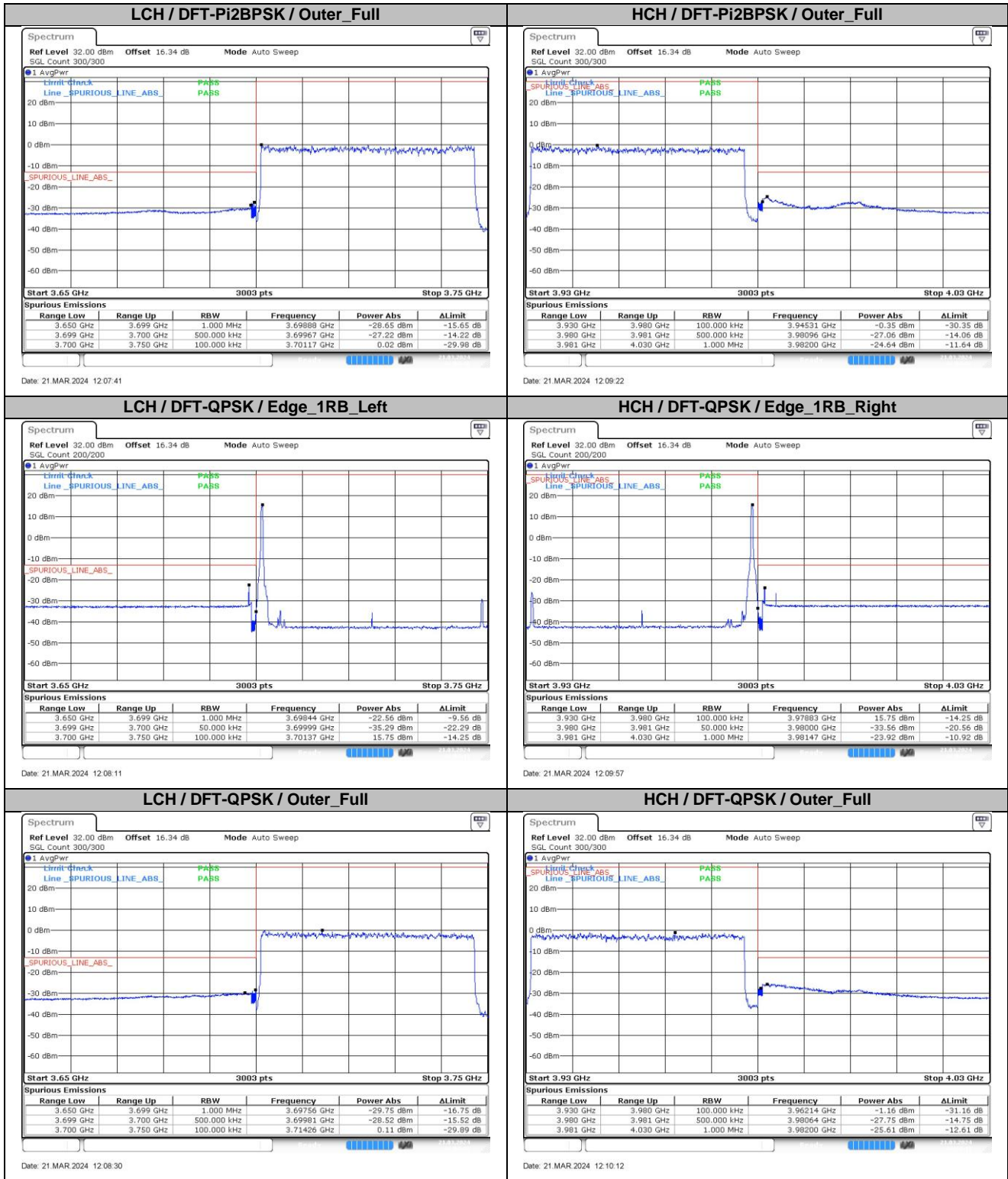


Date: 21.MAR.2024 11:35:08



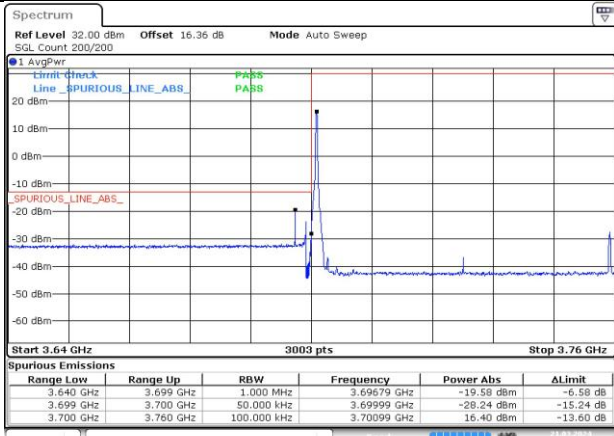






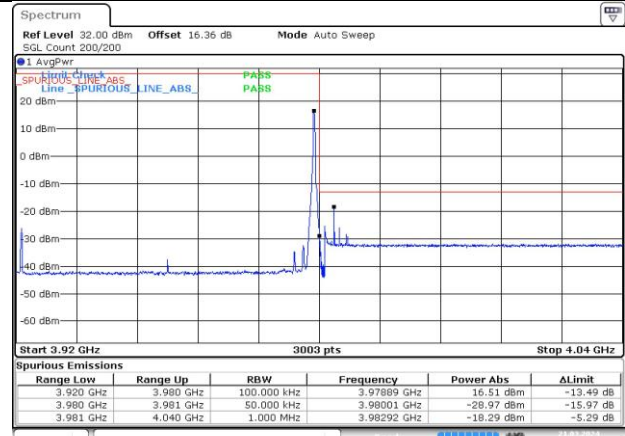
DC\_7A\_n77A / 30KHz / 60MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



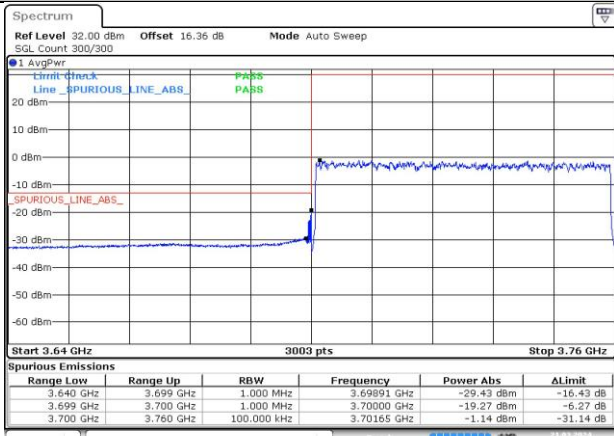
Date: 21.MAR.2024 12:10:43

HCH / DFT-Pi2BPSK / Edge\_1RB\_Right



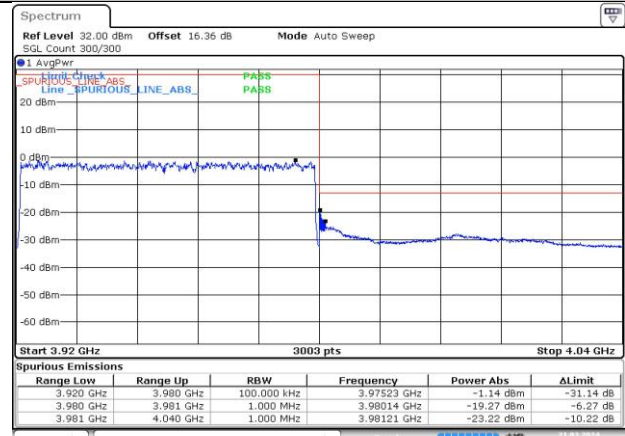
Date: 21.MAR.2024 12:12:22

LCH / DFT-Pi2BPSK / Outer\_Full



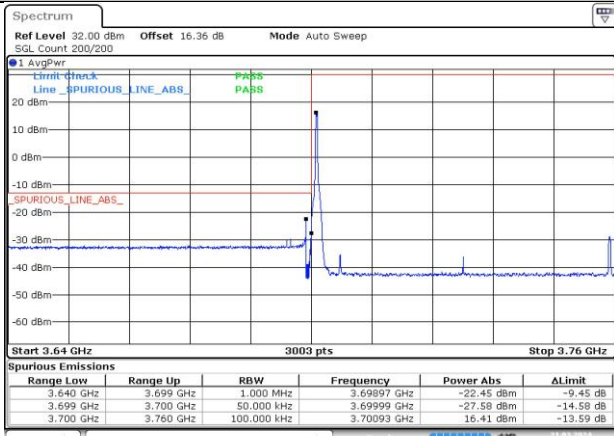
Date: 21.MAR.2024 12:11:02

HCH / DFT-Pi2BPSK / Outer\_Full



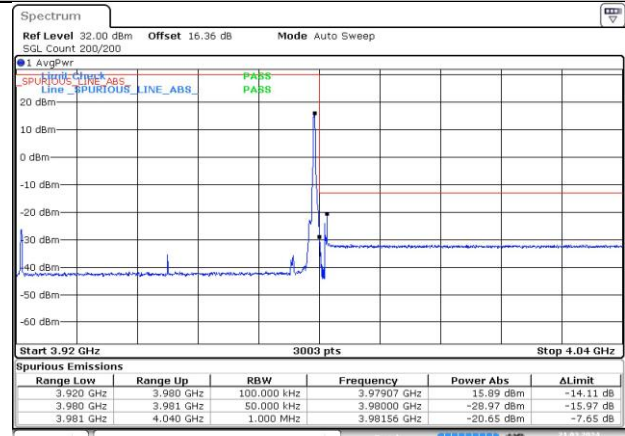
Date: 21.MAR.2024 12:12:35

LCH / DFT-QPSK / Edge\_1RB\_Left



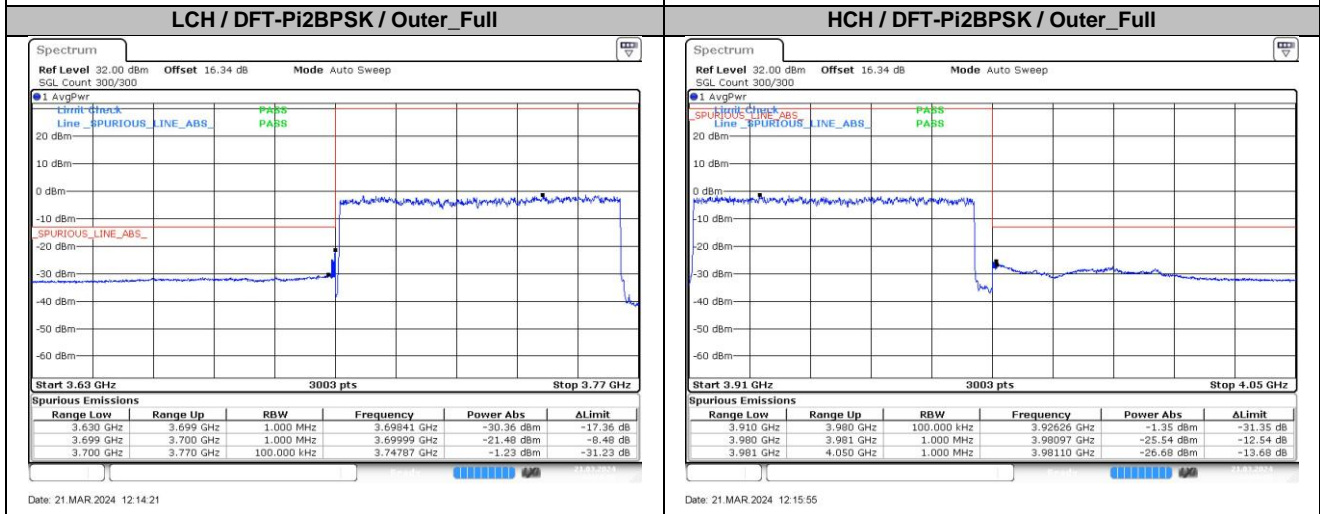
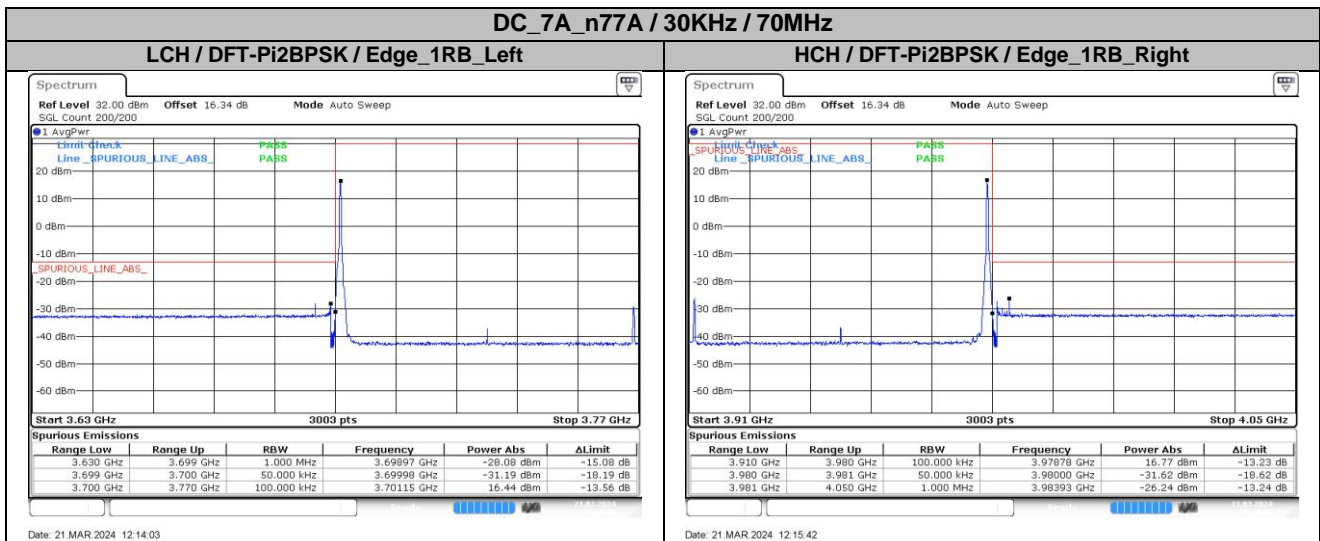
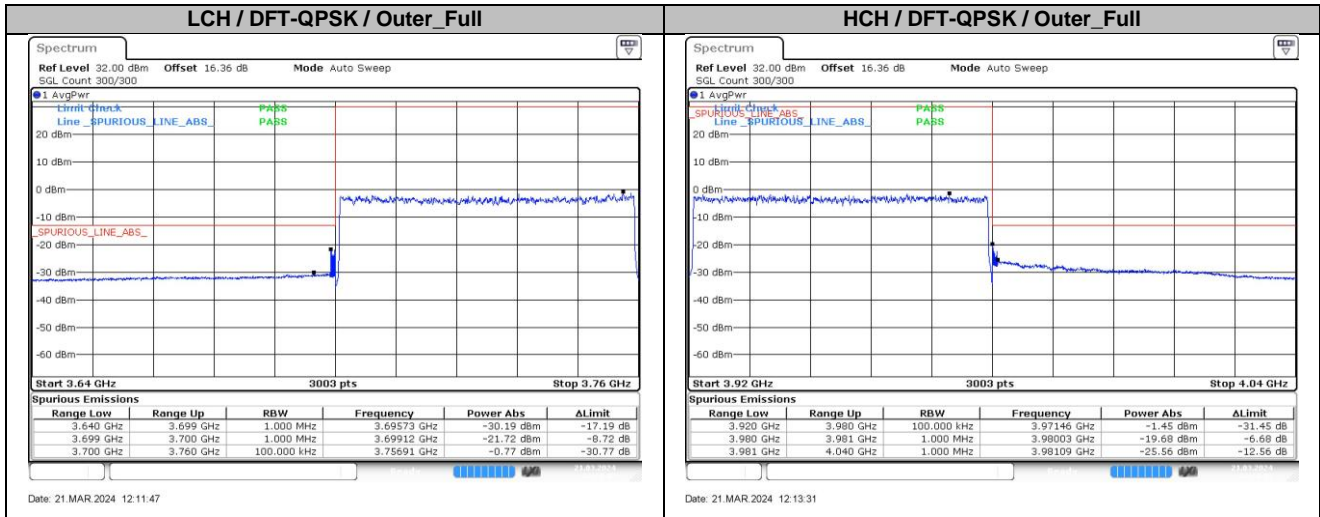
Date: 21.MAR.2024 12:11:31

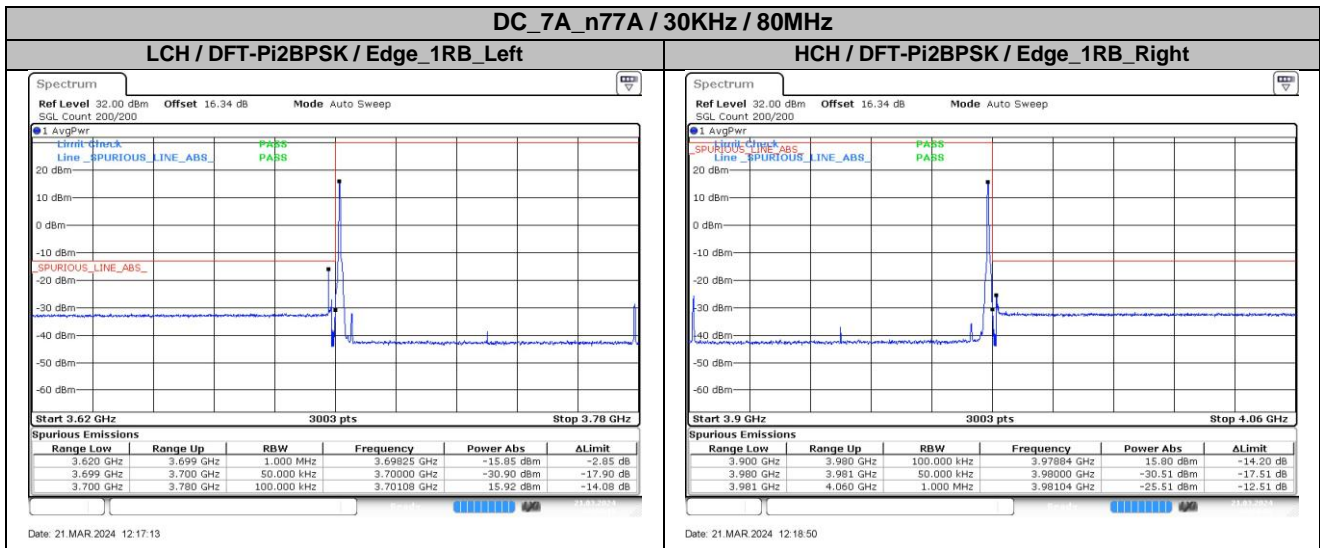
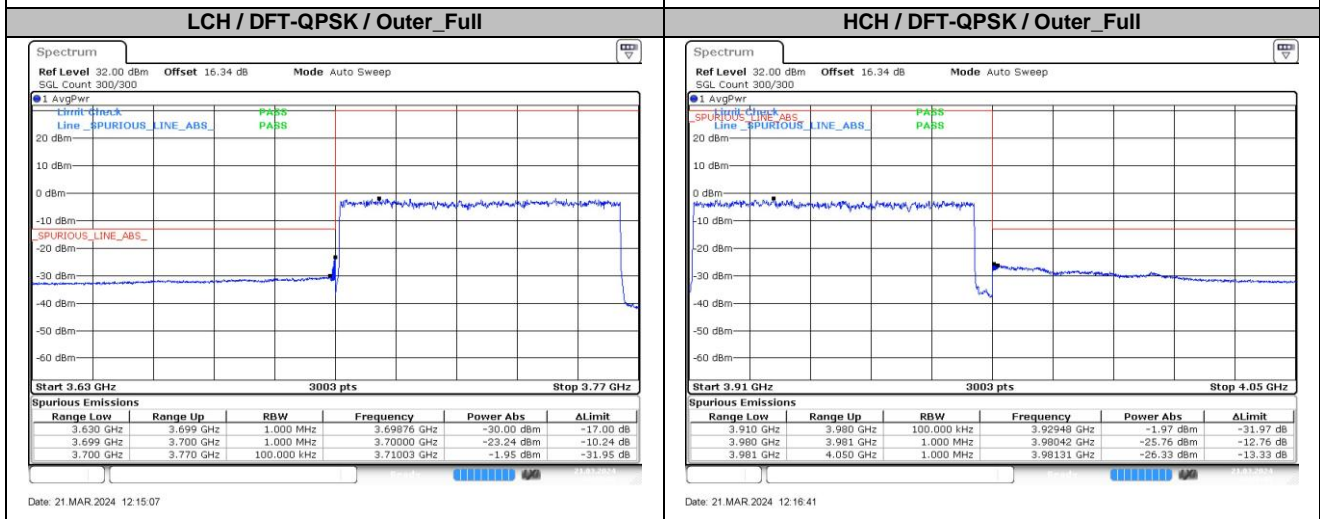
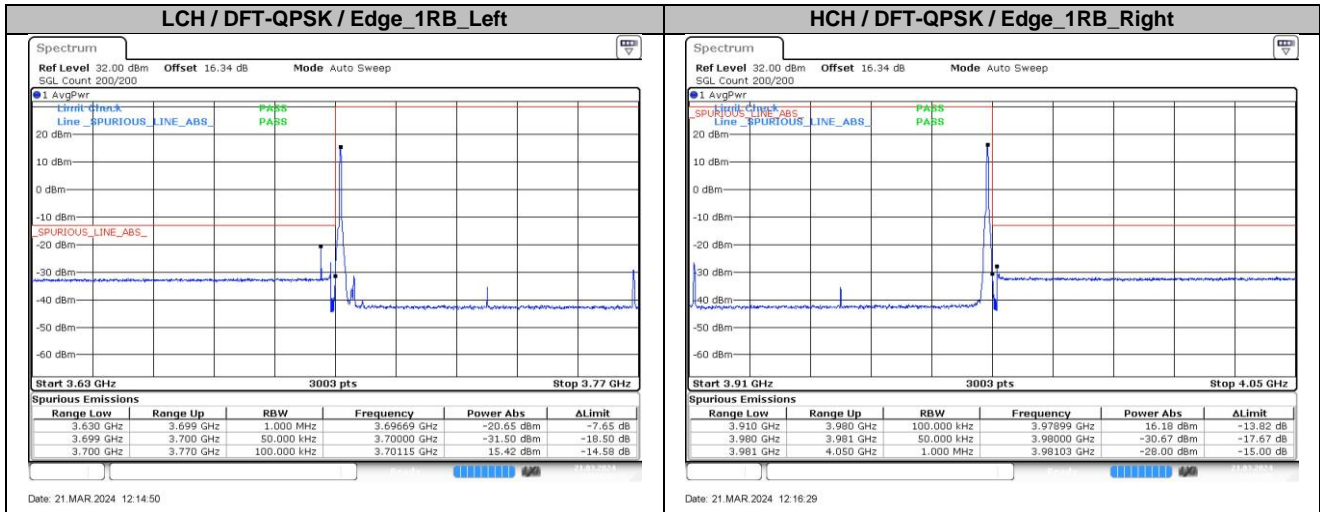
HCH / DFT-QPSK / Edge\_1RB\_Right

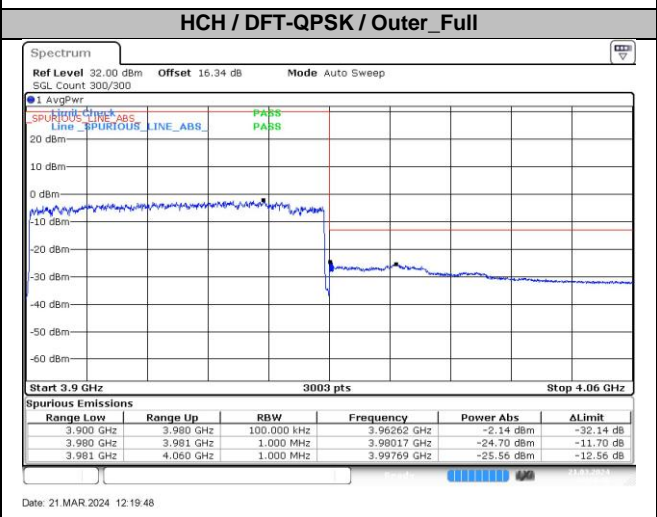
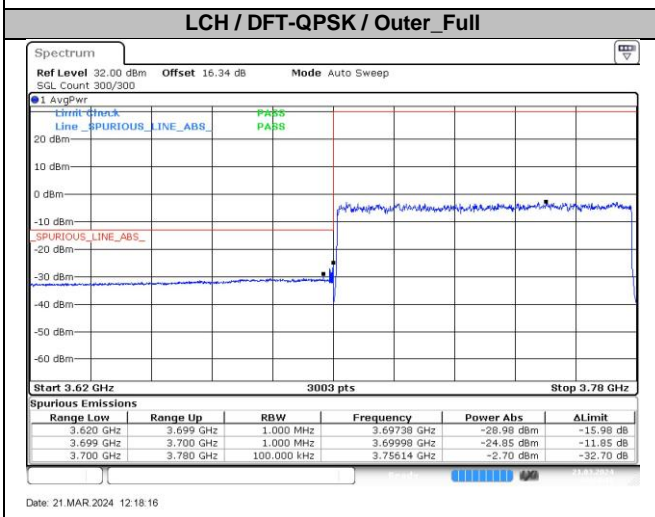
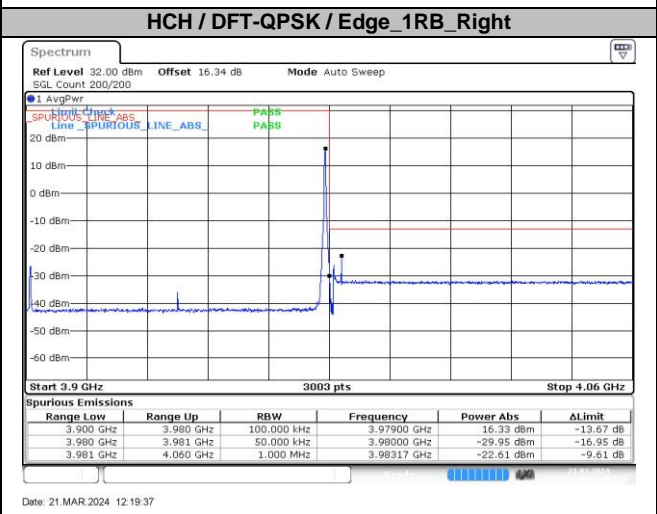
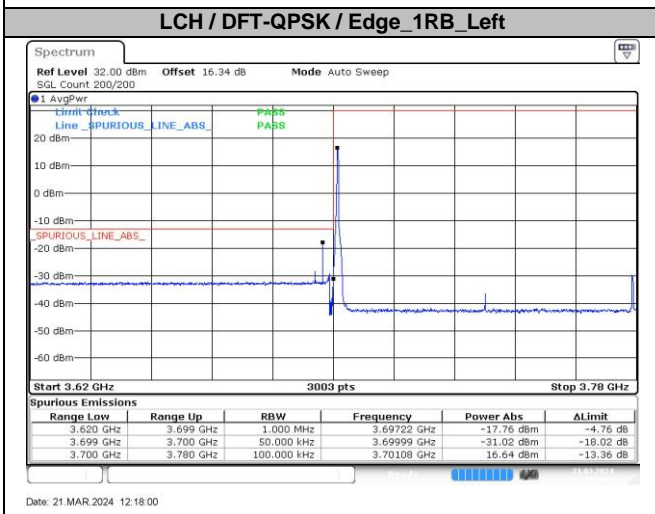
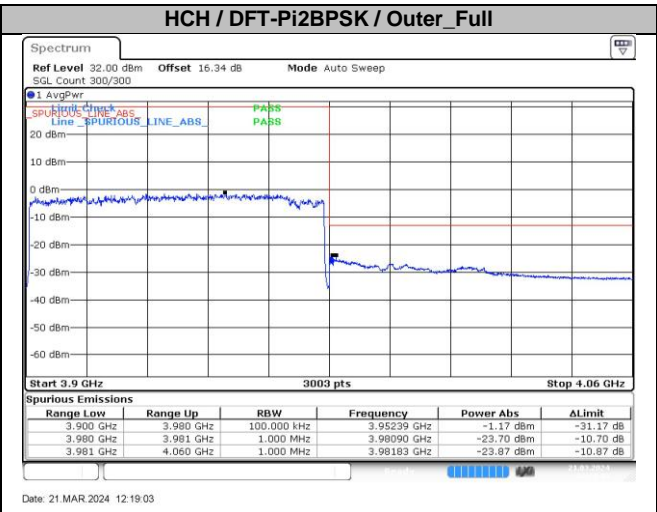
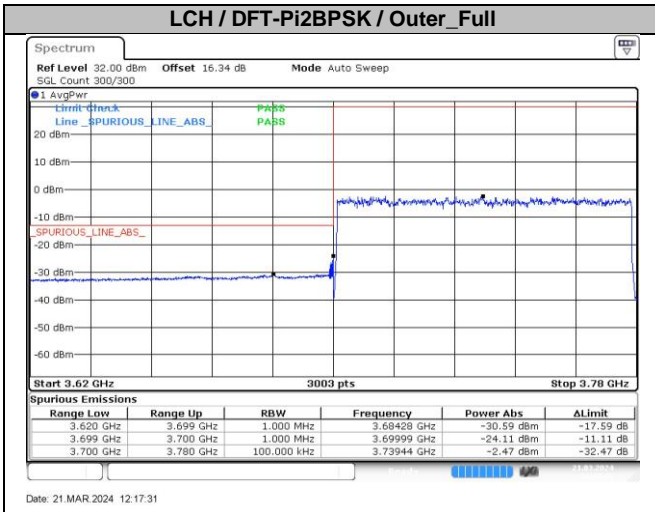


Date: 21.MAR.2024 12:13:19



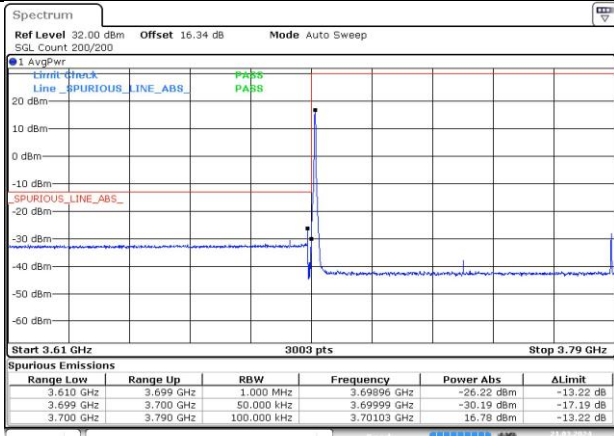






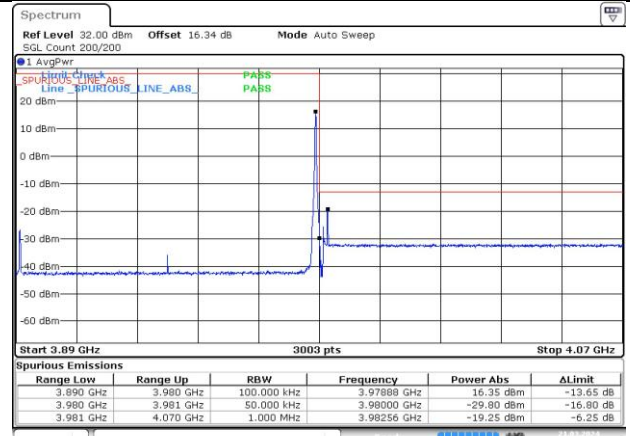
DC\_7A\_n77A / 30KHz / 90MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



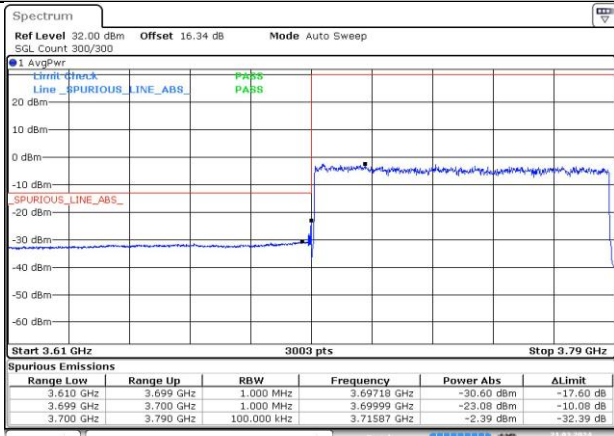
Date: 21.MAR.2024 12.20.19

HCH / DFT-Pi2BPSK / Edge\_1RB\_Right



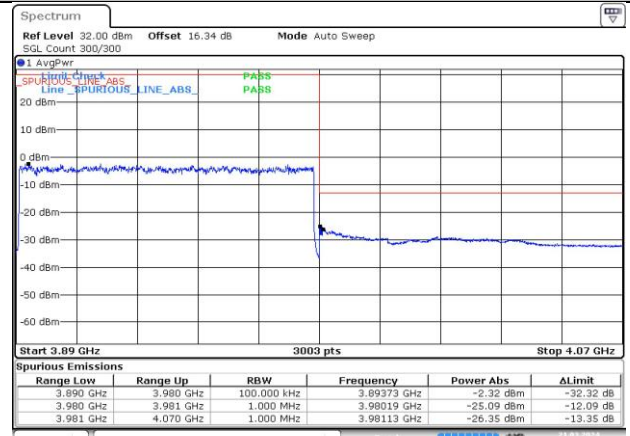
Date: 21.MAR.2024 12.21.54

LCH / DFT-Pi2BPSK / Outer\_Full



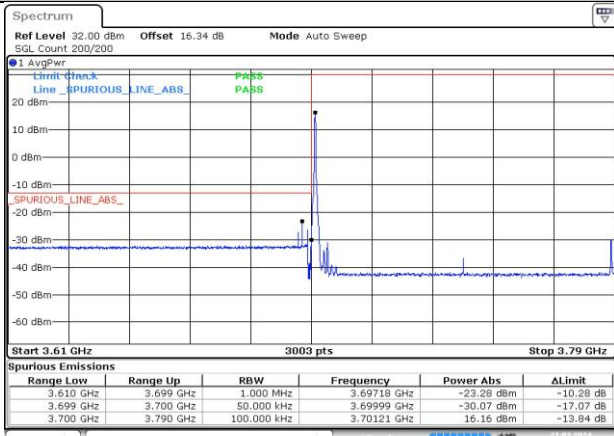
Date: 21.MAR.2024 12.20.35

HCH / DFT-Pi2BPSK / Outer\_Full



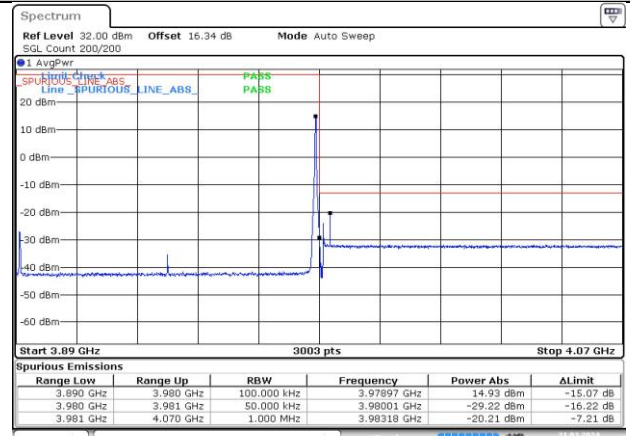
Date: 21.MAR.2024 12.22.07

LCH / DFT-QPSK / Edge\_1RB\_Left

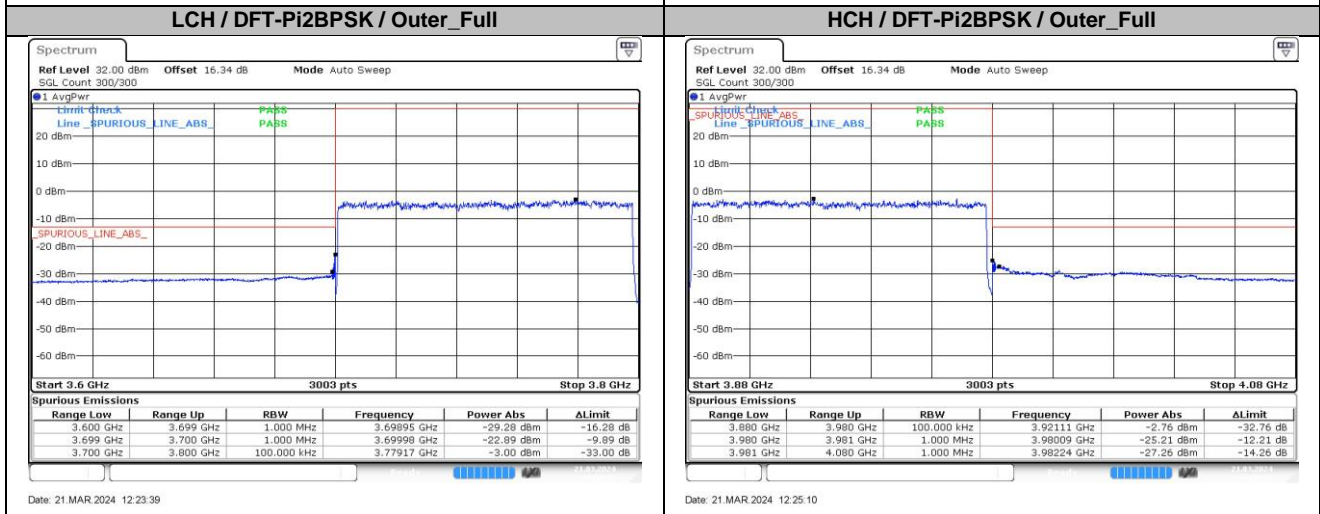
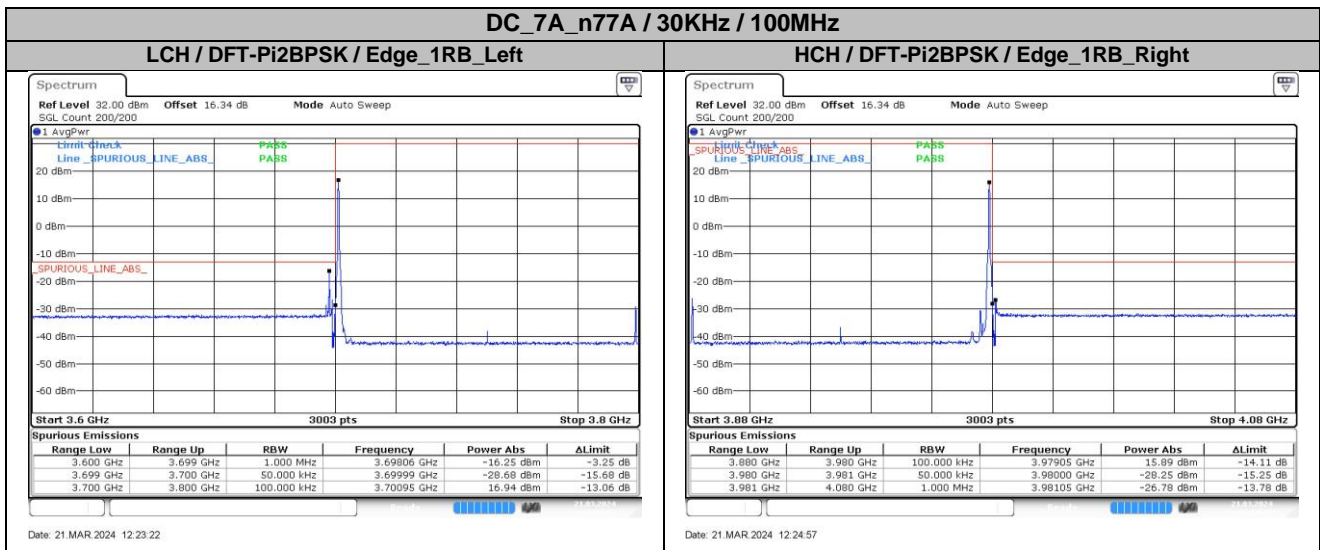
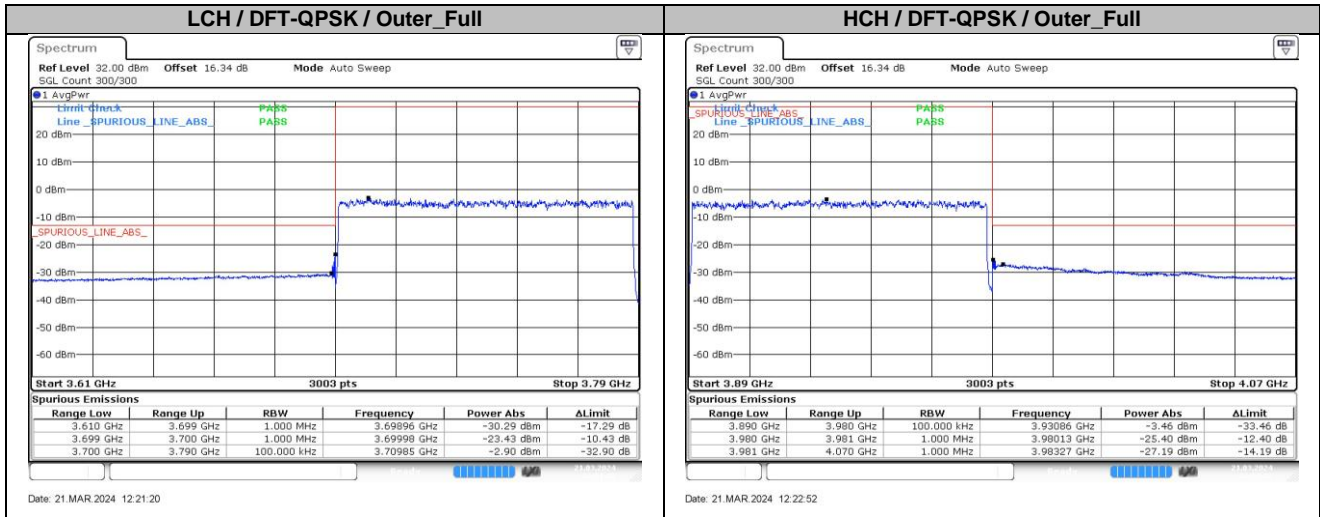


Date: 21.MAR.2024 12.21.05

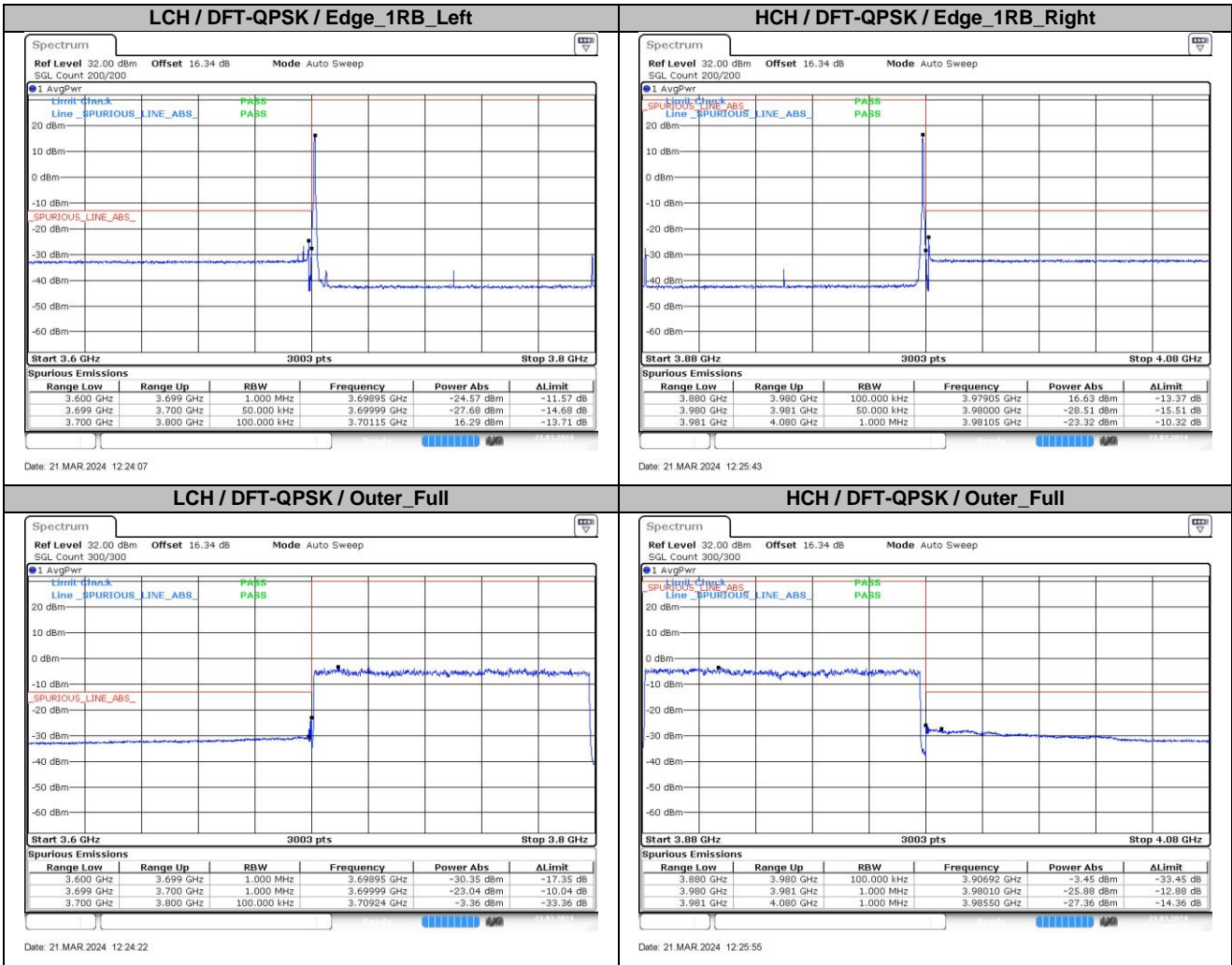
HCH / DFT-QPSK / Edge\_1RB\_Right



Date: 21.MAR.2024 12.22.40

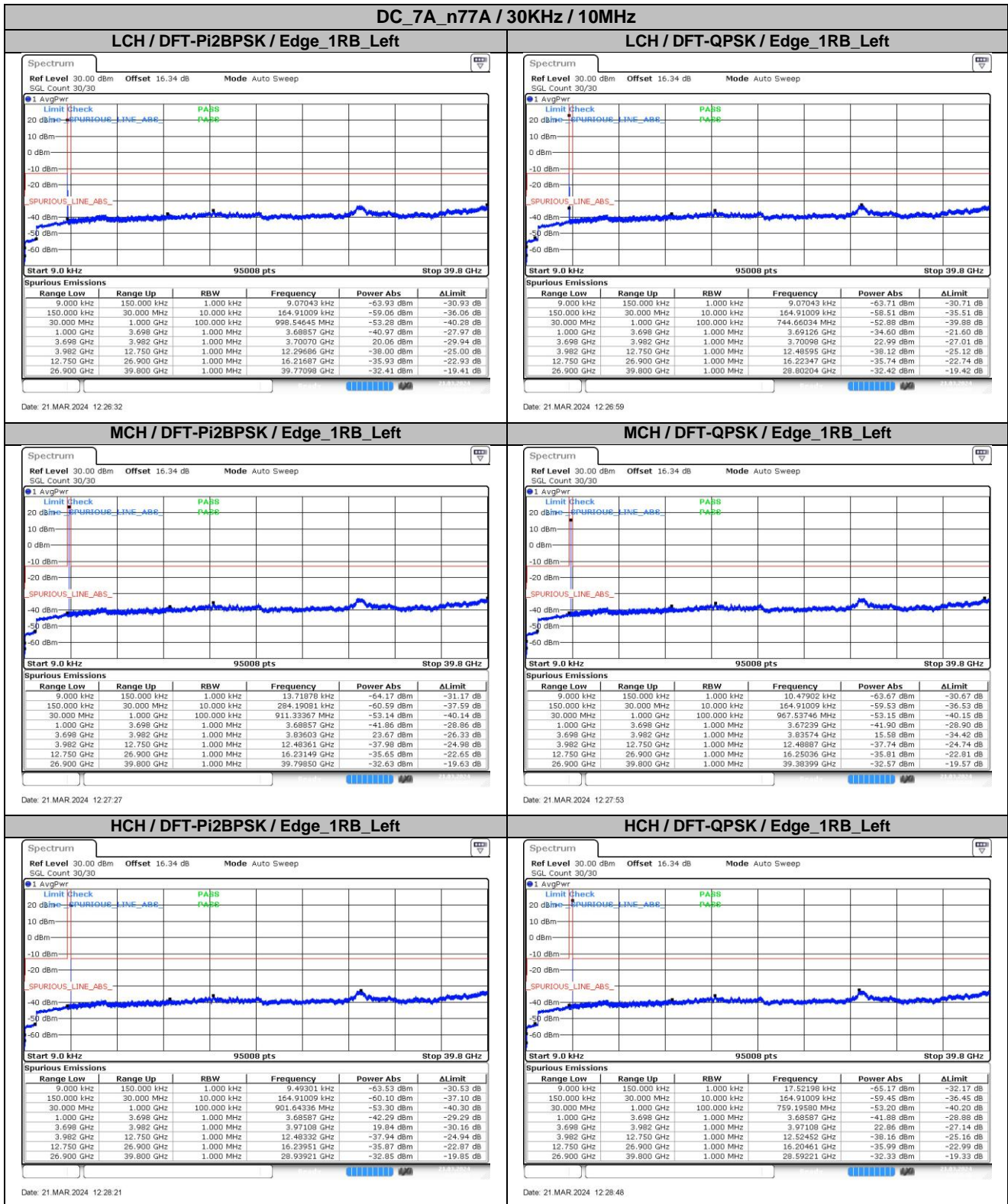






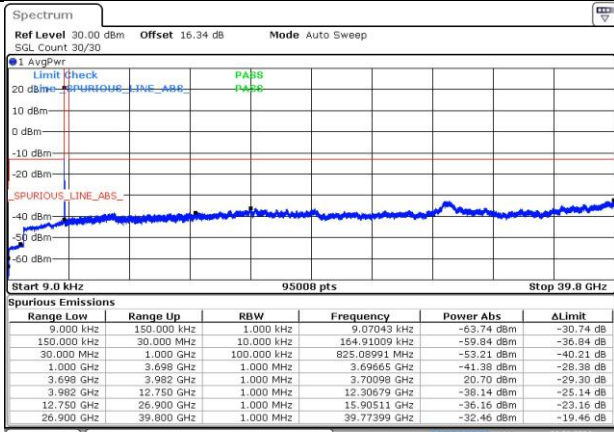
### 6. Conducted Spurious Emission

#### 6.1. Test Plots



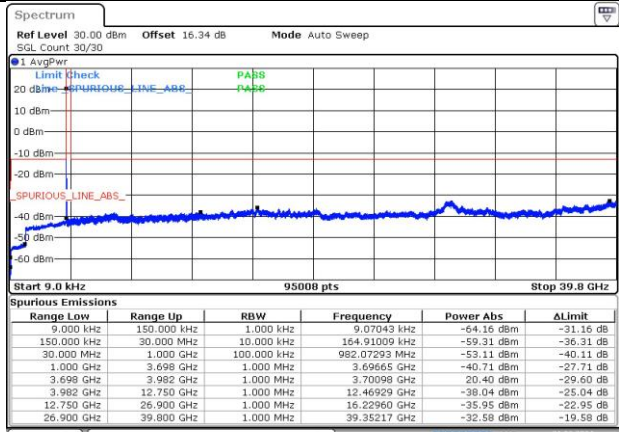
DC\_7A\_n77A / 30KHz / 15MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



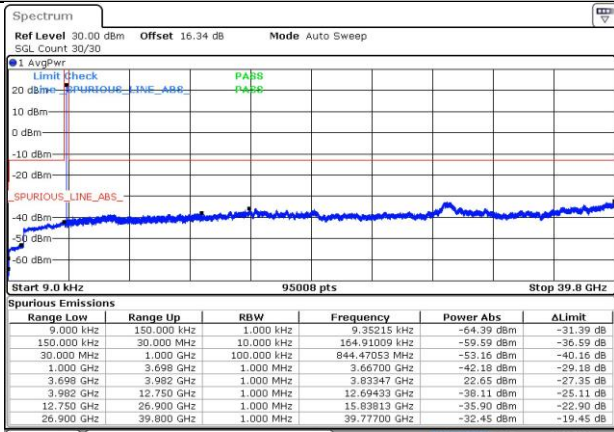
Date: 21.MAR.2024 12.29.17

LCH / DFT-QPSK / Edge\_1RB\_Left



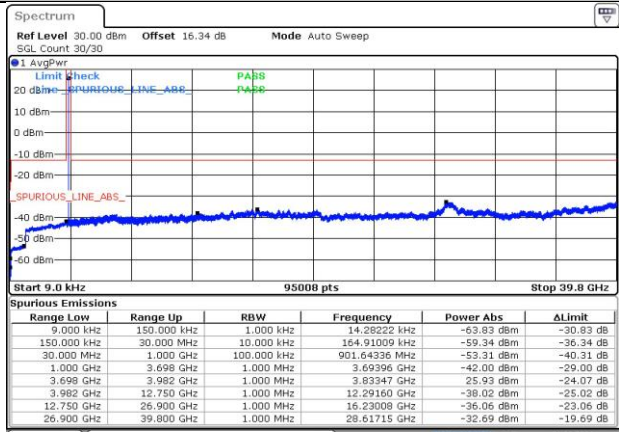
Date: 21.MAR.2024 12.29.43

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



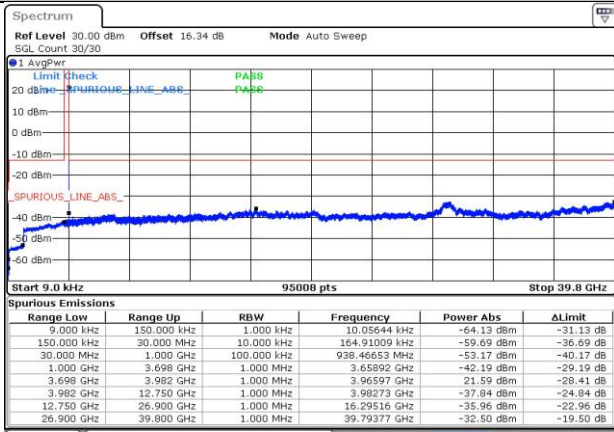
Date: 21.MAR.2024 12.30.10

MCH / DFT-QPSK / Edge\_1RB\_Left



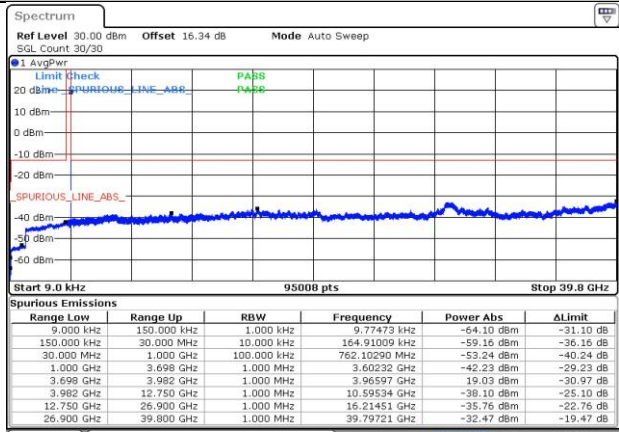
Date: 21.MAR.2024 12.30.37

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 21.MAR.2024 12.31.04

HCH / DFT-QPSK / Edge\_1RB\_Left



Date: 21.MAR.2024 12.31.30