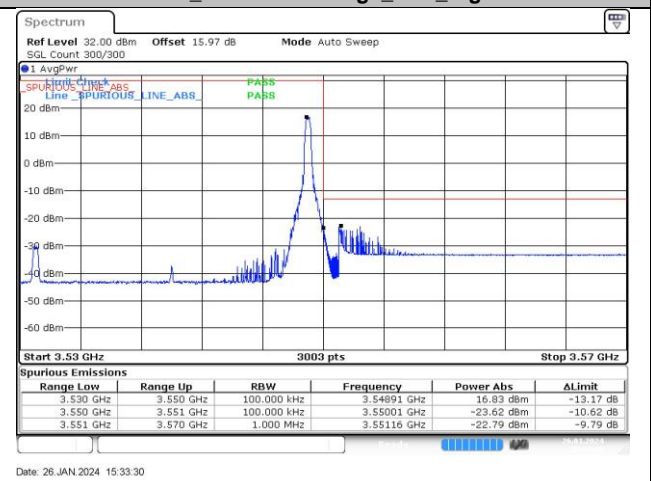
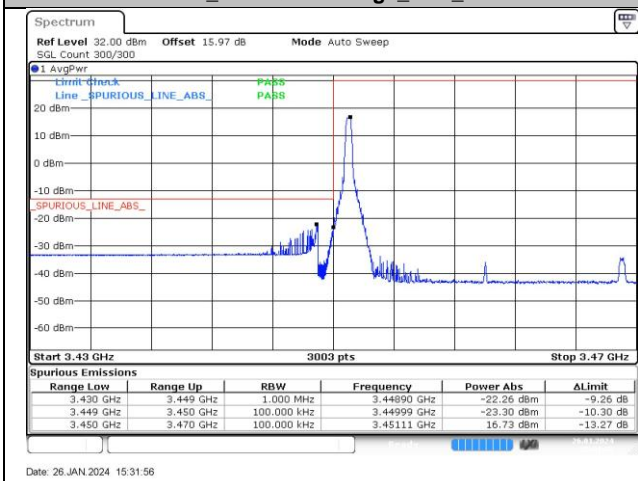


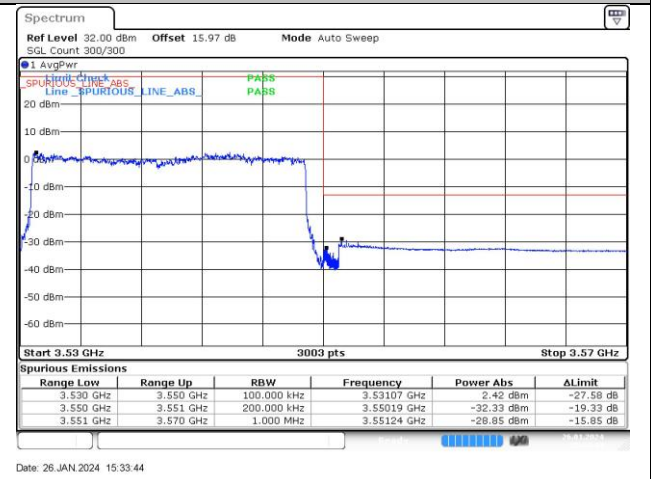
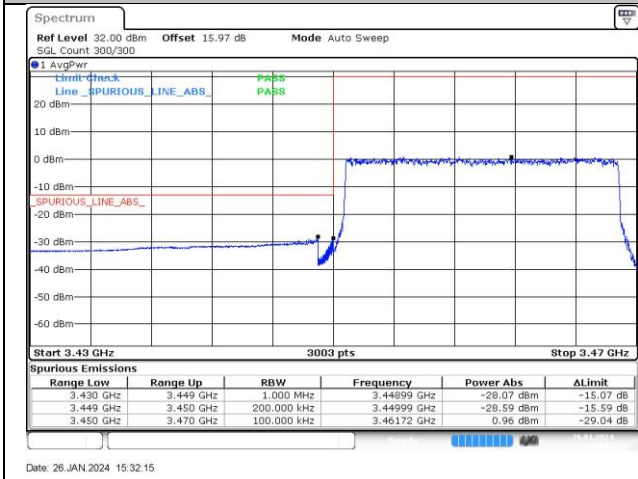
LCH\_DFT-QPSK / Edge\_1RB\_Left

HCH\_DFT-QPSK / Edge\_1RB\_Right



LCH\_DFT-QPSK / Outer\_Full

HCH\_DFT-QPSK / Outer\_Full



n78A / 30KHz / 30MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 26.JAN.2024 15:34:13

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Right



Date: 26.JAN.2024 15:37:31

LCH\_DFT-Pi2BPSK / Outer\_Full



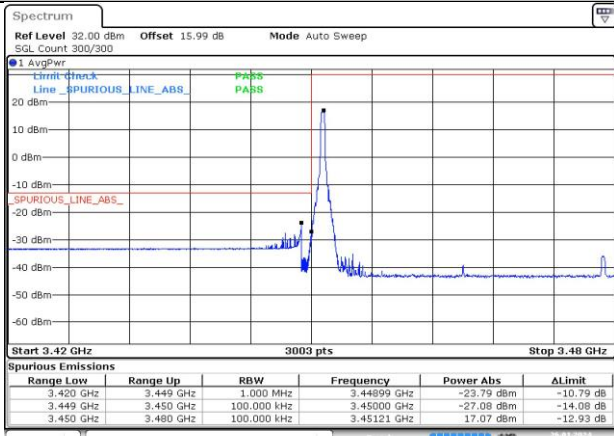
Date: 26.JAN.2024 15:34:34

HCH\_DFT-Pi2BPSK / Outer\_Full



Date: 26.JAN.2024 15:36:59

LCH\_DFT-QPSK / Edge\_1RB\_Left



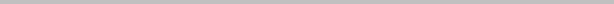
Date: 26.JAN.2024 15:35:45

HCH\_DFT-QPSK / Edge\_1RB\_Right

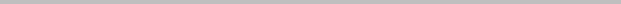


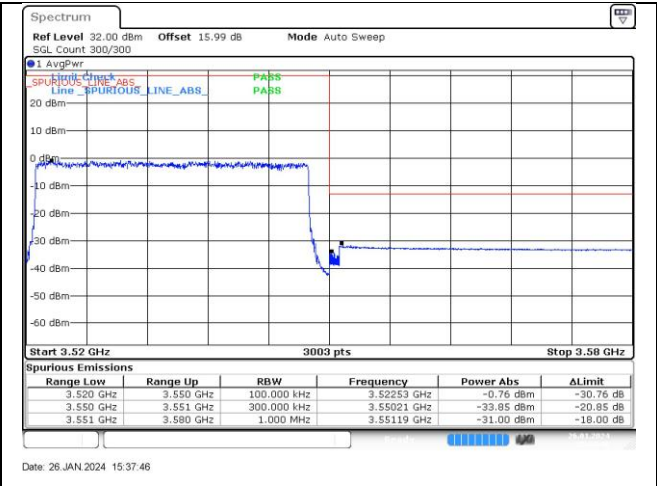
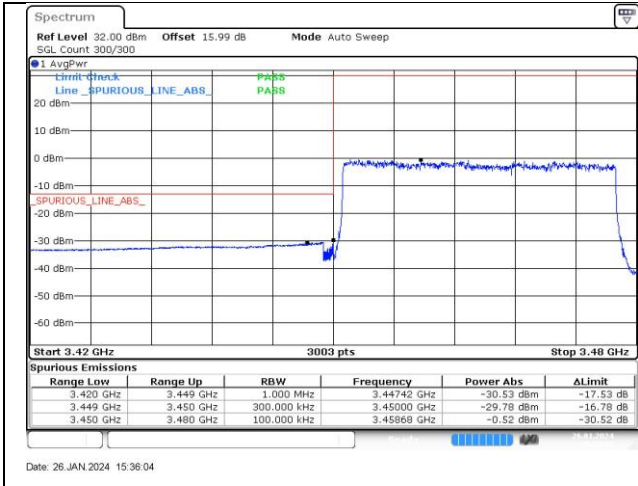
Date: 26.JAN.2024 15:37:31

LCH\_DFT-QPSK / Outer\_Full



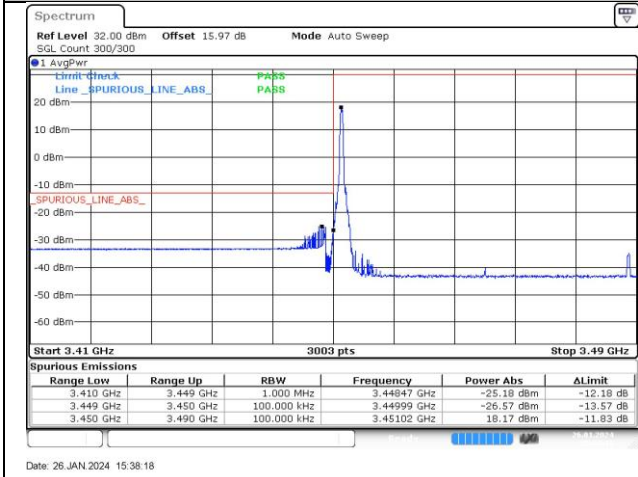
HCH\_DFT-QPSK / Outer\_Full



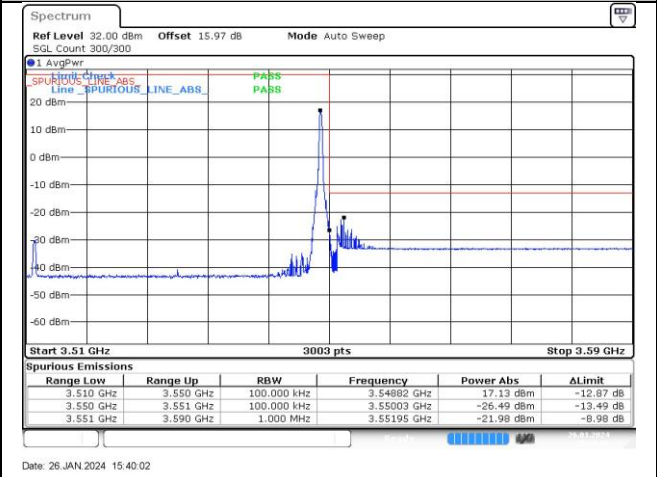


n78A / 30KHz / 40MHz

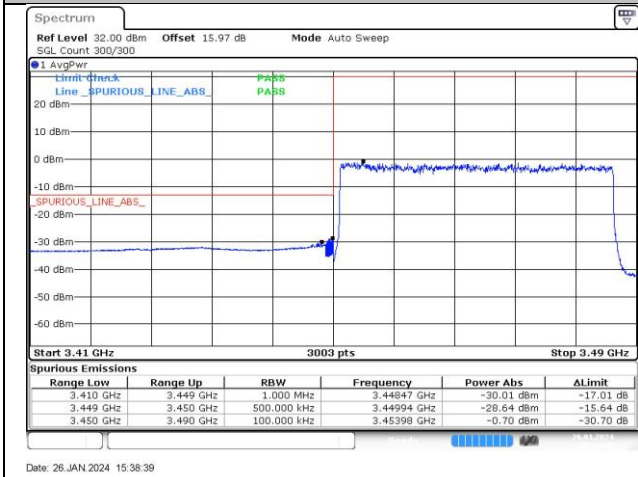
LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



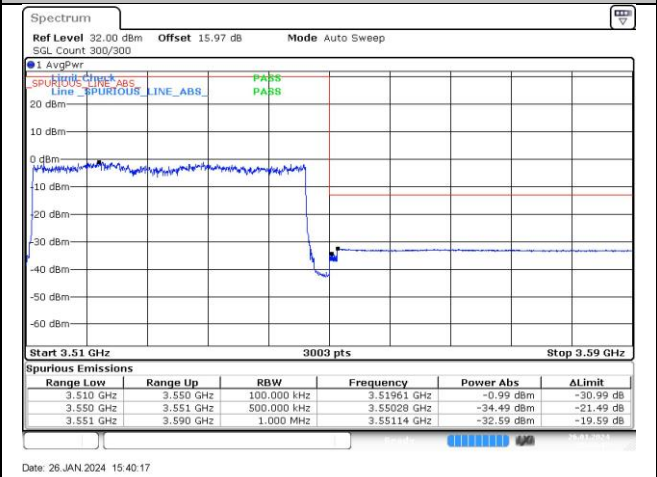
HCH\_DFT-Pi2BPSK / Edge\_1RB\_Right



LCH\_DFT-Pi2BPSK / Outer\_Full

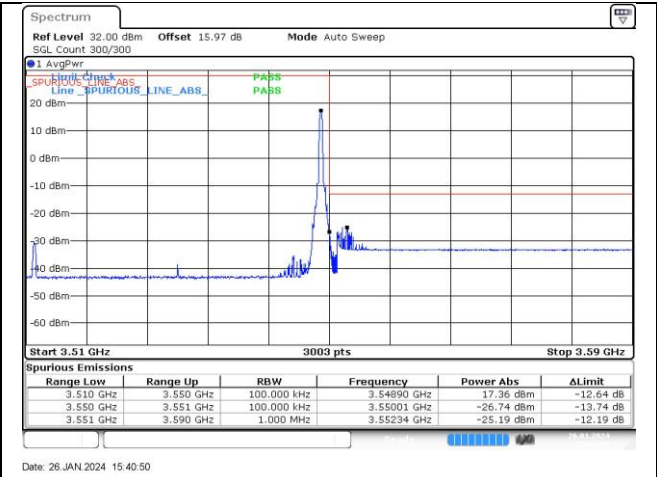
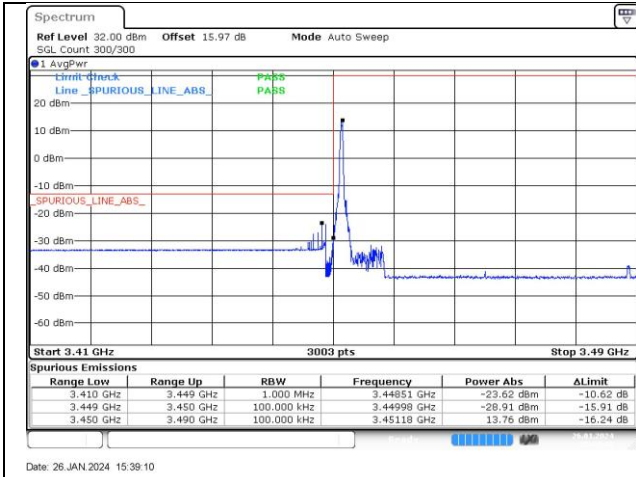


HCH\_DFT-Pi2BPSK / Outer\_Full



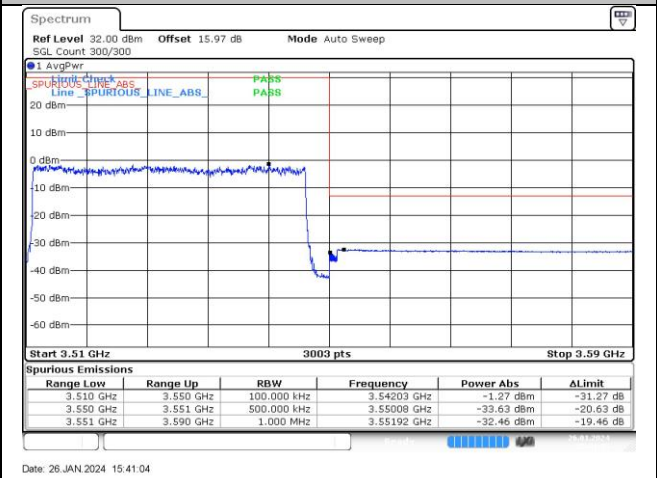
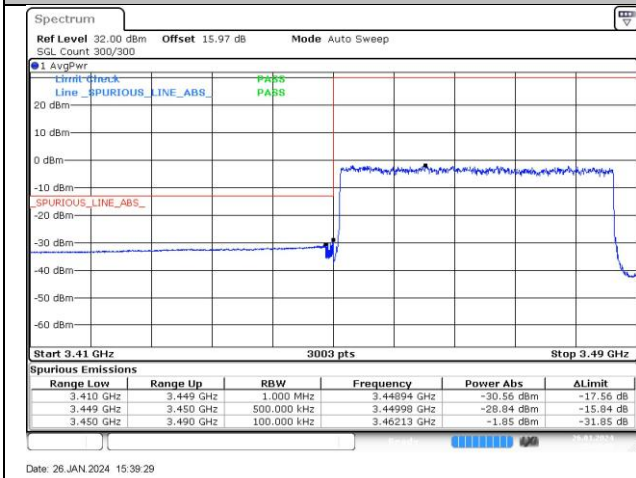
LCH\_DFT-QPSK / Edge\_1RB\_Left

HCH\_DFT-QPSK / Edge\_1RB\_Right

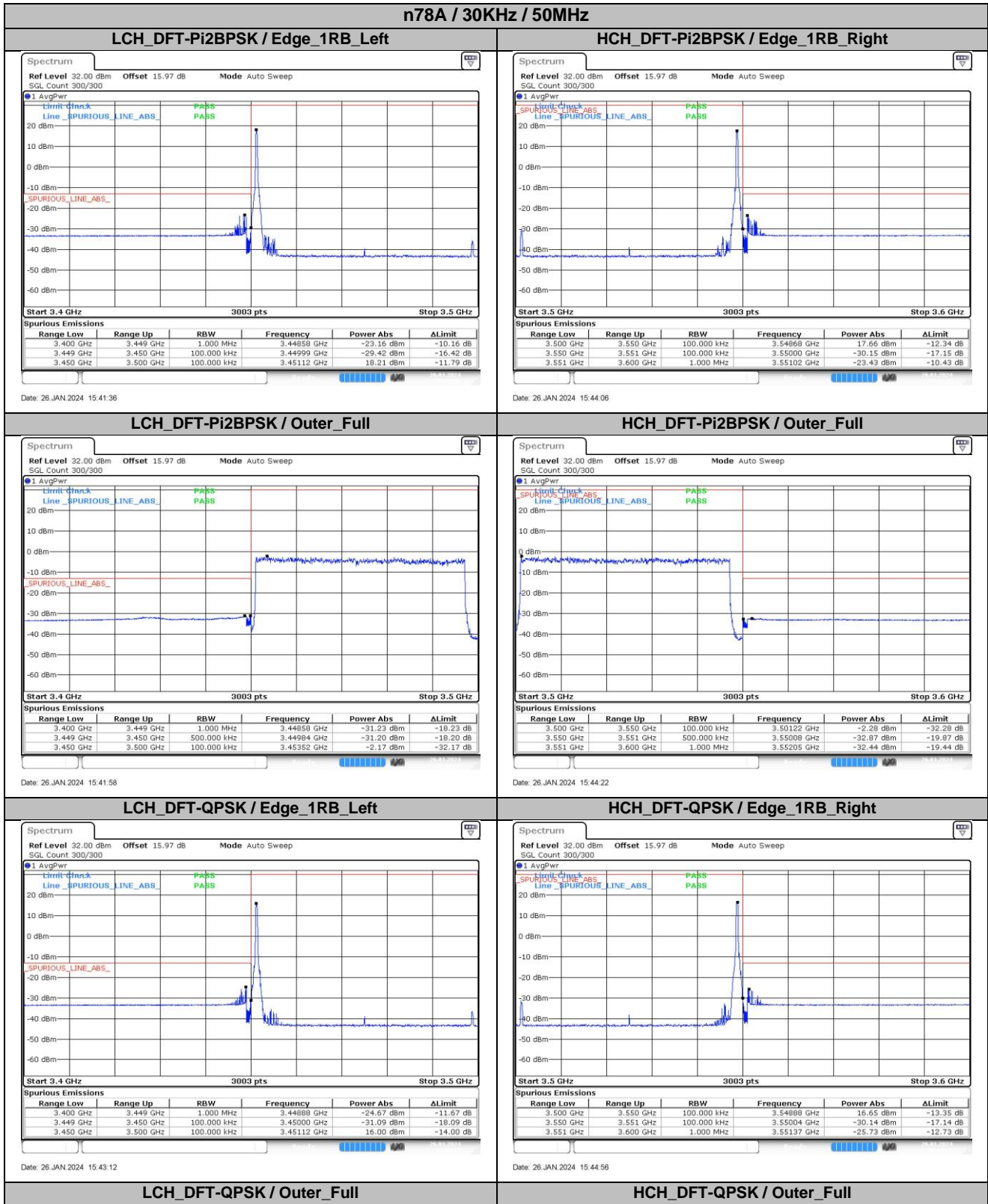


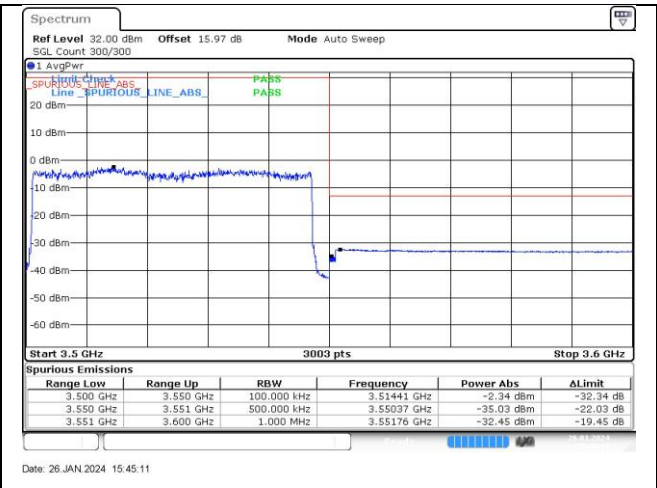
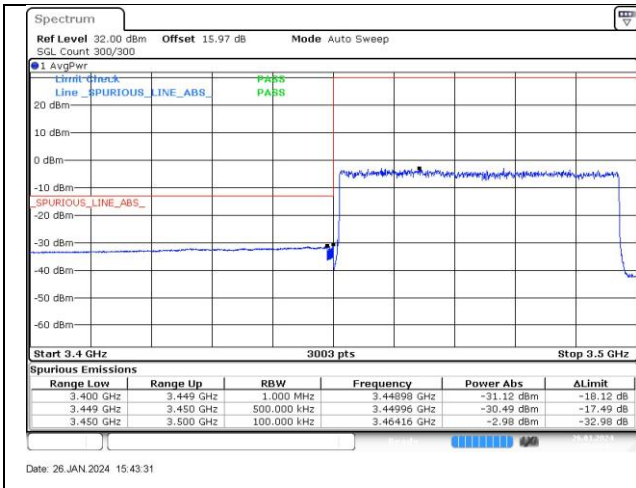
LCH\_DFT-QPSK / Outer\_Full

HCH\_DFT-QPSK / Outer\_Full



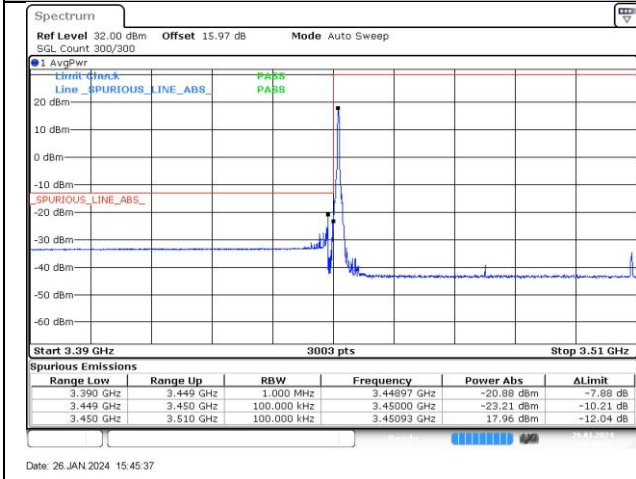




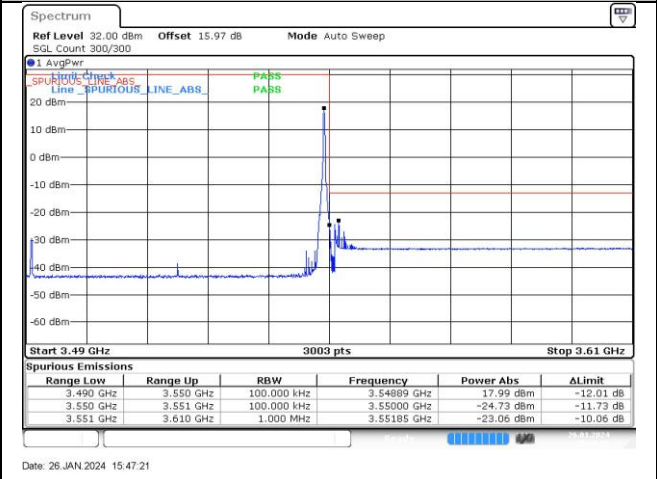


n78A / 30KHz / 60MHz

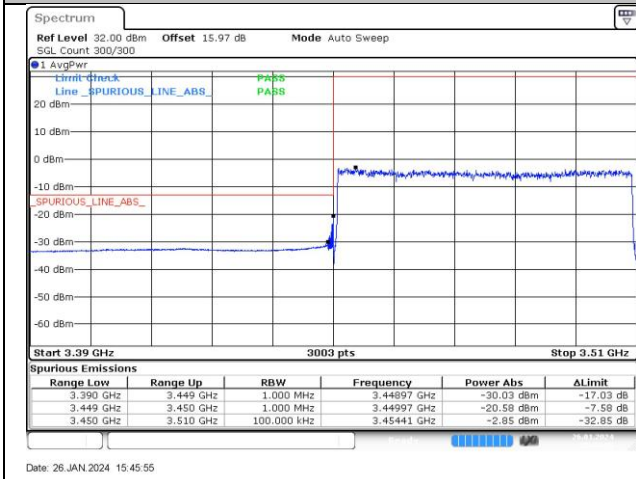
LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



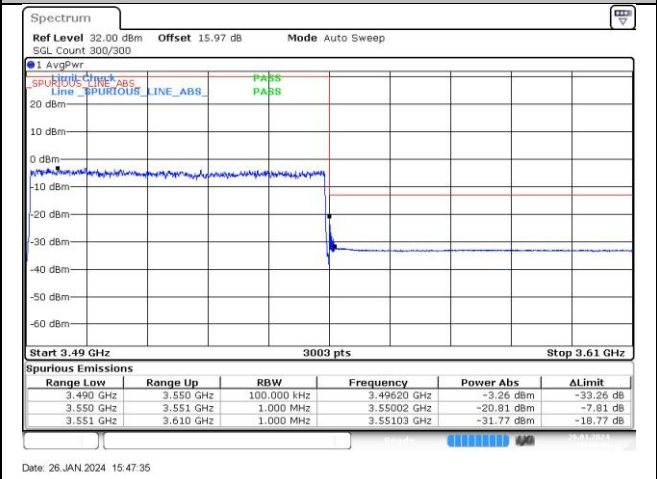
HCH\_DFT-Pi2BPSK / Edge\_1RB\_Right



LCH\_DFT-Pi2BPSK / Outer\_Full

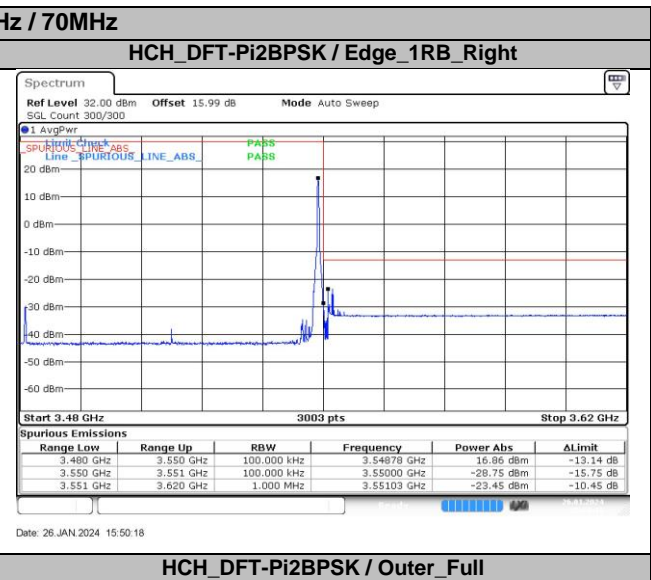
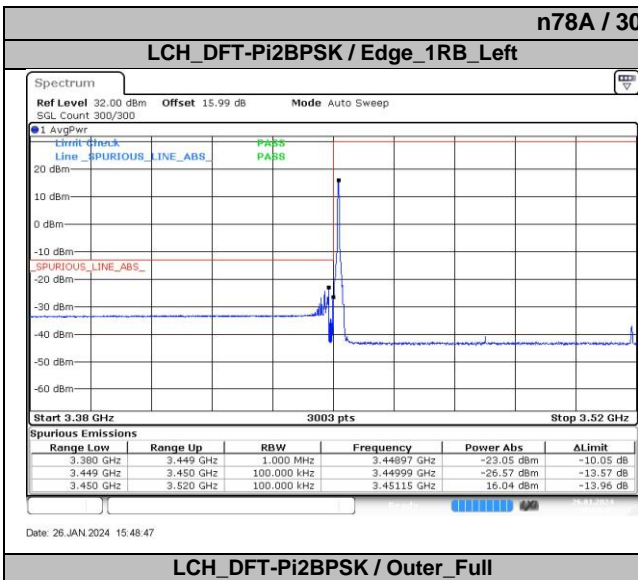
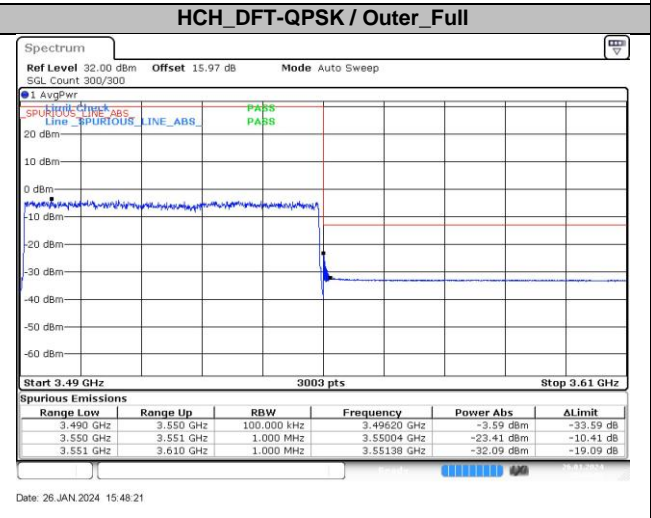
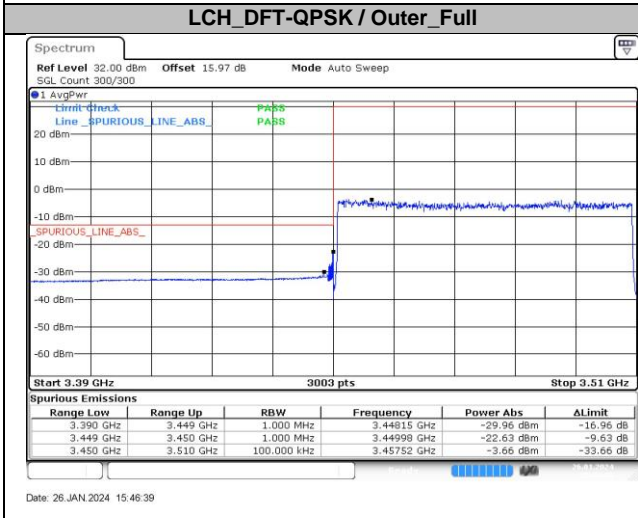
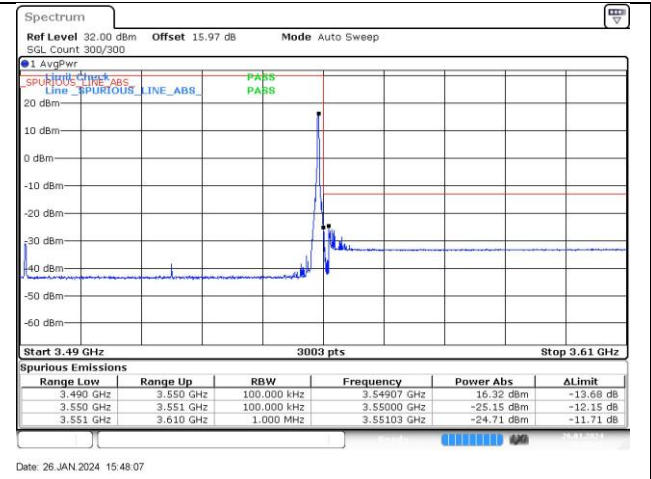
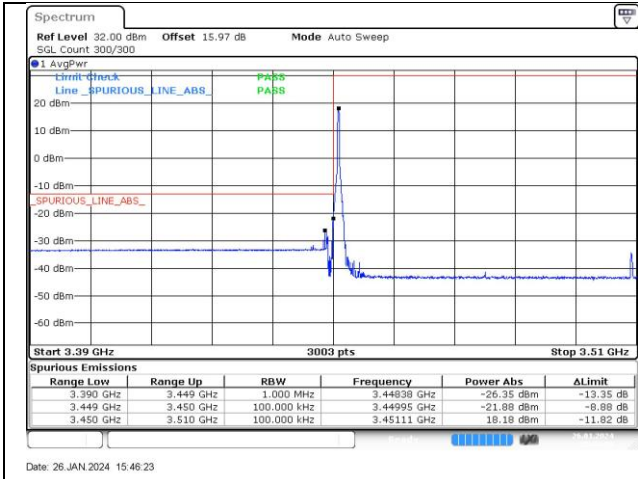


HCH\_DFT-Pi2BPSK / Outer\_Full

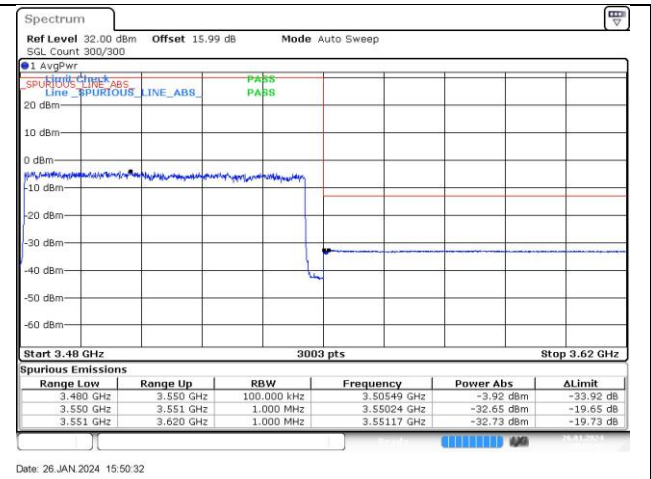
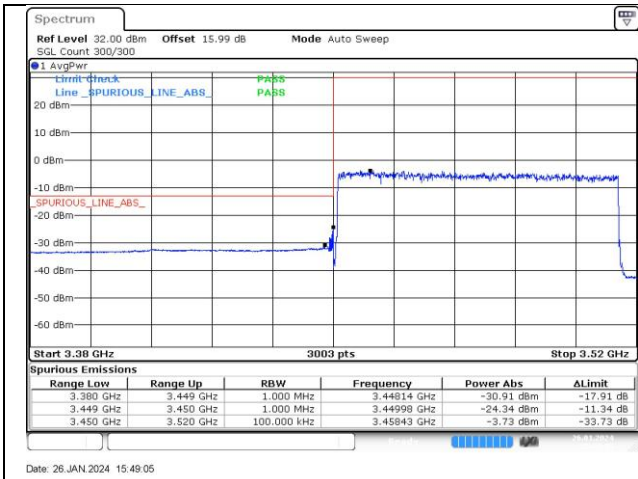


LCH\_DFT-QPSK / Edge\_1RB\_Left

HCH\_DFT-QPSK / Edge\_1RB\_Right

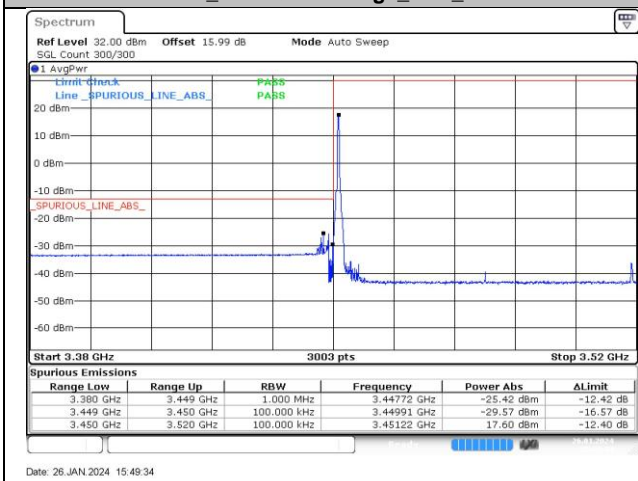






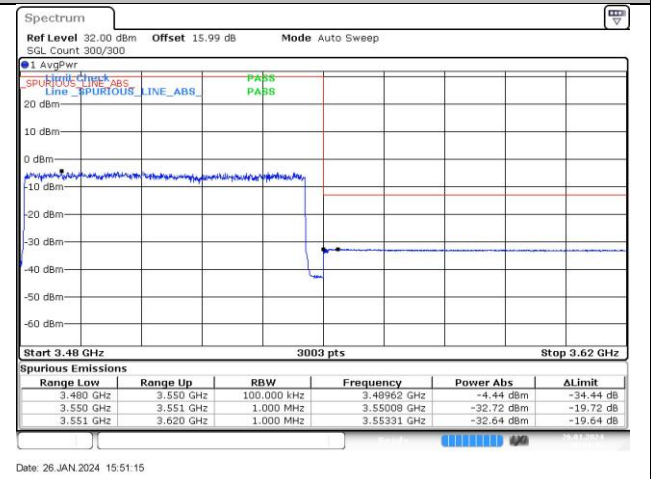
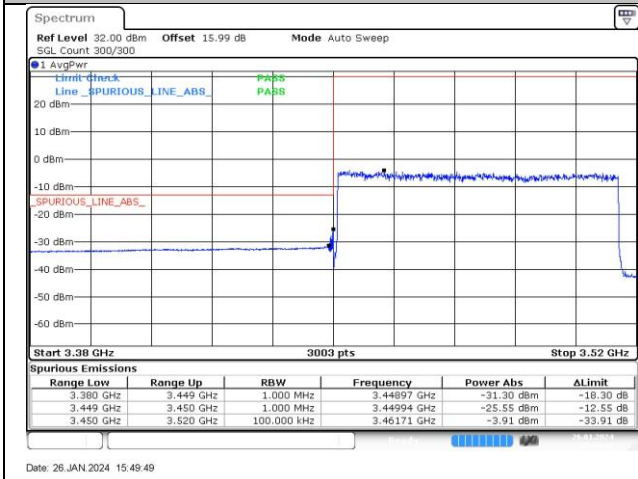
LCH\_DFT-QPSK / Edge\_1RB\_Left

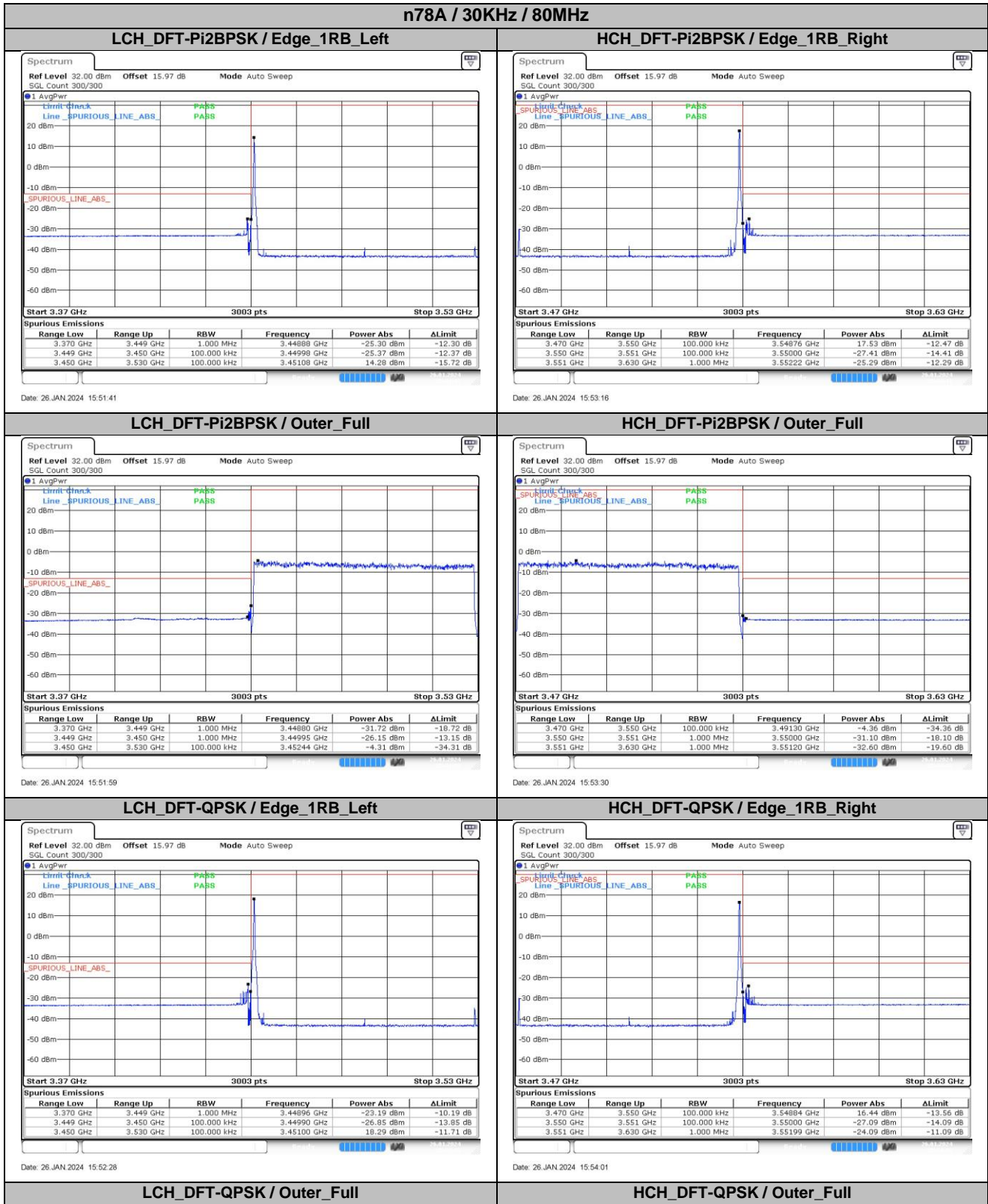
HCH\_DFT-QPSK / Edge\_1RB\_Right

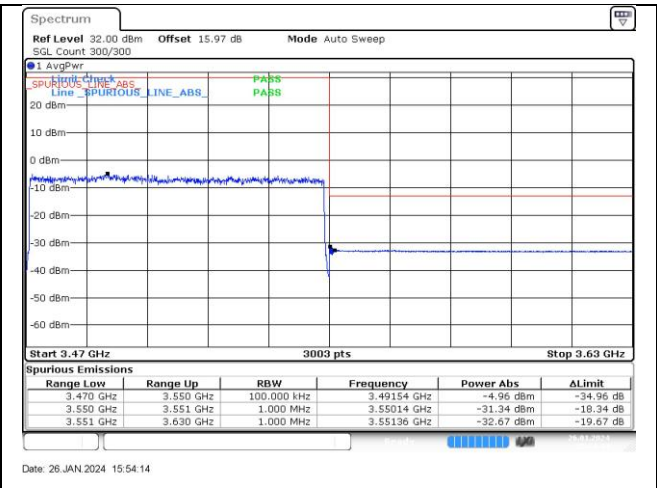
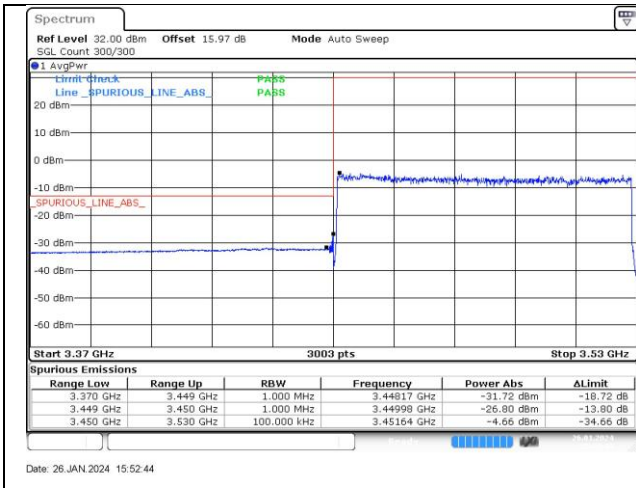


LCH\_DFT-QPSK / Outer\_Full

HCH\_DFT-QPSK / Outer\_Full

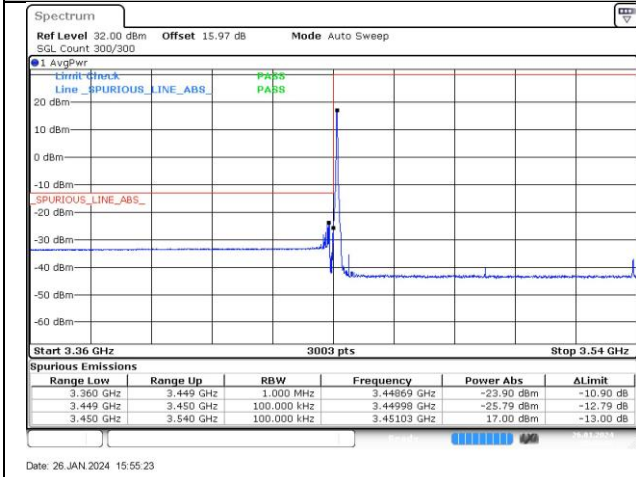




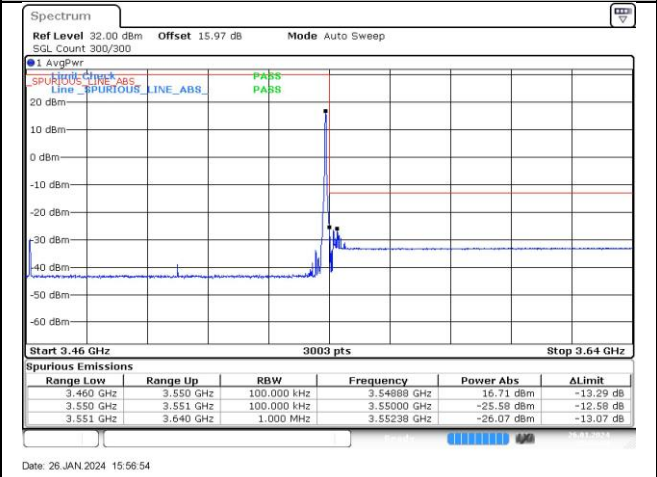


n78A / 30KHz / 90MHz

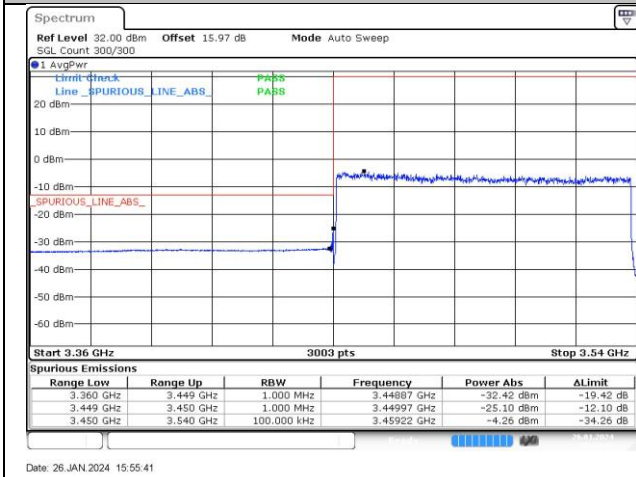
LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



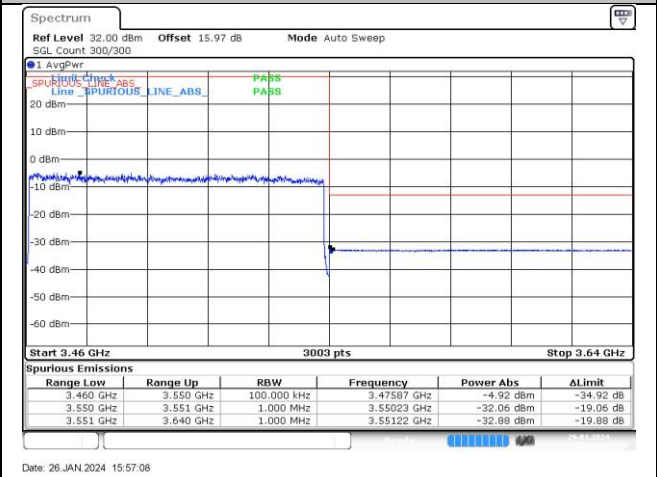
HCH\_DFT-Pi2BPSK / Edge\_1RB\_Right



LCH\_DFT-Pi2BPSK / Outer\_Full

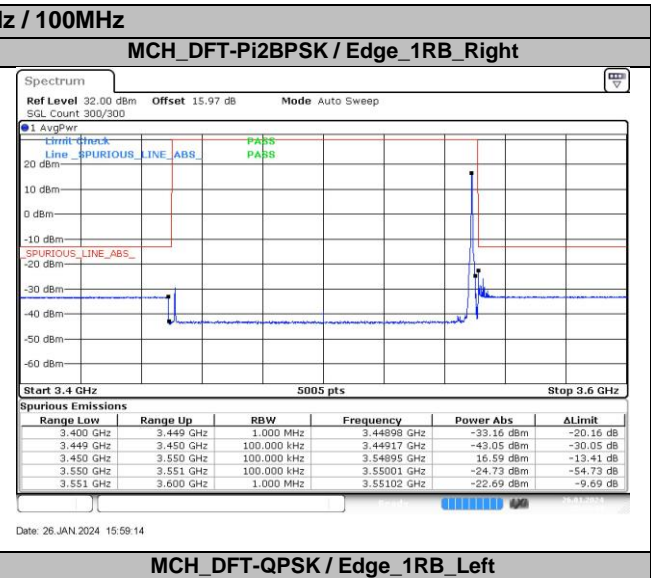
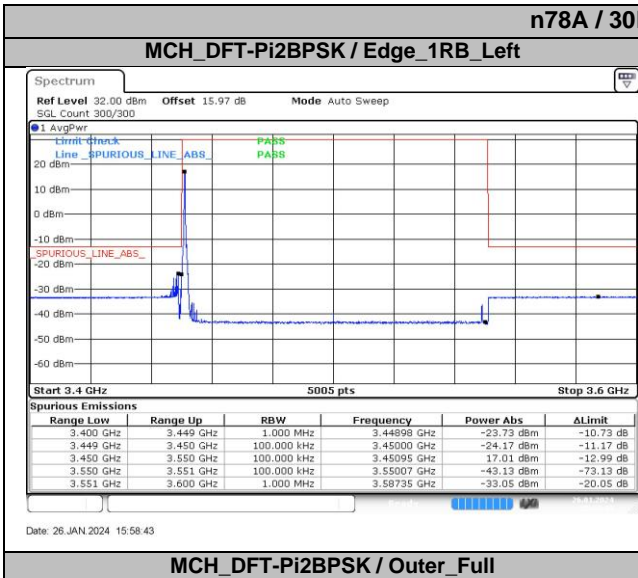
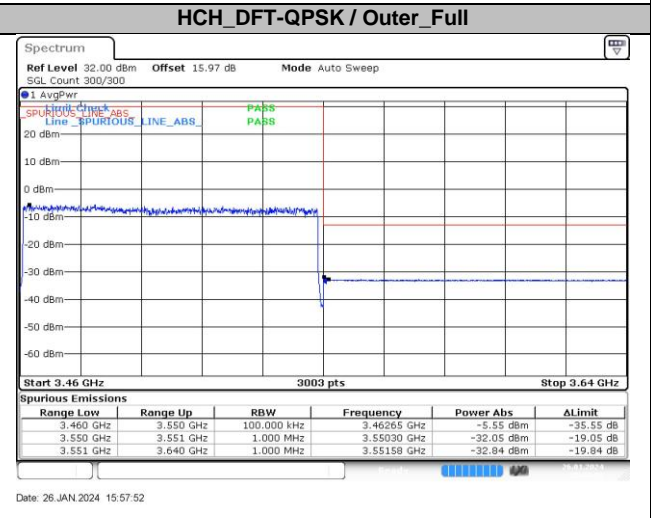
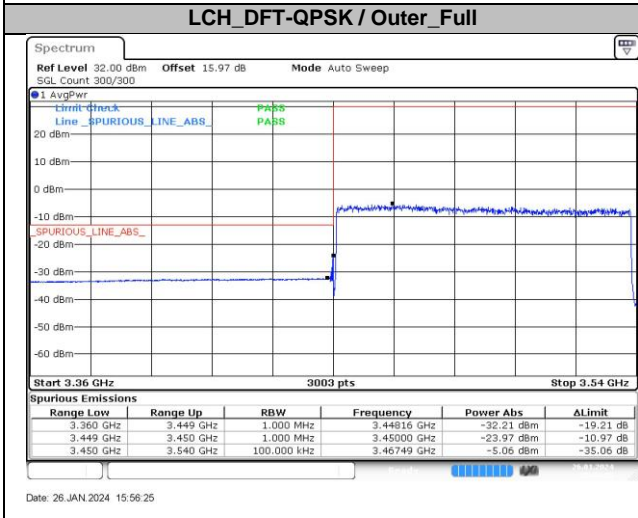
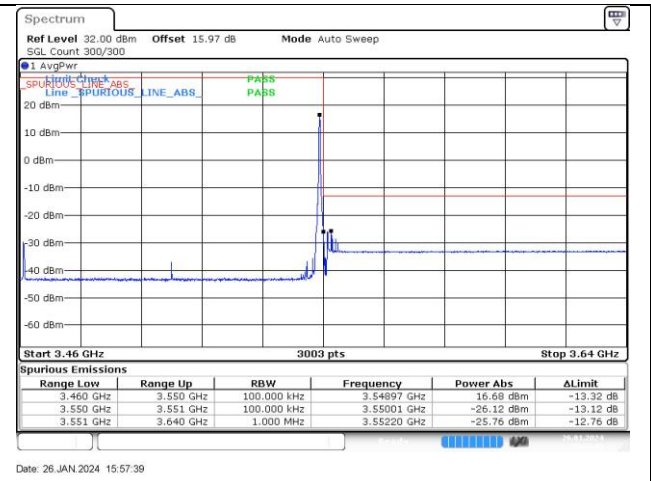
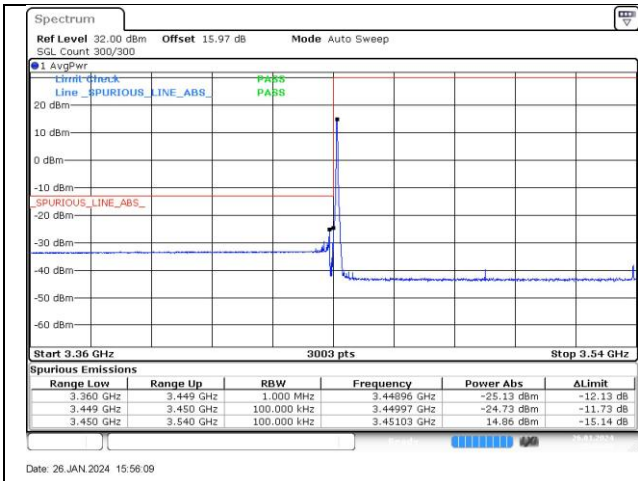


HCH\_DFT-Pi2BPSK / Outer\_Full



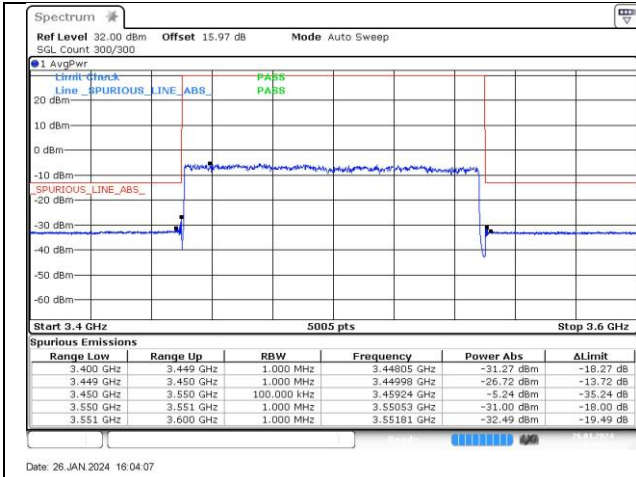
LCH\_DFT-QPSK / Edge\_1RB\_Left

HCH\_DFT-QPSK / Edge\_1RB\_Right

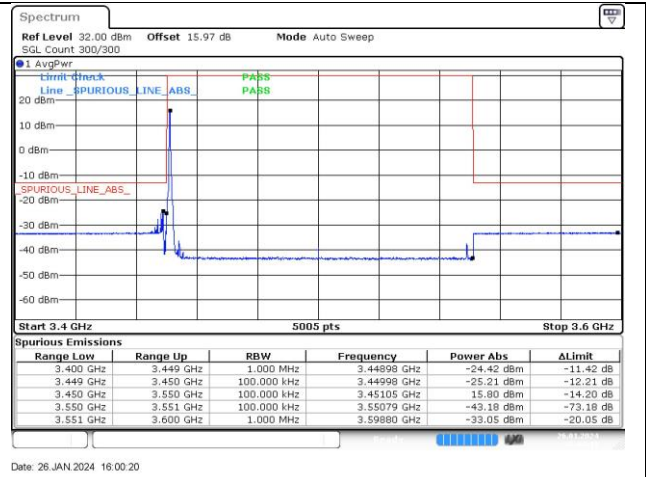


MCH\_DFT-Pi2BPSK / Outer\_Full

MCH\_DFT-QPSK / Edge\_1RB\_Left

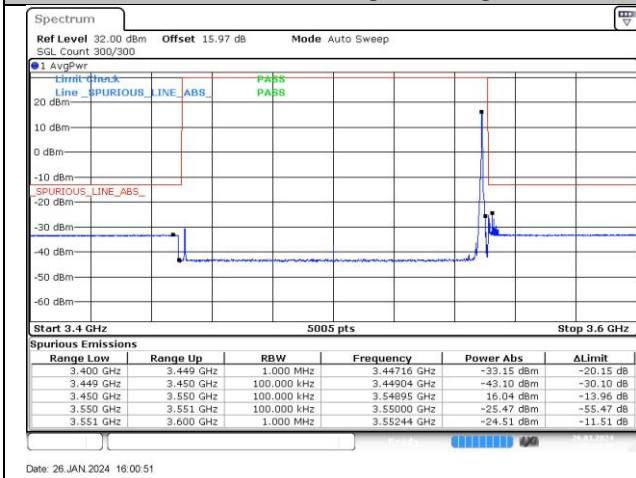


Date: 26 JAN 2024 16:04:07



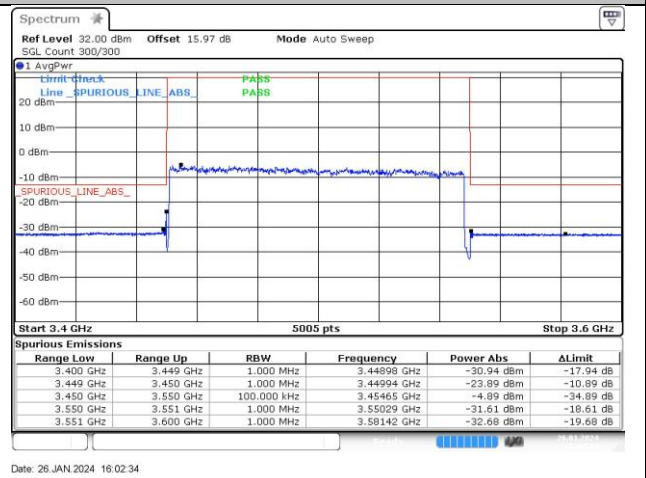
Date: 26 JAN 2024 16:00:20

MCH\_DFT-QPSK / Edge\_1RB\_Right



Date: 26 JAN 2024 16:00:51

MCH\_DFT-QPSK / Outer\_Full

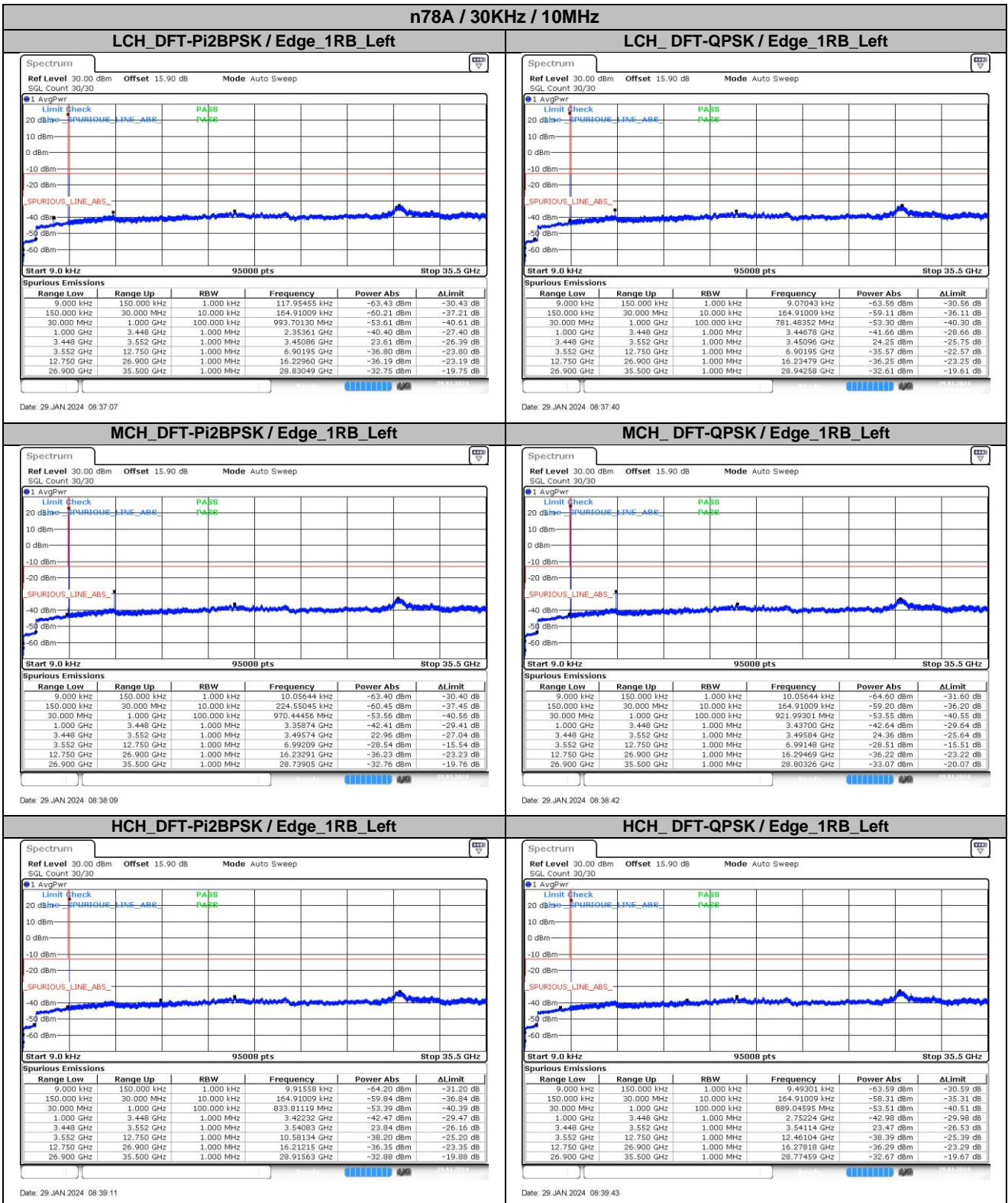


Date: 26 JAN 2024 16:00:34



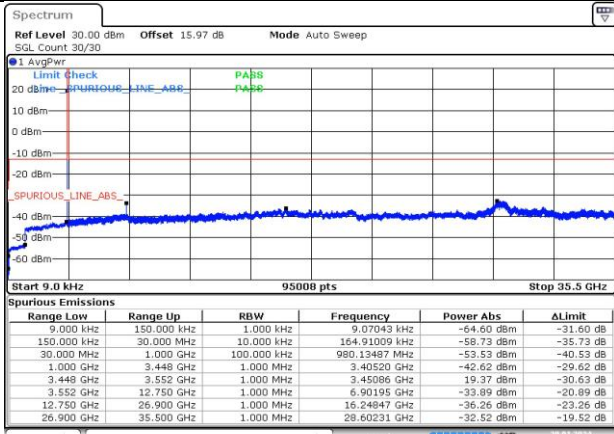
### 6. Conducted Spurious Emission

#### 6.1. Test Plots



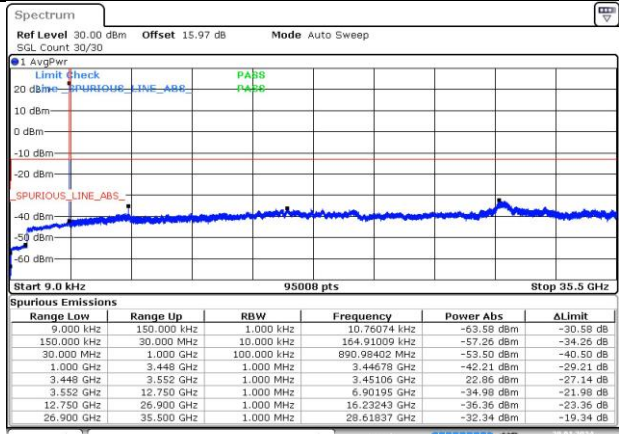
n78A / 30KHz / 15MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



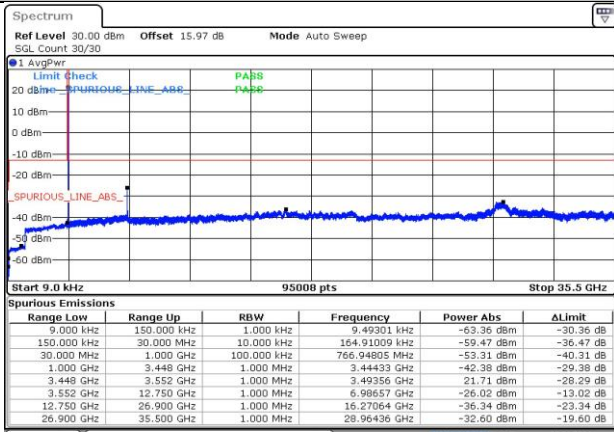
Date: 29.JAN.2024 08:40:18

LCH\_DFT-QPSK / Edge\_1RB\_Left



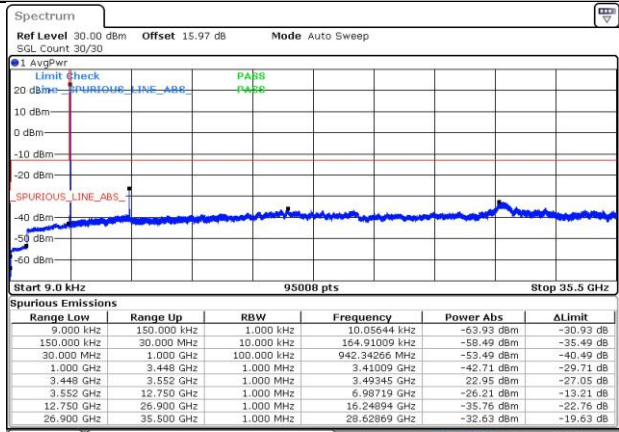
Date: 29.JAN.2024 08:41:32

MCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



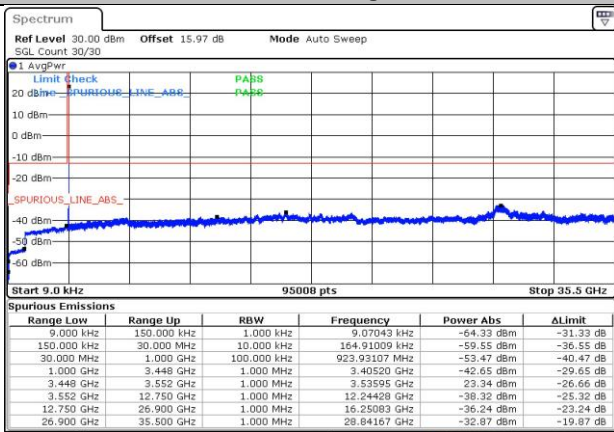
Date: 29.JAN.2024 08:42:02

MCH\_DFT-QPSK / Edge\_1RB\_Left



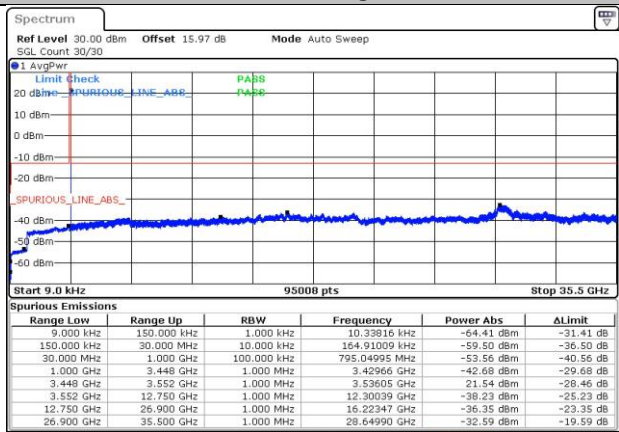
Date: 29.JAN.2024 08:42:34

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 29.JAN.2024 08:43:06

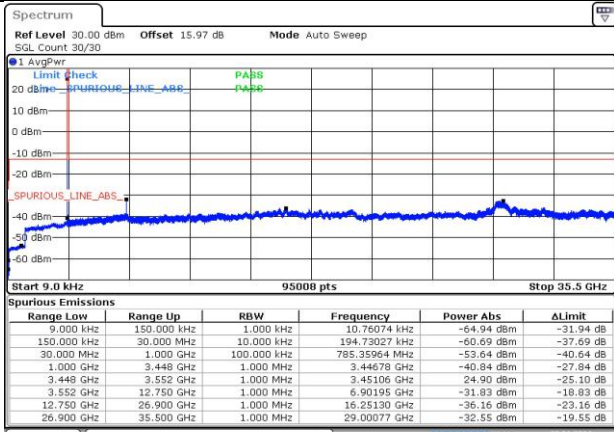
HCH\_DFT-QPSK / Edge\_1RB\_Left



Date: 29.JAN.2024 08:43:38

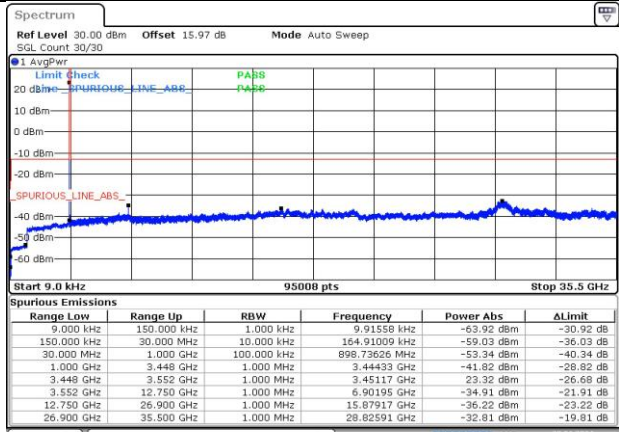
n78A / 30KHz / 20MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



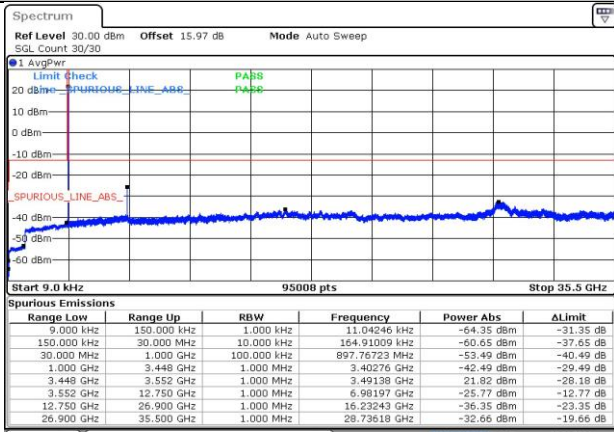
Date: 29 JAN 2024 08:44:09

LCH\_DFT-QPSK / Edge\_1RB\_Left



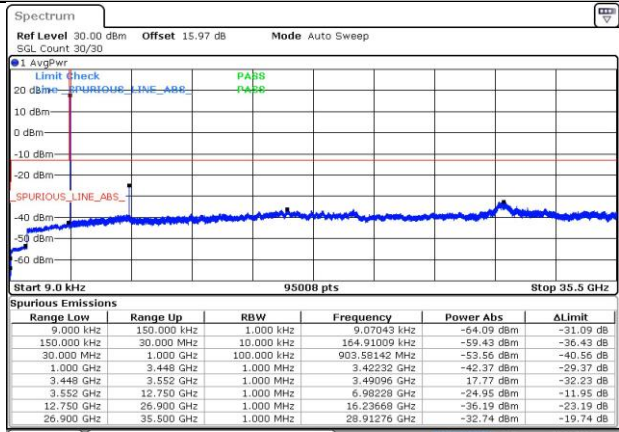
Date: 29 JAN 2024 08:44:56

MCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



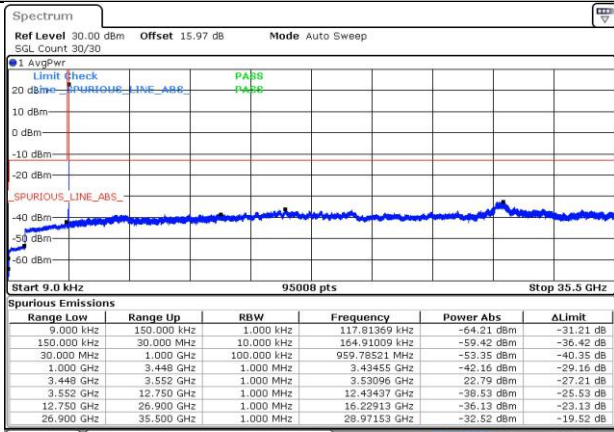
Date: 29 JAN 2024 08:45:28

MCH\_DFT-QPSK / Edge\_1RB\_Left



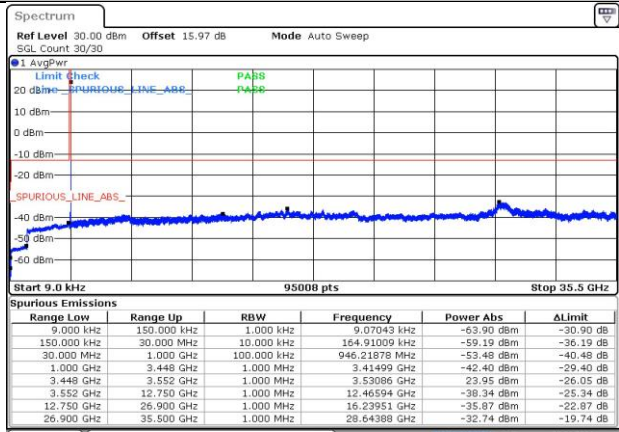
Date: 29 JAN 2024 08:45:58

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 29 JAN 2024 08:46:28

HCH\_DFT-QPSK / Edge\_1RB\_Left



Date: 29 JAN 2024 08:47:00