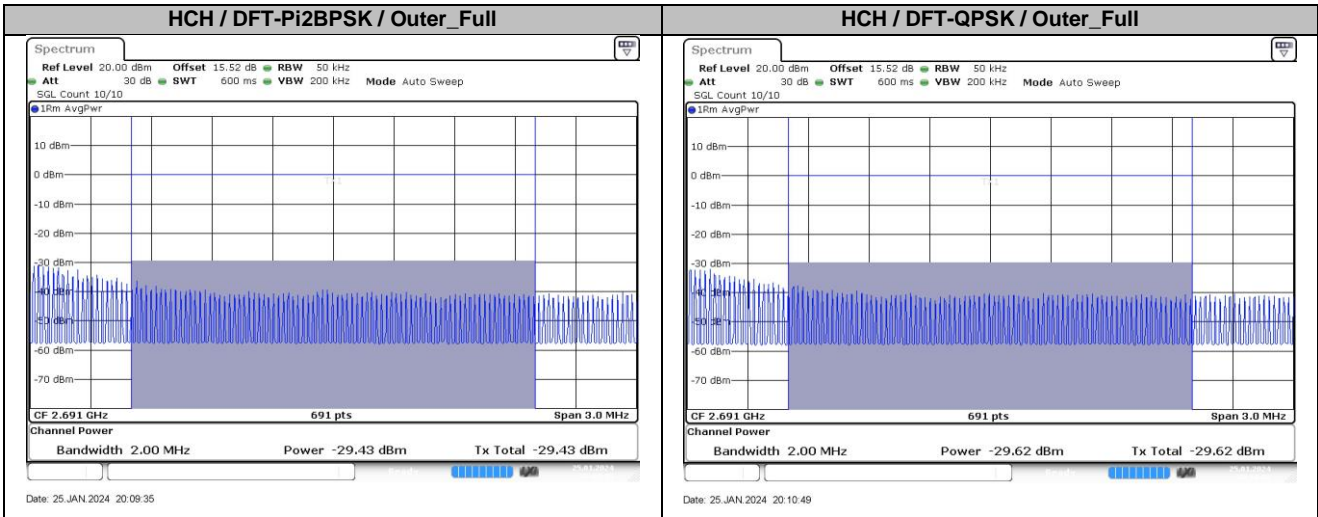
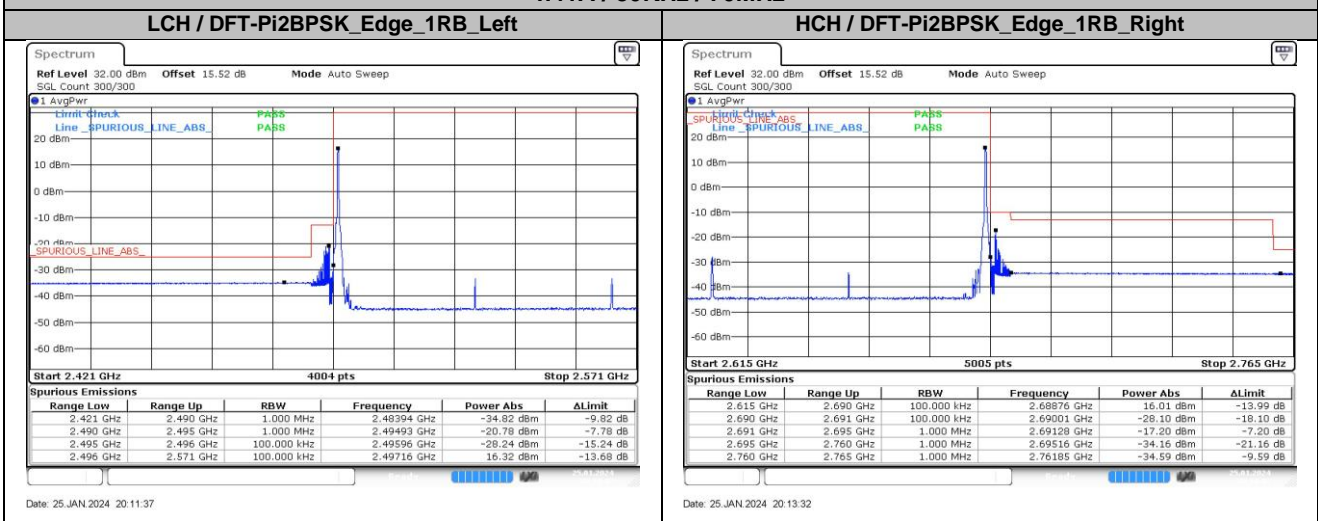
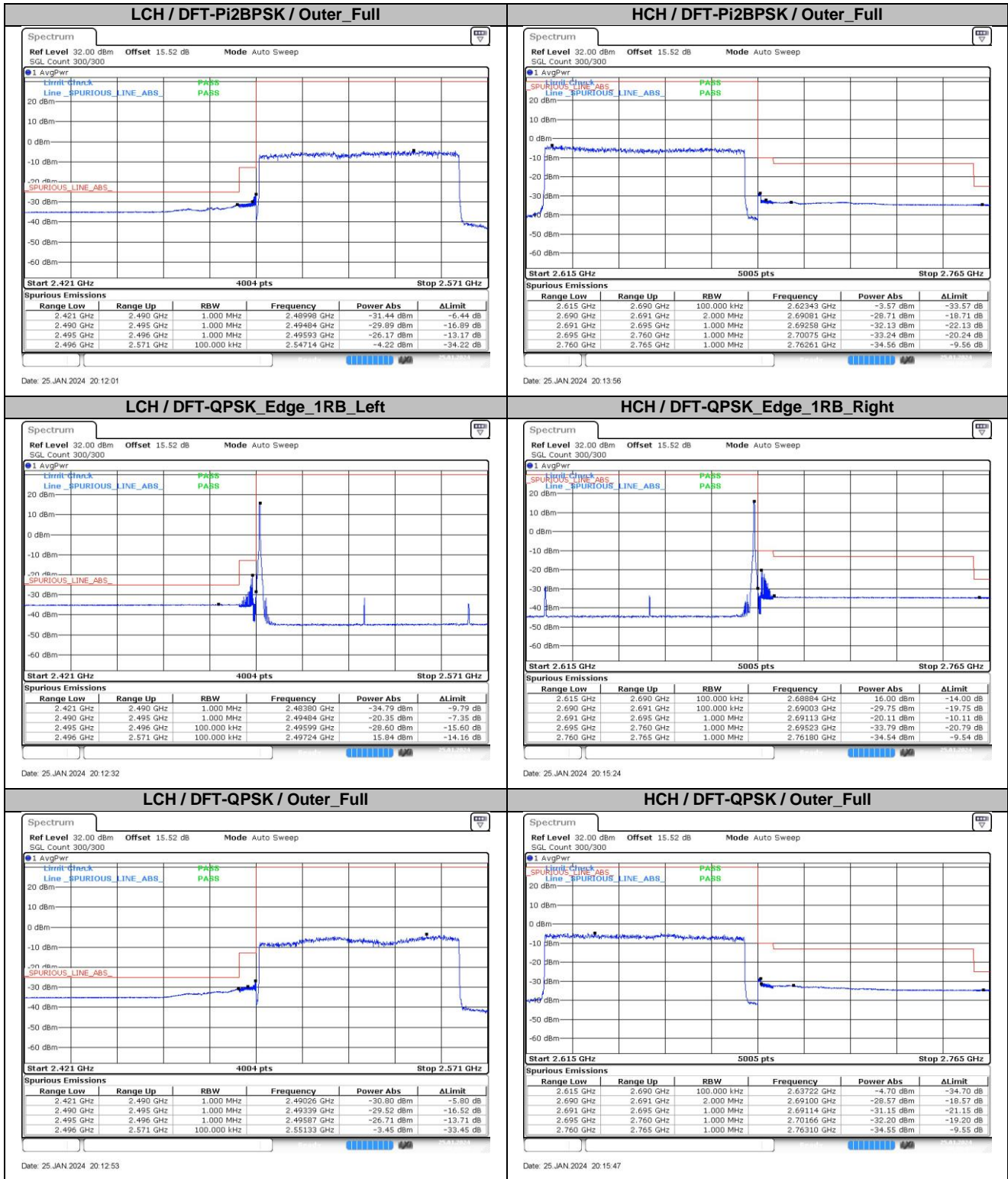


Channel Power:



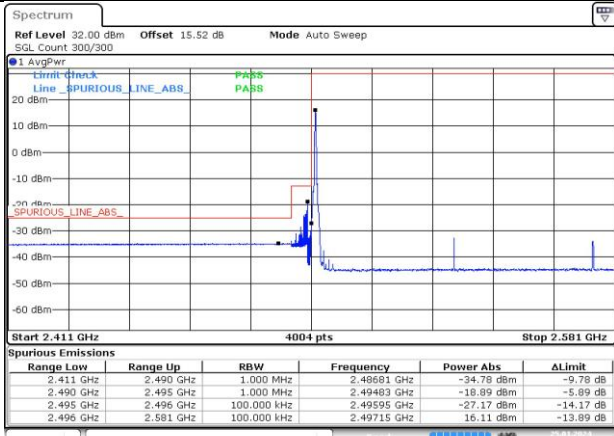
n41A / 30KHz / 70MHz





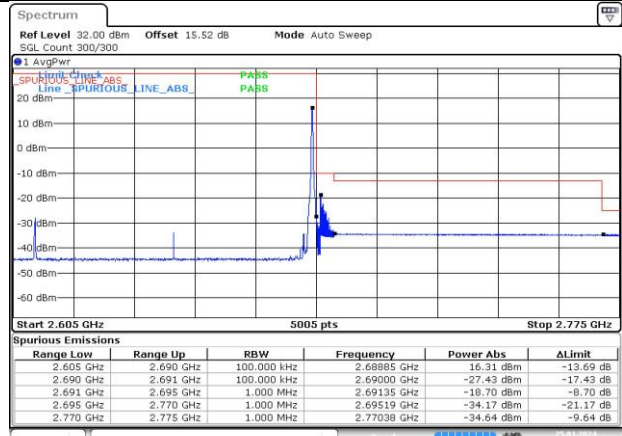
n41A / 30KHz / 80MHz

LCH / DFT-Pi2BPSK Edge_1RB_Left



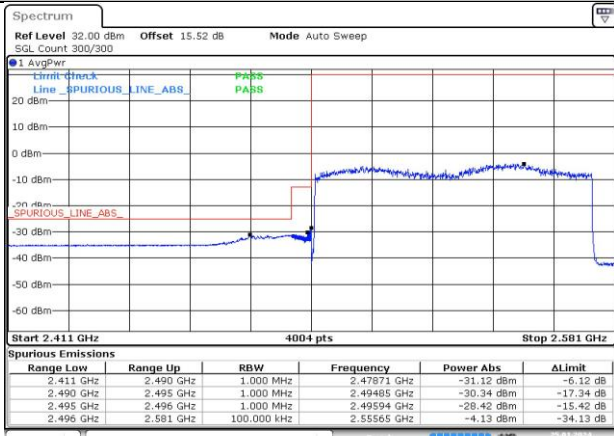
Date: 25.JAN.2024 20:16:37

HCH / DFT-Pi2BPSK Edge_1RB_Right



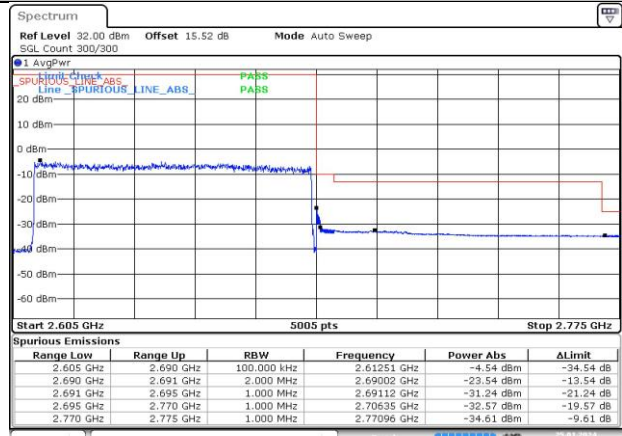
Date: 25.JAN.2024 20:16:33

LCH / DFT-Pi2BPSK / Outer_Full



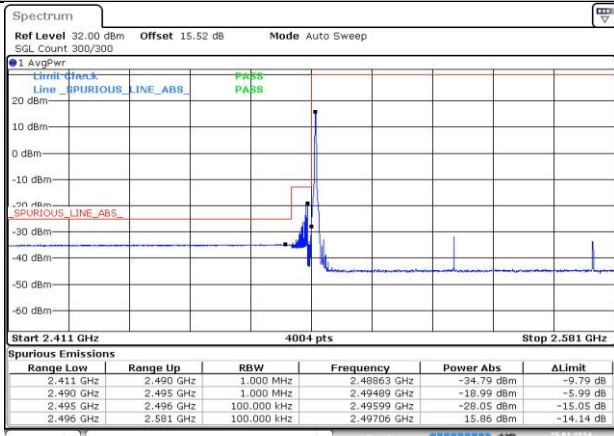
Date: 25.JAN.2024 20:17:00

HCH / DFT-Pi2BPSK / Outer_Full



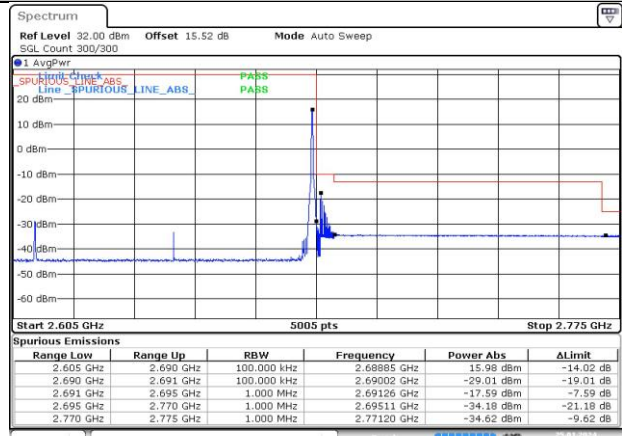
Date: 25.JAN.2024 20:18:57

LCH / DFT-QPSK Edge_1RB_Left



Date: 25.JAN.2024 20:17:33

HCH / DFT-QPSK Edge_1RB_Right



Date: 25.JAN.2024 20:19:36

