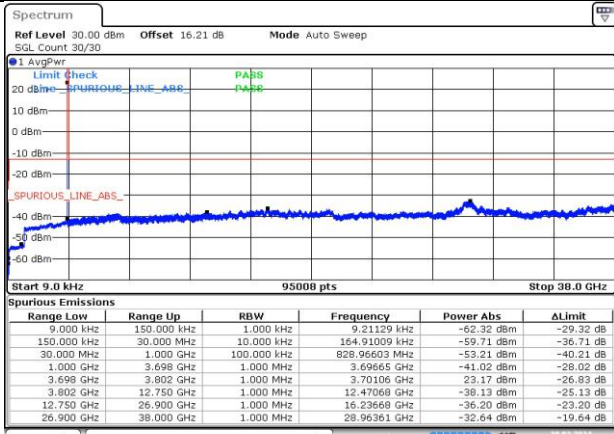


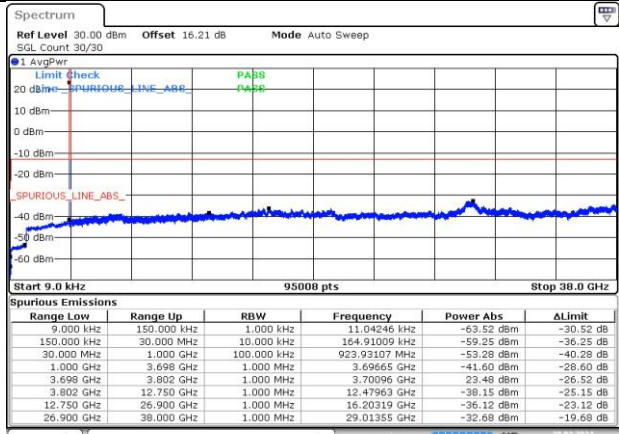
n78A / 30KHz / 20MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



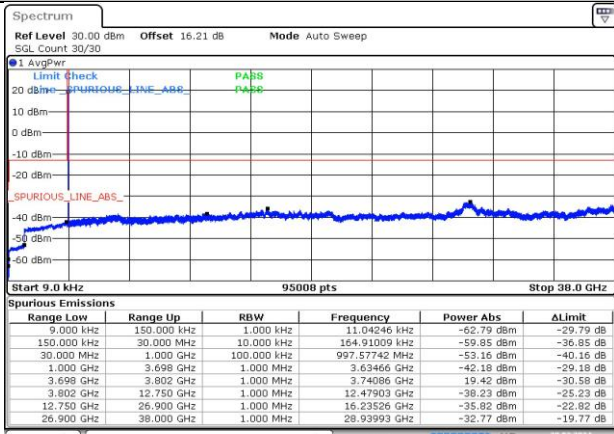
Date: 20 MAR 2024 03:55:34

LCH_DFT-QPSK / Edge_1RB_Left



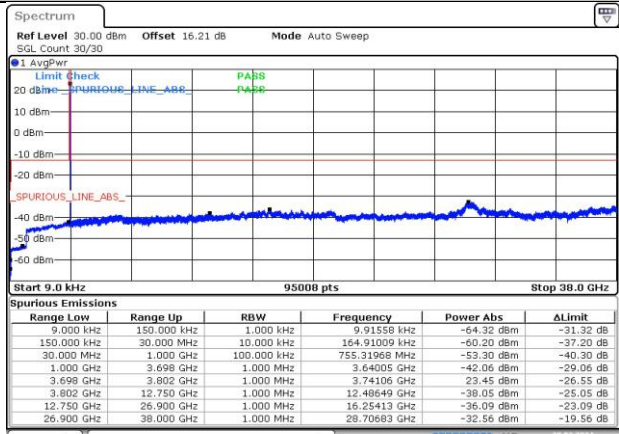
Date: 20 MAR 2024 03:56:07

MCH_DFT-Pi2BPSK / Edge_1RB_Left



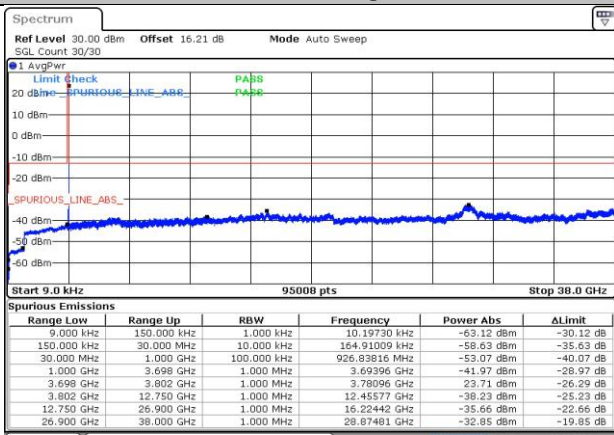
Date: 20 MAR 2024 03:56:37

MCH_DFT-QPSK / Edge_1RB_Left



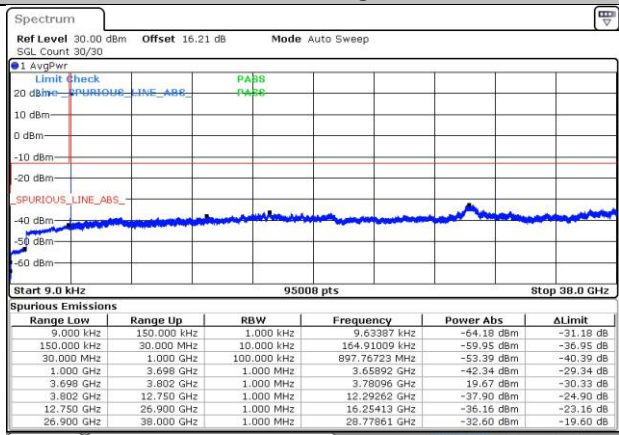
Date: 20 MAR 2024 03:57:09

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 20 MAR 2024 03:57:39

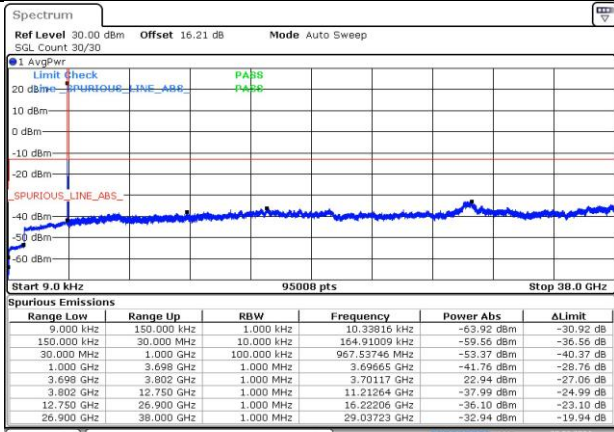
HCH_DFT-QPSK / Edge_1RB_Left



Date: 20 MAR 2024 03:58:13

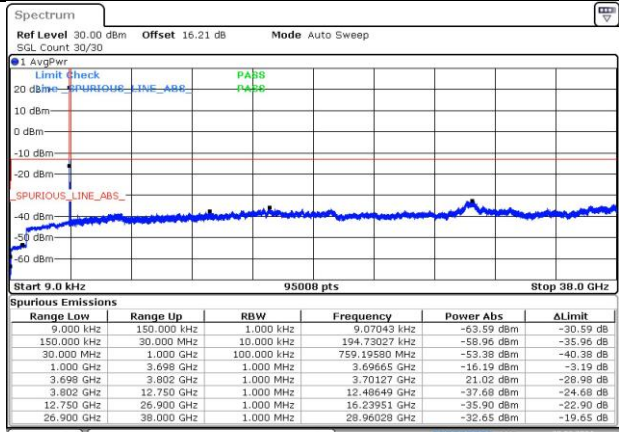
n78A / 30KHz / 30MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



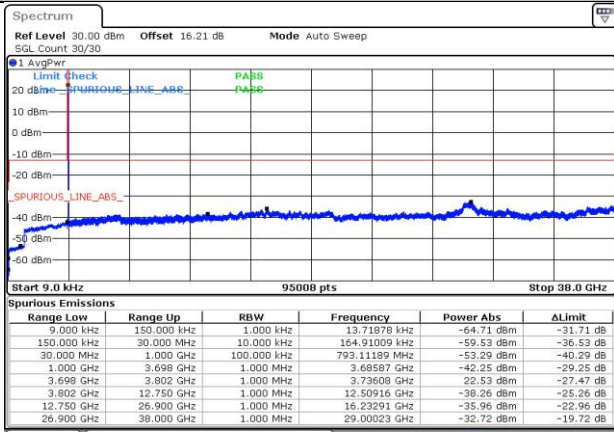
Date: 20 MAR 2024 03:59:03

LCH_DFT-QPSK / Edge_1RB_Left



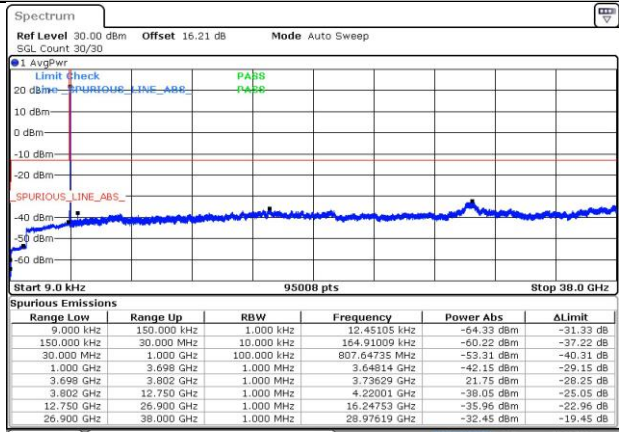
Date: 20 MAR 2024 03:59:36

MCH_DFT-Pi2BPSK / Edge_1RB_Left



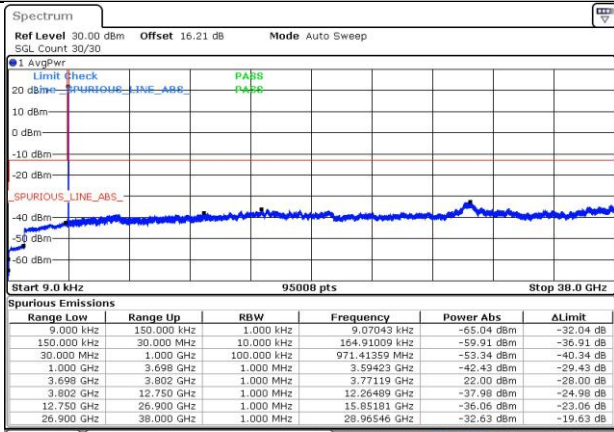
Date: 20 MAR 2024 04:00:06

MCH_DFT-QPSK / Edge_1RB_Left



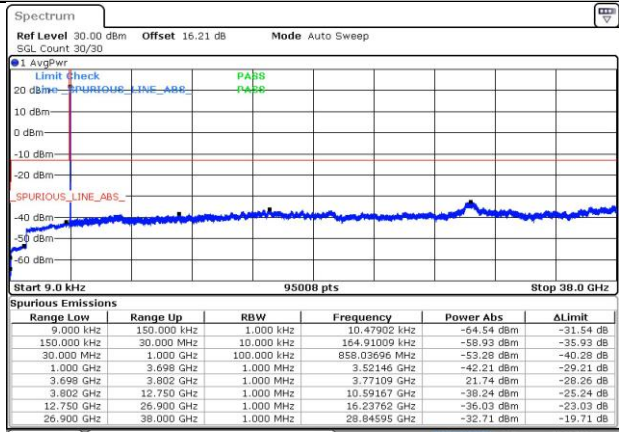
Date: 20 MAR 2024 04:01:22

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 20 MAR 2024 04:01:56

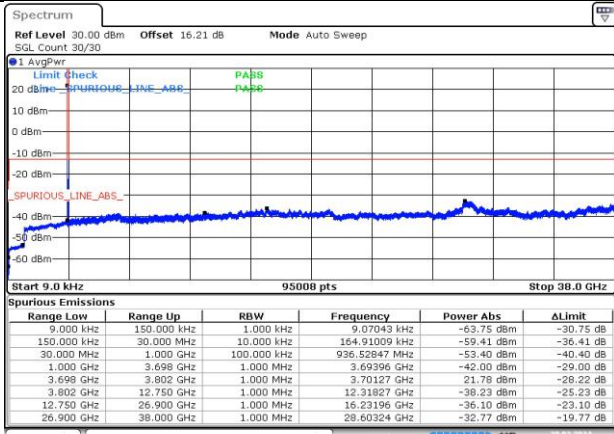
HCH_DFT-QPSK / Edge_1RB_Left



Date: 20 MAR 2024 04:02:30

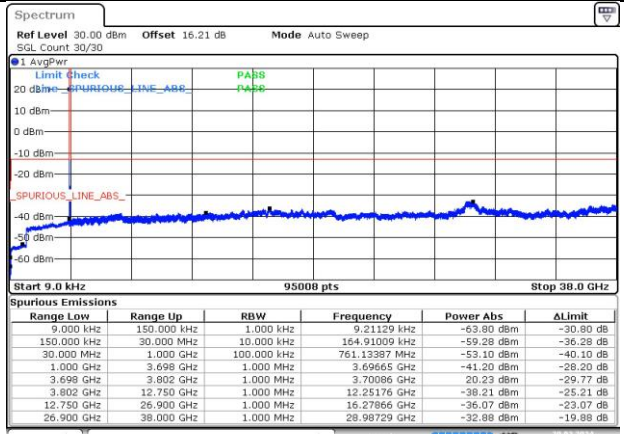
n78A / 30KHz / 40MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



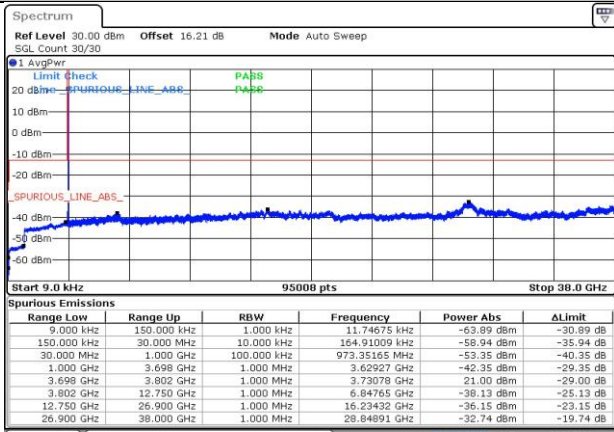
Date: 20.MAR.2024 04:03:20

LCH_DFT-QPSK / Edge_1RB_Left



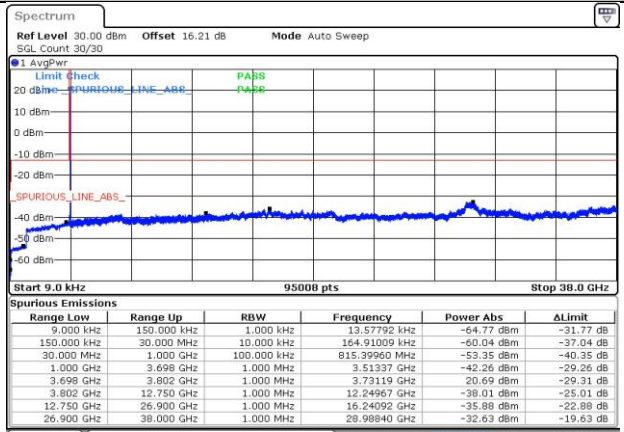
Date: 20.MAR.2024 04:03:53

MCH_DFT-Pi2BPSK / Edge_1RB_Left



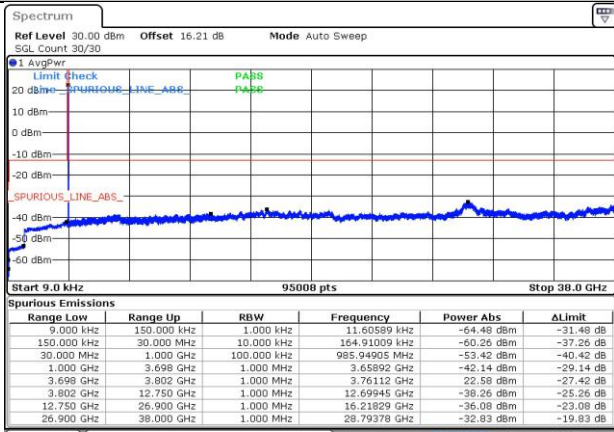
Date: 20.MAR.2024 04:04:23

MCH_DFT-QPSK / Edge_1RB_Left



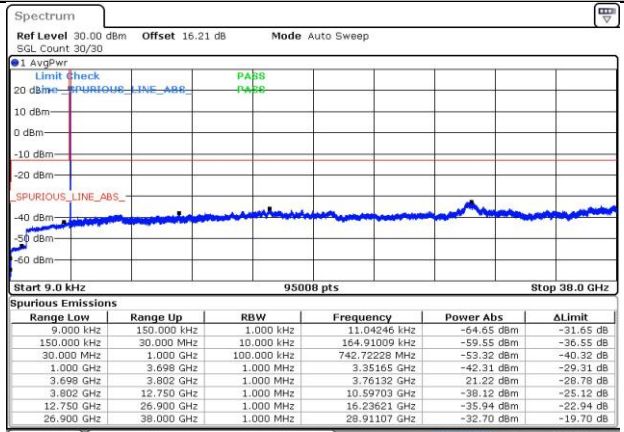
Date: 20.MAR.2024 04:04:56

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 20.MAR.2024 04:05:28

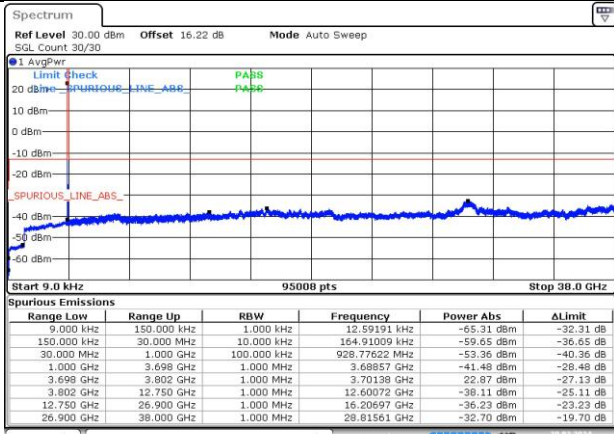
HCH_DFT-QPSK / Edge_1RB_Left



Date: 20.MAR.2024 04:05:59

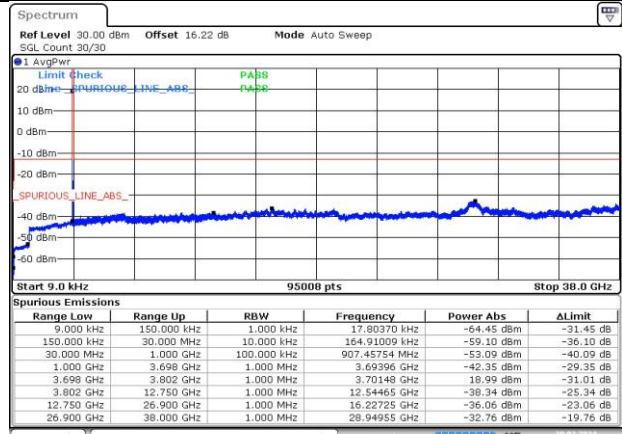
n78A / 30KHz / 50MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



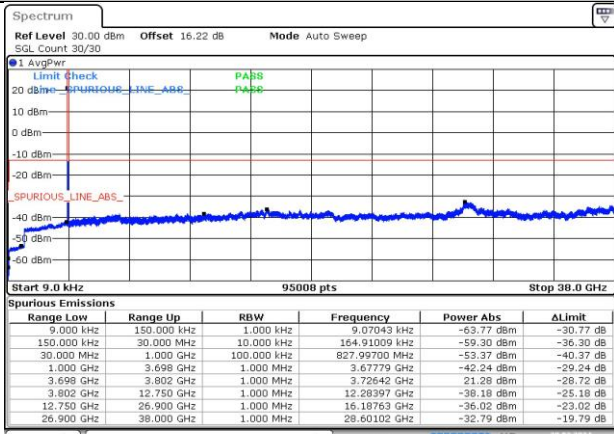
Date: 20 MAR 2024 04:06:49

LCH_DFT-QPSK / Edge_1RB_Left



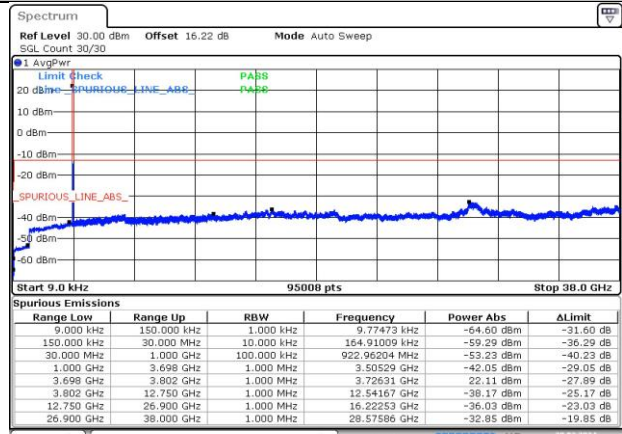
Date: 20 MAR 2024 04:07:22

MCH_DFT-Pi2BPSK / Edge_1RB_Left



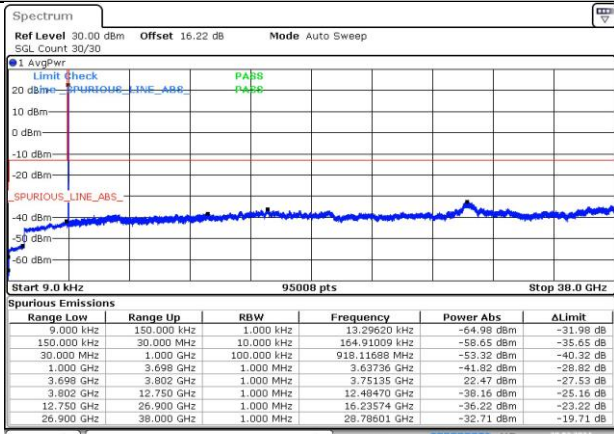
Date: 20 MAR 2024 04:07:56

MCH_DFT-QPSK / Edge_1RB_Left



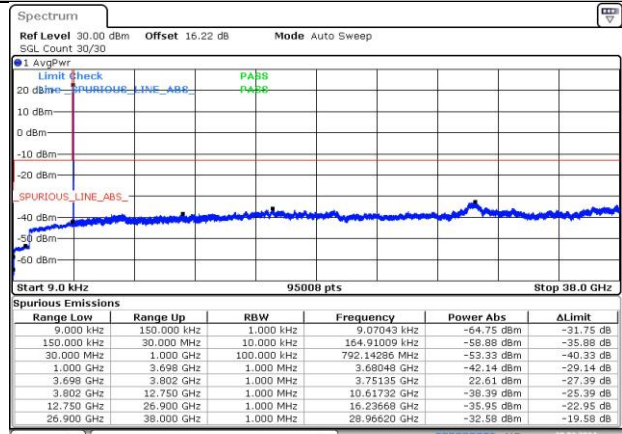
Date: 20 MAR 2024 04:08:30

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 20 MAR 2024 04:09:00

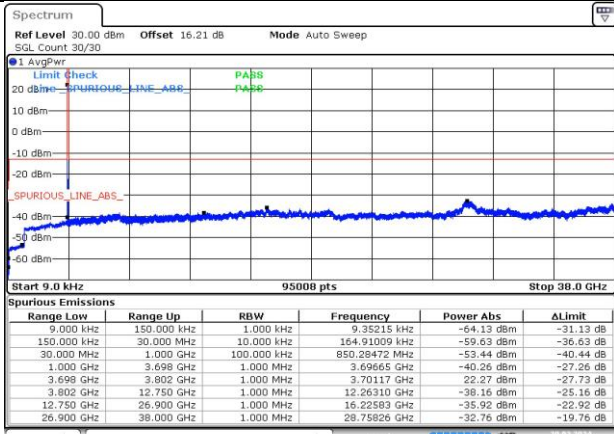
HCH_DFT-QPSK / Edge_1RB_Left



Date: 20 MAR 2024 04:09:33

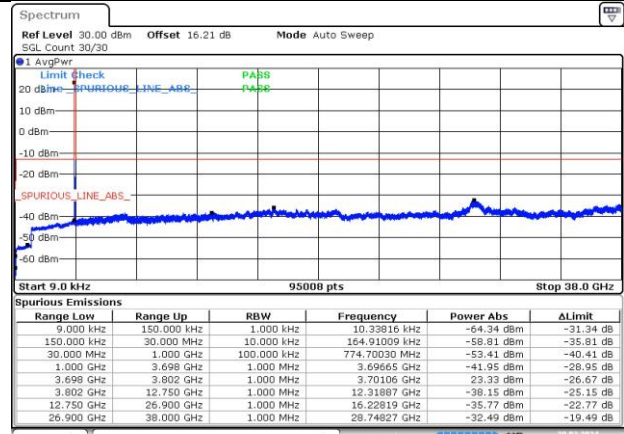
n78A / 30KHz / 60MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



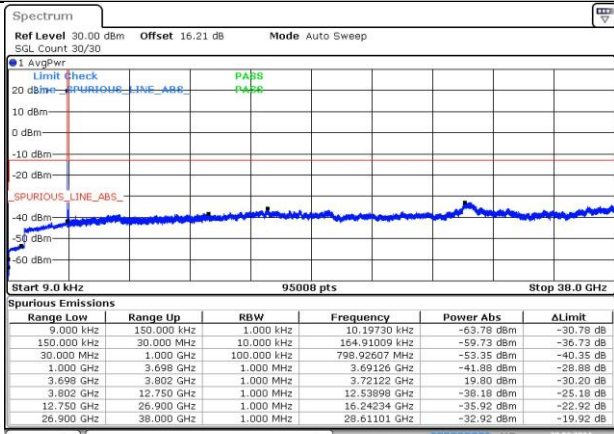
Date: 20.MAR.2024 04:10:23

LCH_DFT-QPSK / Edge_1RB_Left



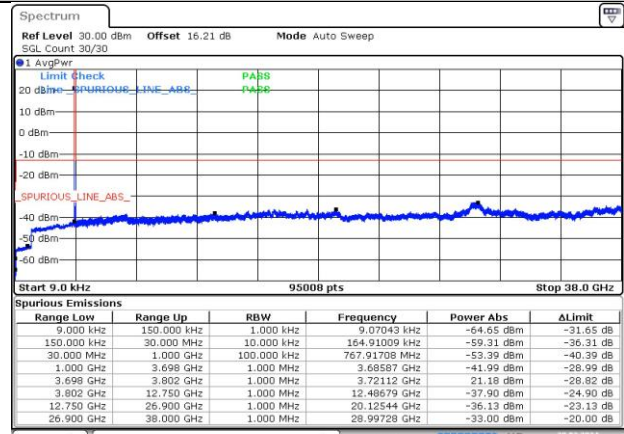
Date: 20.MAR.2024 04:10:56

MCH_DFT-Pi2BPSK / Edge_1RB_Left



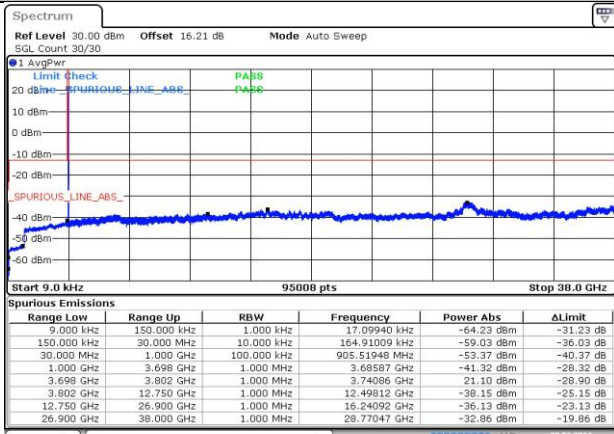
Date: 20.MAR.2024 04:11:28

MCH_DFT-QPSK / Edge_1RB_Left



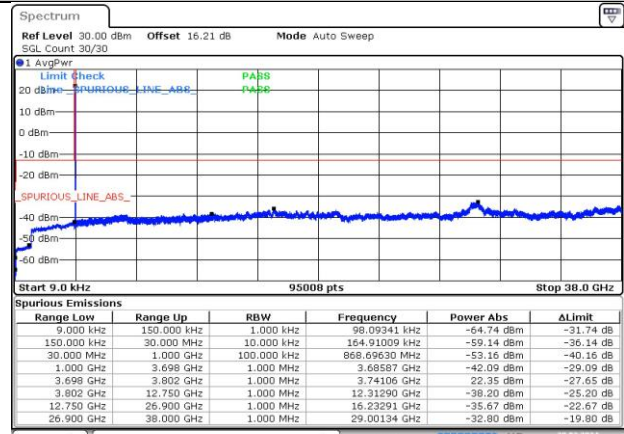
Date: 20.MAR.2024 04:11:59

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 20.MAR.2024 04:12:31

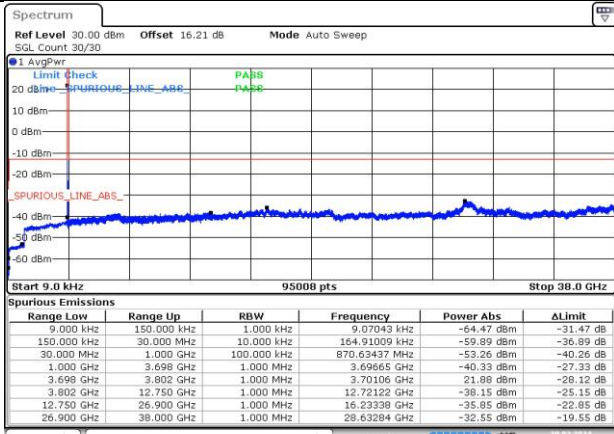
HCH_DFT-QPSK / Edge_1RB_Left



Date: 20.MAR.2024 04:13:04

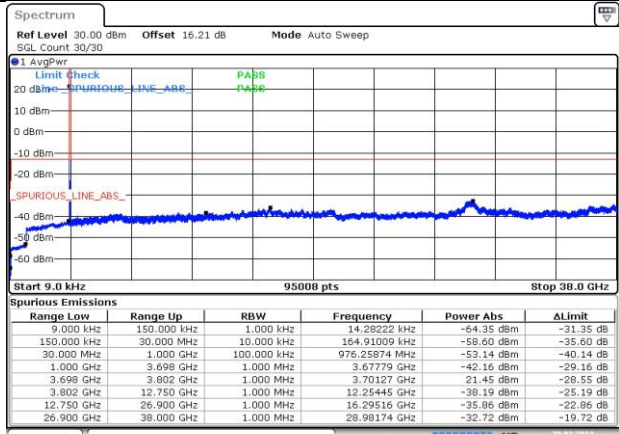
n78A / 30KHz / 70MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



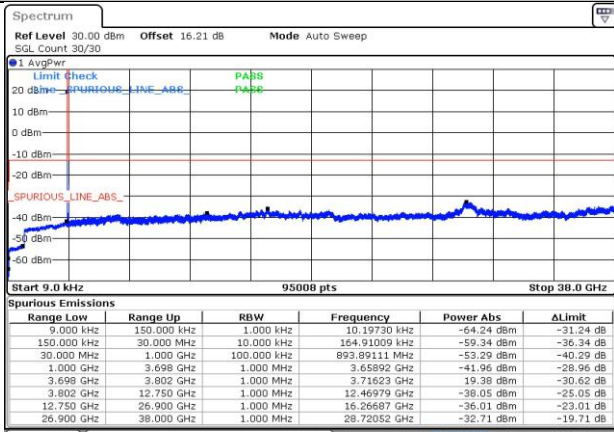
Date: 20.MAR.2024 04:13:53

LCH_DFT-QPSK / Edge_1RB_Left



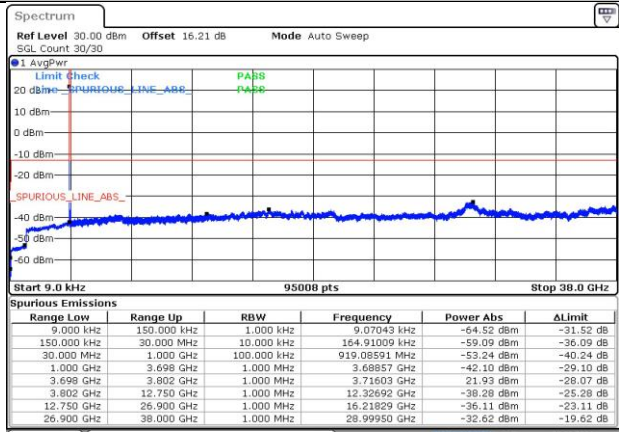
Date: 20.MAR.2024 04:14:27

MCH_DFT-Pi2BPSK / Edge_1RB_Left



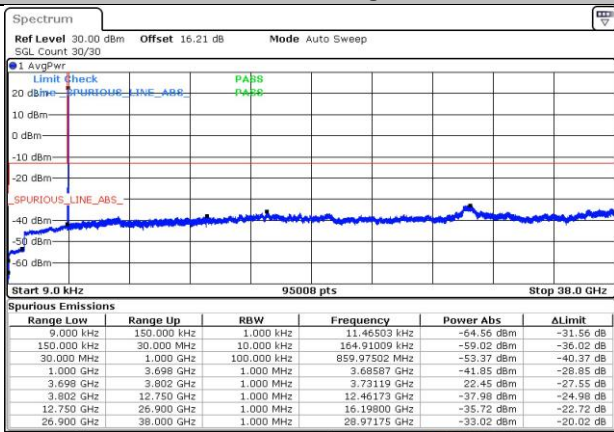
Date: 20.MAR.2024 04:14:56

MCH_DFT-QPSK / Edge_1RB_Left



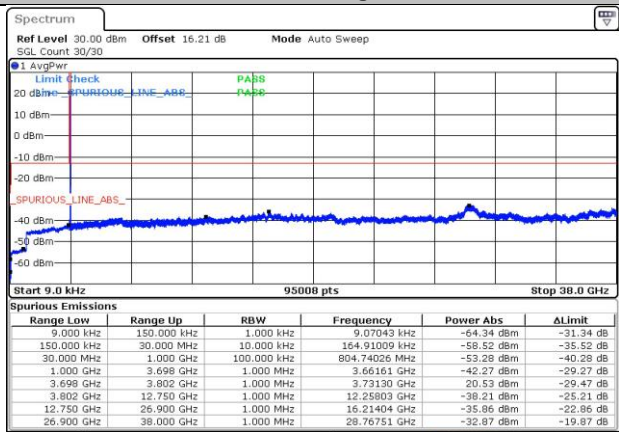
Date: 20.MAR.2024 04:15:30

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 20.MAR.2024 04:16:01

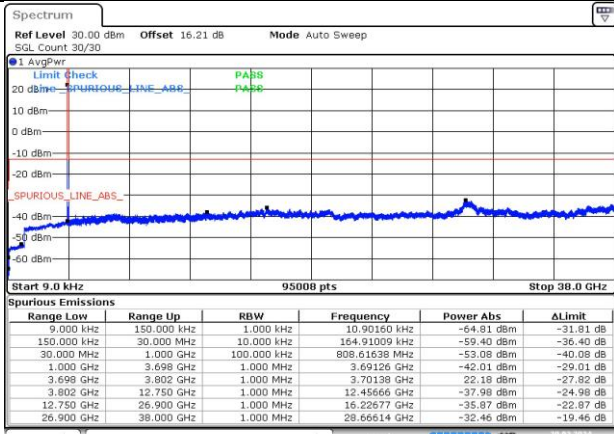
HCH_DFT-QPSK / Edge_1RB_Left



Date: 20.MAR.2024 04:16:35

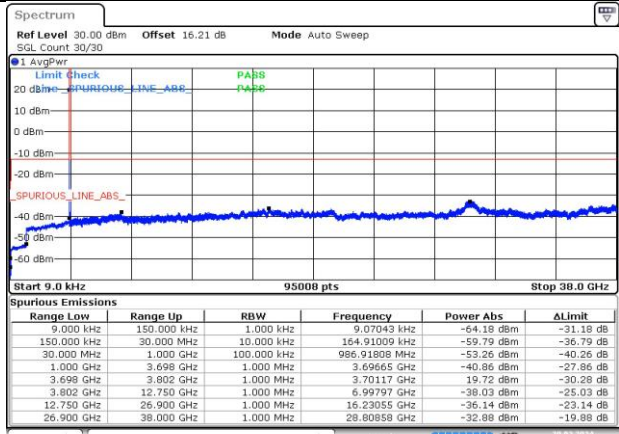
n78A / 30KHz / 80MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



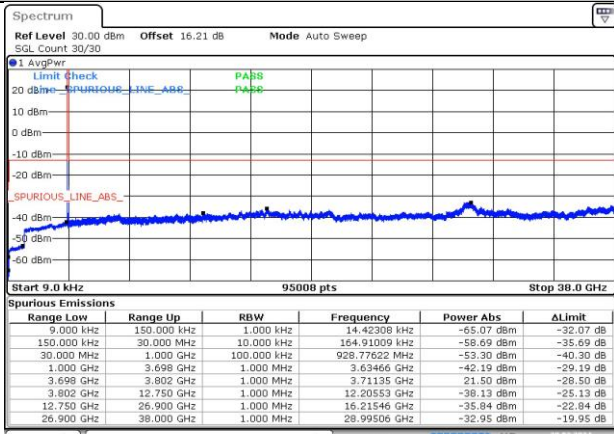
Date: 20 MAR 2024 04:17:25

LCH_DFT-QPSK / Edge_1RB_Left



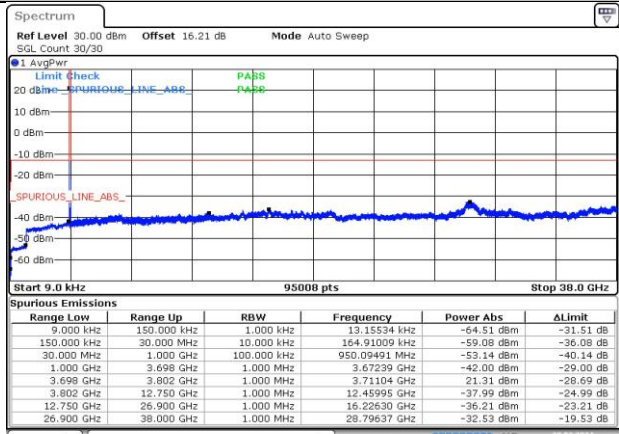
Date: 20 MAR 2024 04:17:58

MCH_DFT-Pi2BPSK / Edge_1RB_Left



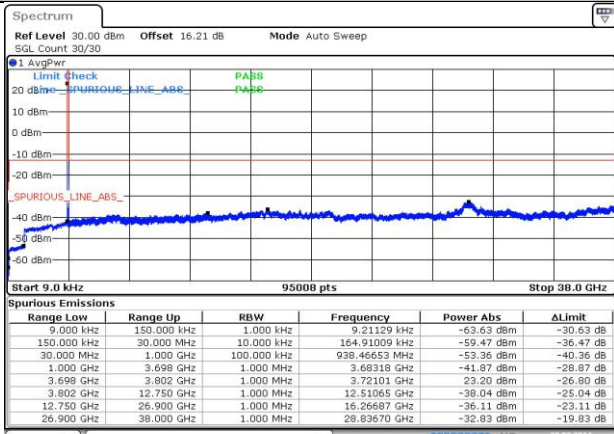
Date: 20 MAR 2024 04:18:32

MCH_DFT-QPSK / Edge_1RB_Left



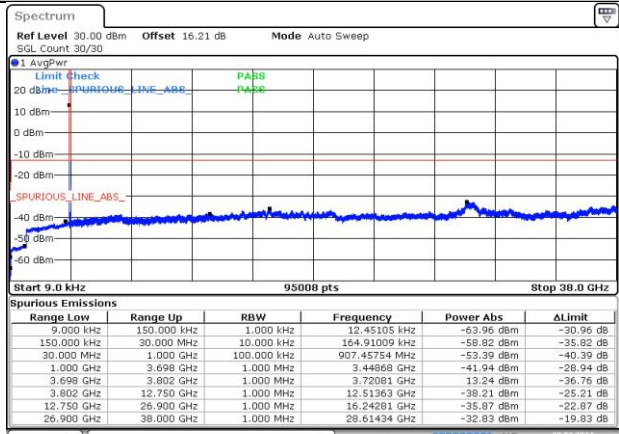
Date: 20 MAR 2024 04:19:05

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 20 MAR 2024 04:19:35

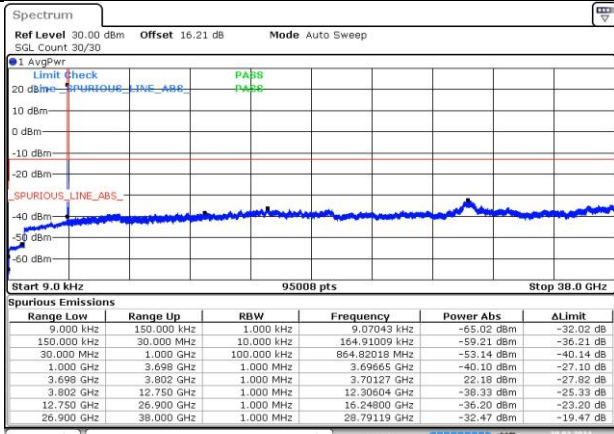
HCH_DFT-QPSK / Edge_1RB_Left



Date: 20 MAR 2024 04:20:08

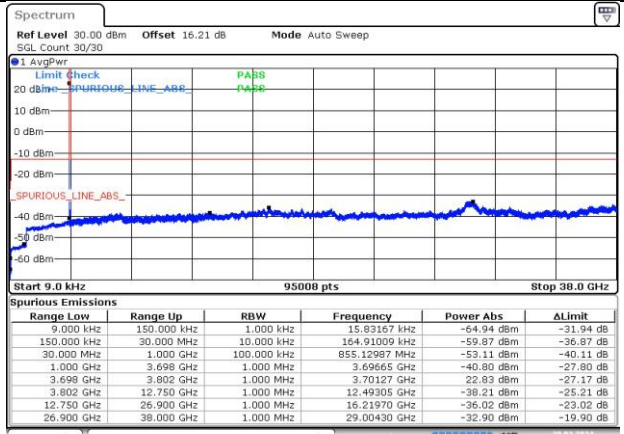
n78A / 30KHz / 90MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



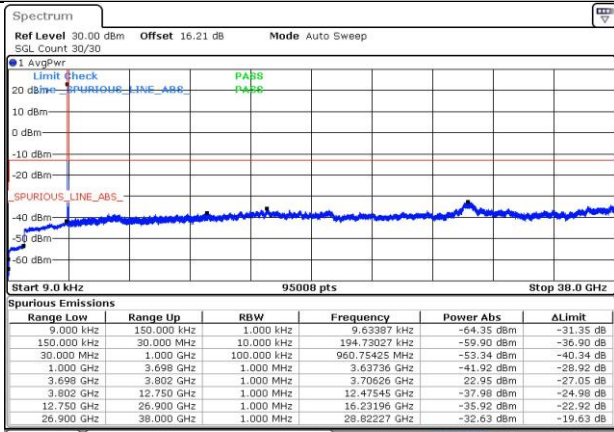
Date: 20 MAR 2024 04:20:58

LCH_DFT-QPSK / Edge_1RB_Left



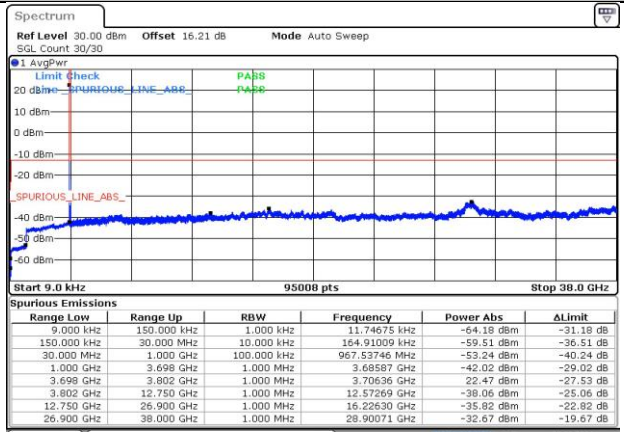
Date: 20 MAR 2024 04:21:31

MCH_DFT-Pi2BPSK / Edge_1RB_Left



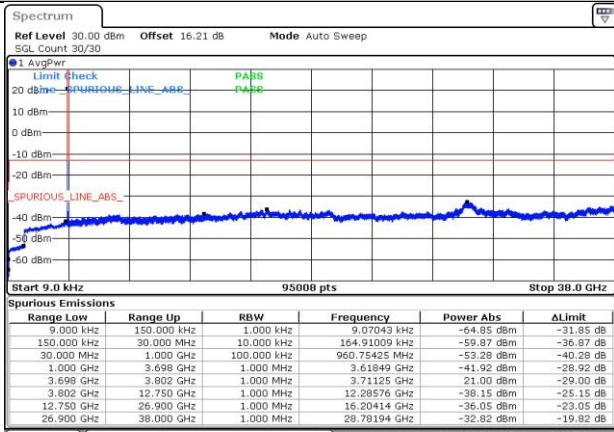
Date: 20 MAR 2024 04:22:01

MCH_DFT-QPSK / Edge_1RB_Left



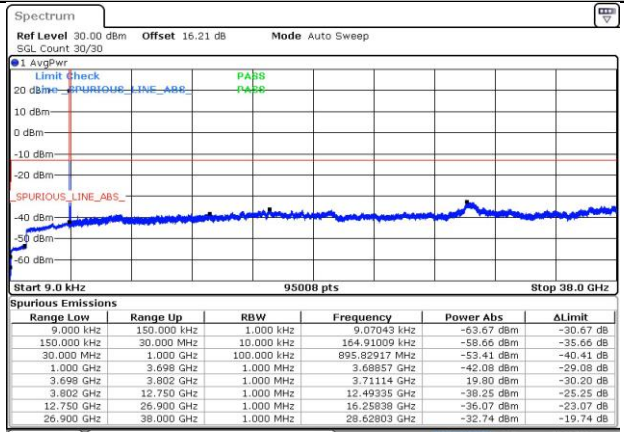
Date: 20 MAR 2024 04:22:34

HCH_DFT-Pi2BPSK / Edge_1RB_Left

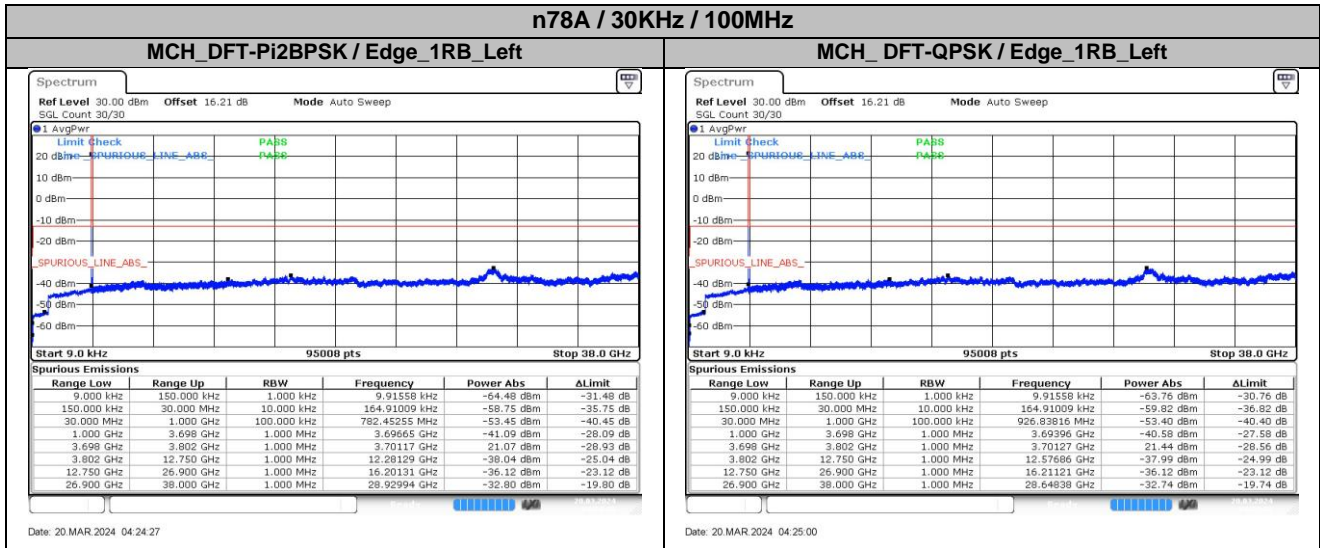


Date: 20 MAR 2024 04:23:04

HCH_DFT-QPSK / Edge_1RB_Left



Date: 20 MAR 2024 04:23:37



7. Frequency Stability

7.1. Test Results

7.1.1. Frequency Error Vs Voltage

SCS	Bandwidth	Channel	RB Config	Modulation	Temperature	Voltage	Deviation Result		Verdict
							(Hz)	(ppm)	
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	LV	-9.90	-0.002640	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	NV	-5.80	-0.001547	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	HV	-5.20	-0.001387	Pass

7.1.2. Frequency Error Vs Temperature

SCS	Bandwidth	Channel	RB Config	Modulation	Temperature	Voltage	Deviation Result		Verdict
							(Hz)	(ppm)	
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-30°C	NV	-3.70	-0.000987	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-20°C	NV	-6.20	-0.001653	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-10°C	NV	-11.20	-0.002987	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	0°C	NV	-10.00	-0.002667	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	10°C	NV	-8.60	-0.002293	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	20°C	NV	-15.20	-0.004053	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	30°C	NV	-11.10	-0.002960	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	40°C	NV	-10.20	-0.002720	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	50°C	NV	-5.50	-0.001467	Pass

The End