

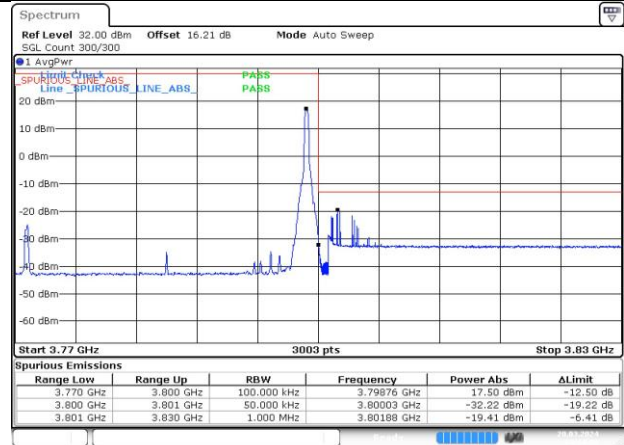
n78A / 30KHz / 30MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



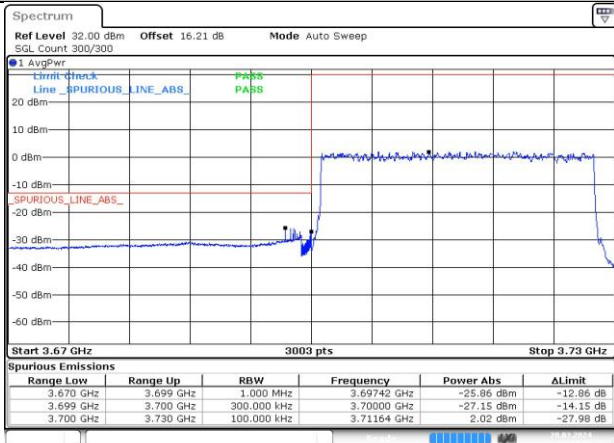
Date: 20 MAR 2024 03:12:51

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Right



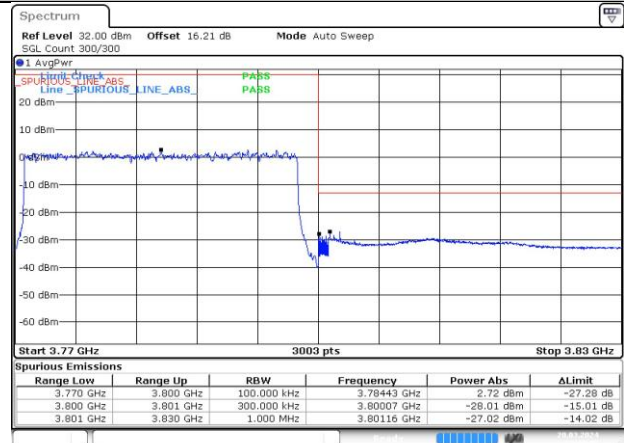
Date: 20 MAR 2024 03:14:59

LCH\_DFT-Pi2BPSK / Outer\_Full



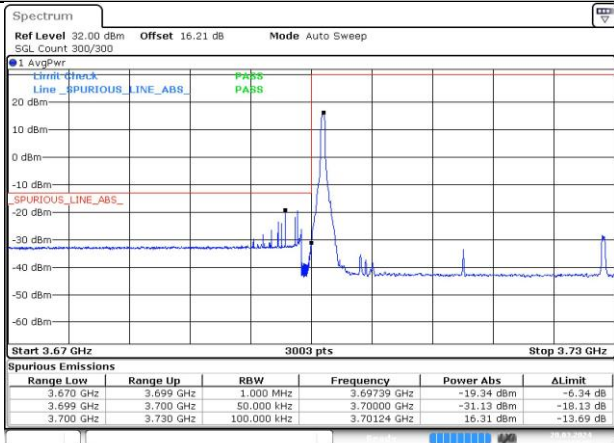
Date: 20 MAR 2024 03:13:08

HCH\_DFT-Pi2BPSK / Outer\_Full



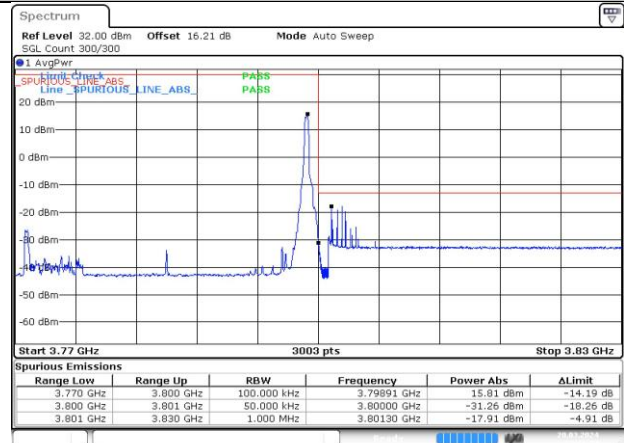
Date: 20 MAR 2024 03:15:15

LCH\_DFT-QPSK / Edge\_1RB\_Left

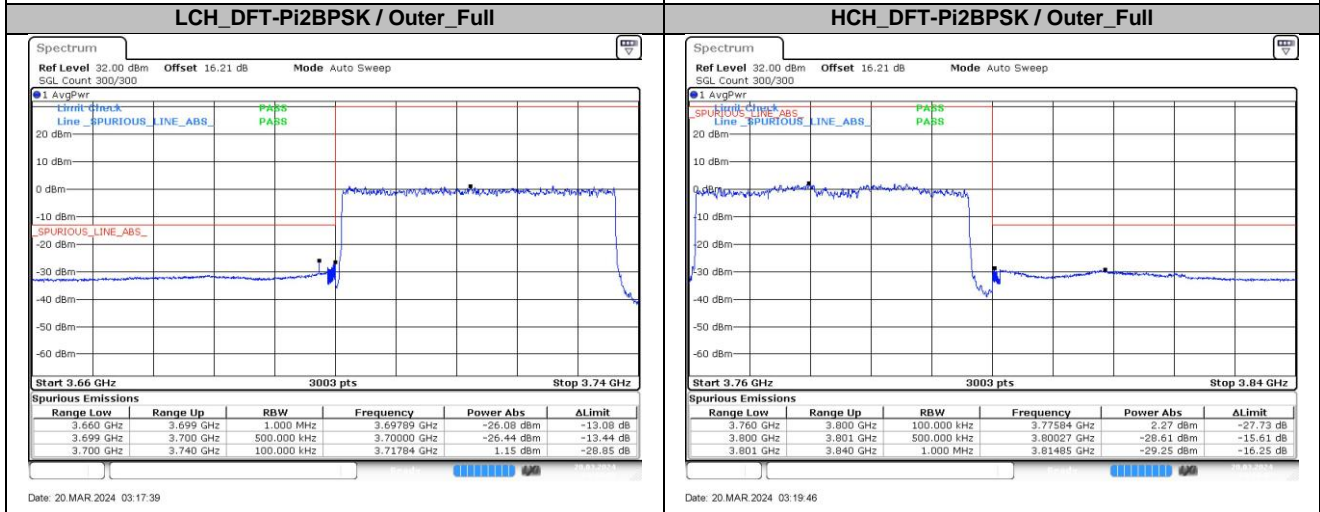
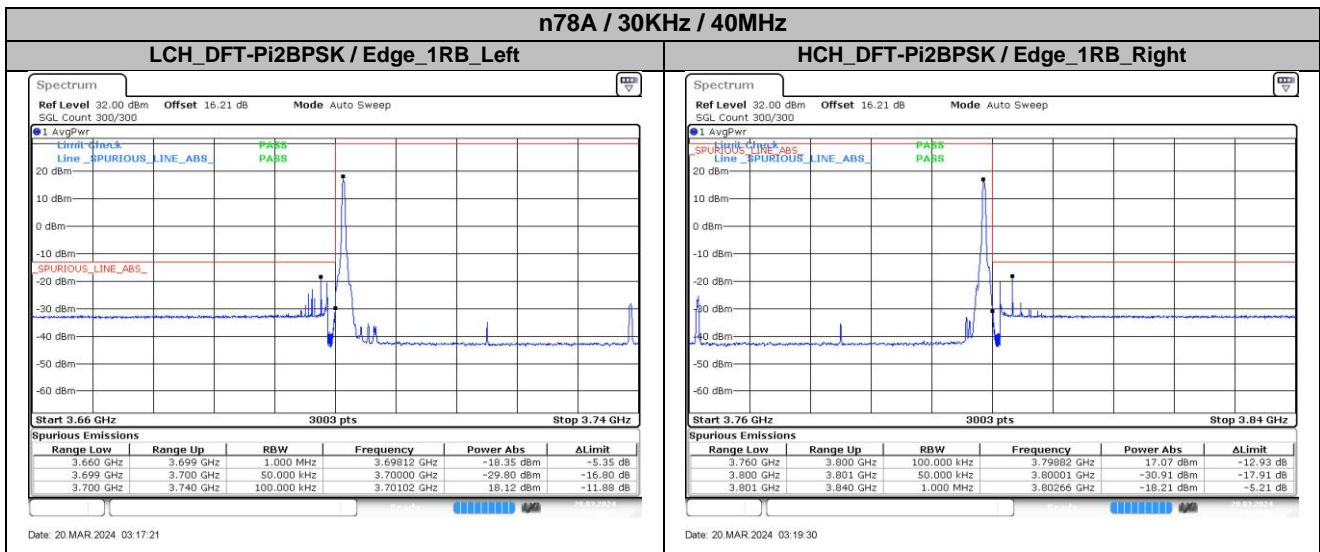
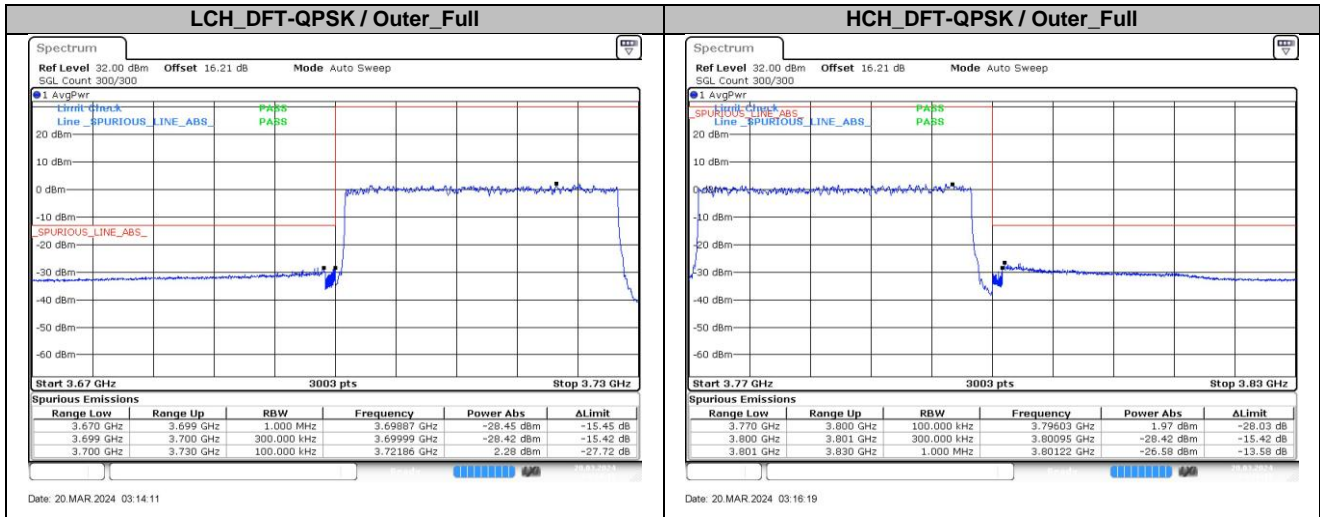


Date: 20 MAR 2024 03:13:55

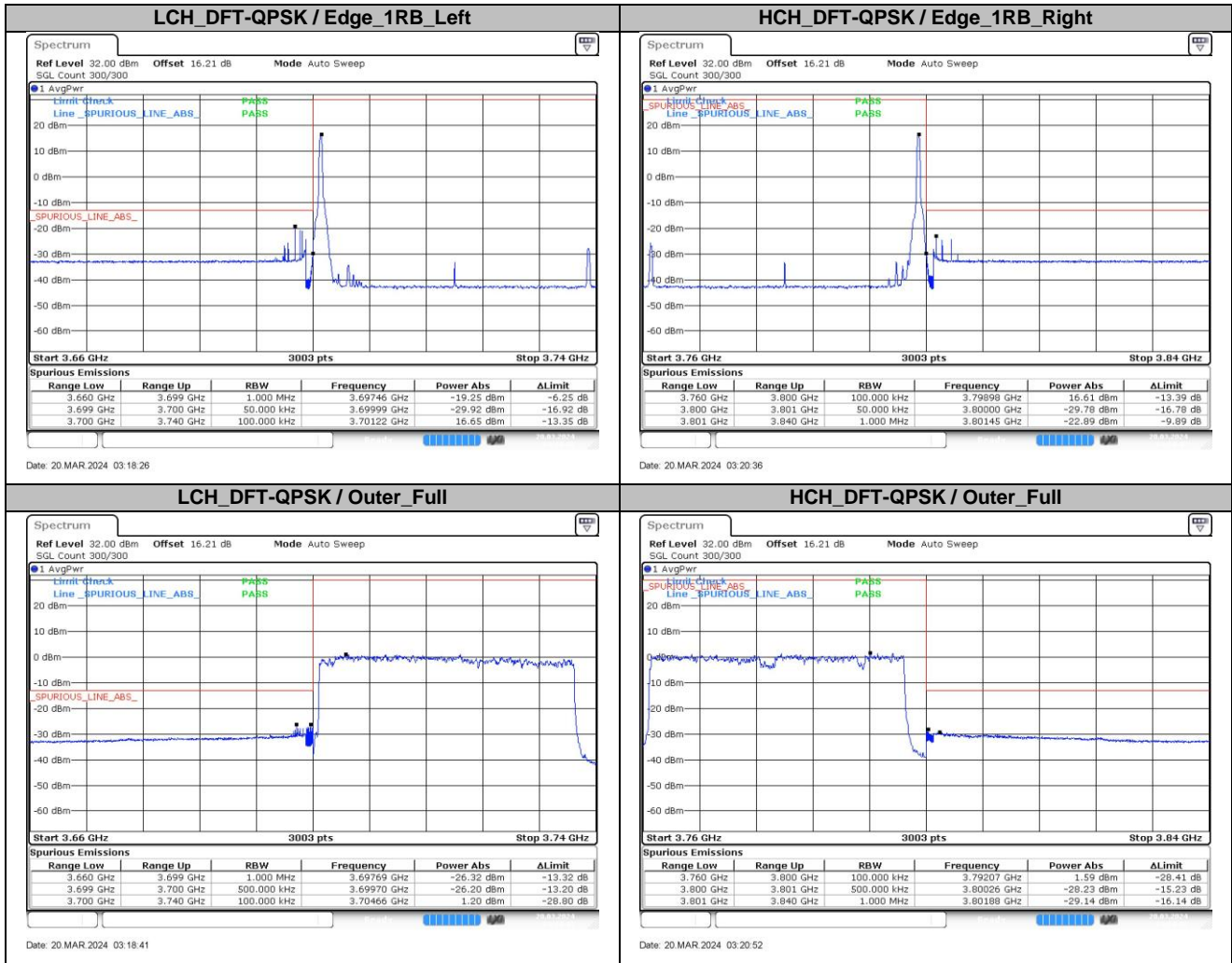
HCH\_DFT-QPSK / Edge\_1RB\_Right

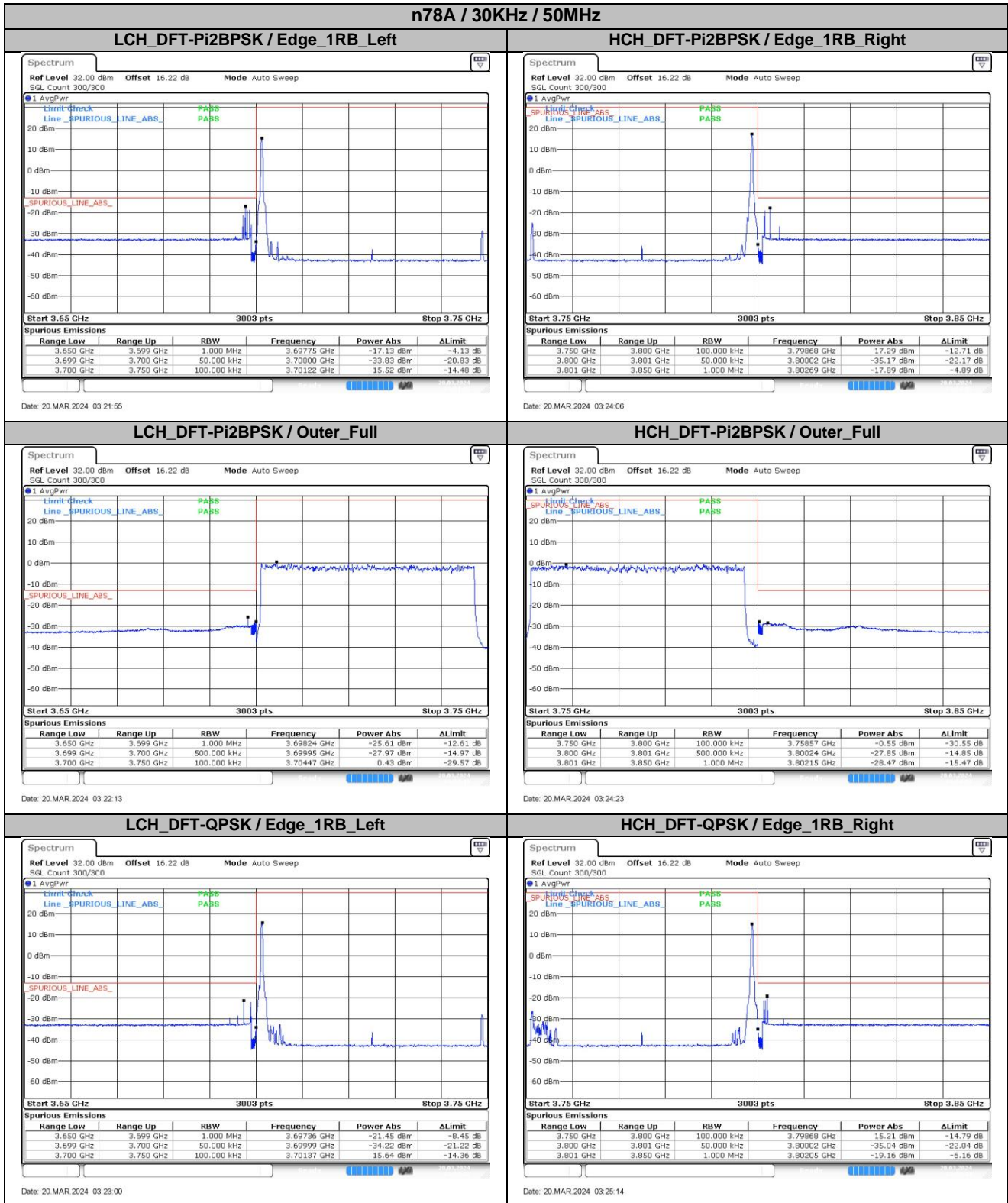


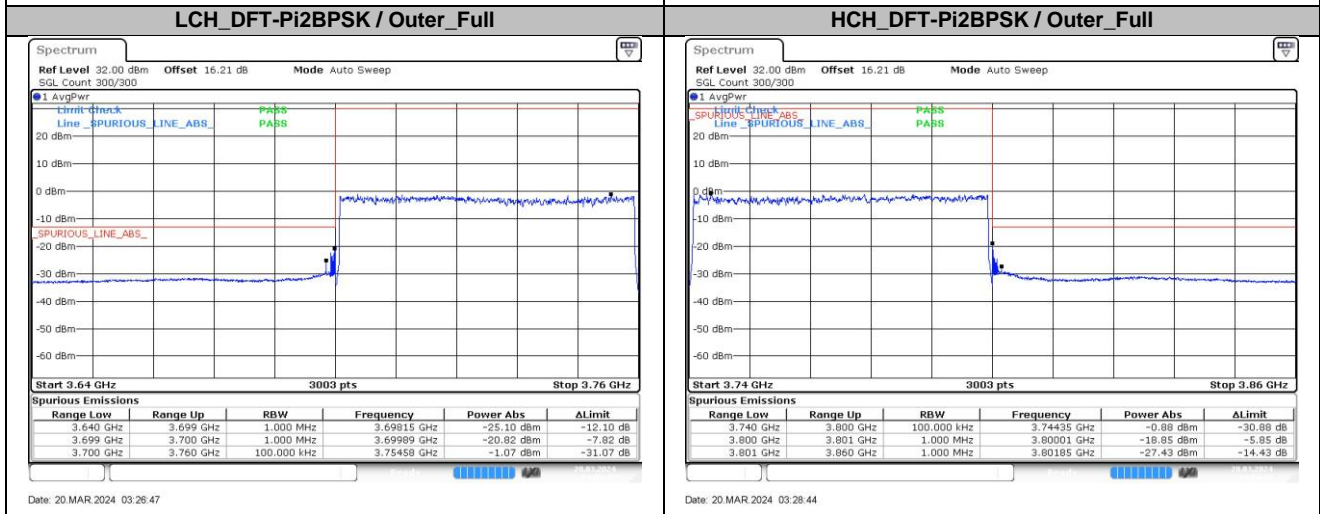
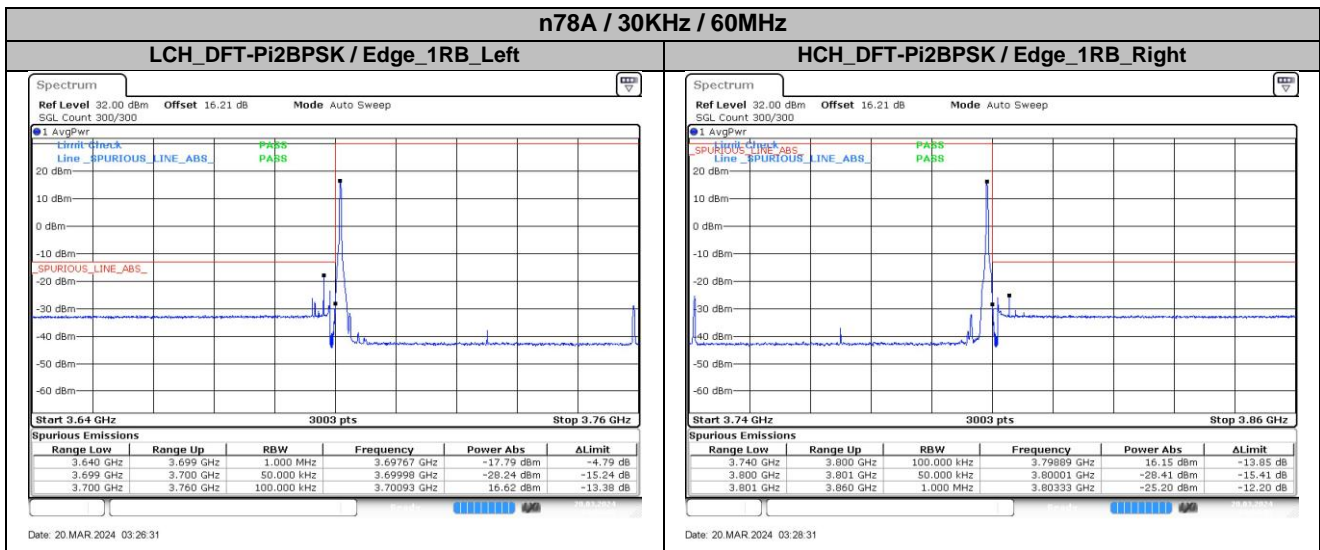
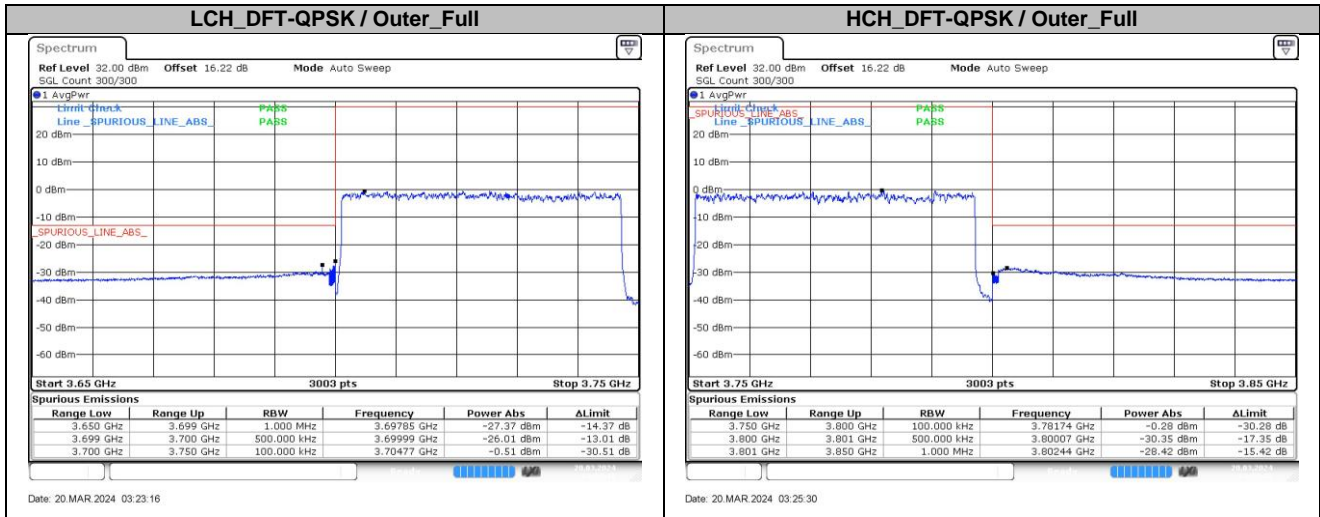
Date: 20 MAR 2024 03:16:04



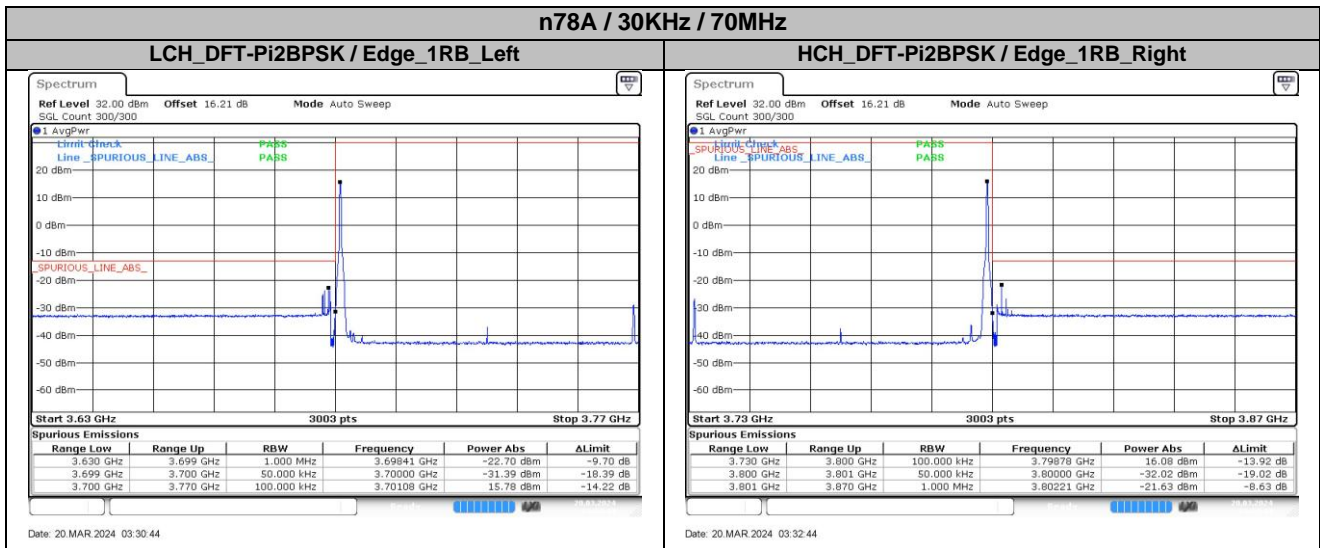
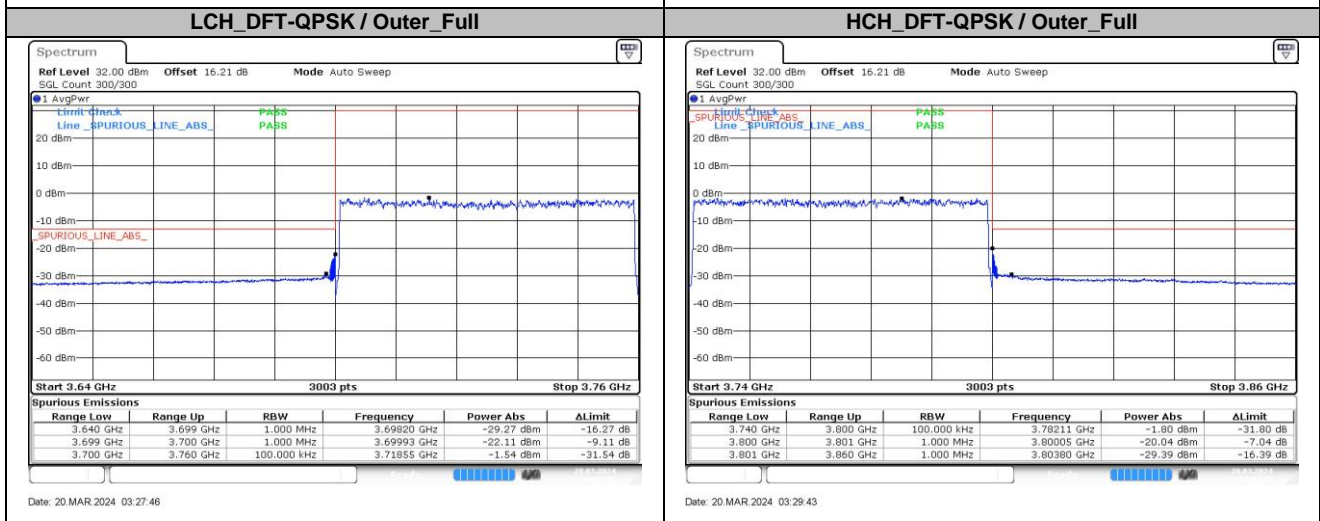
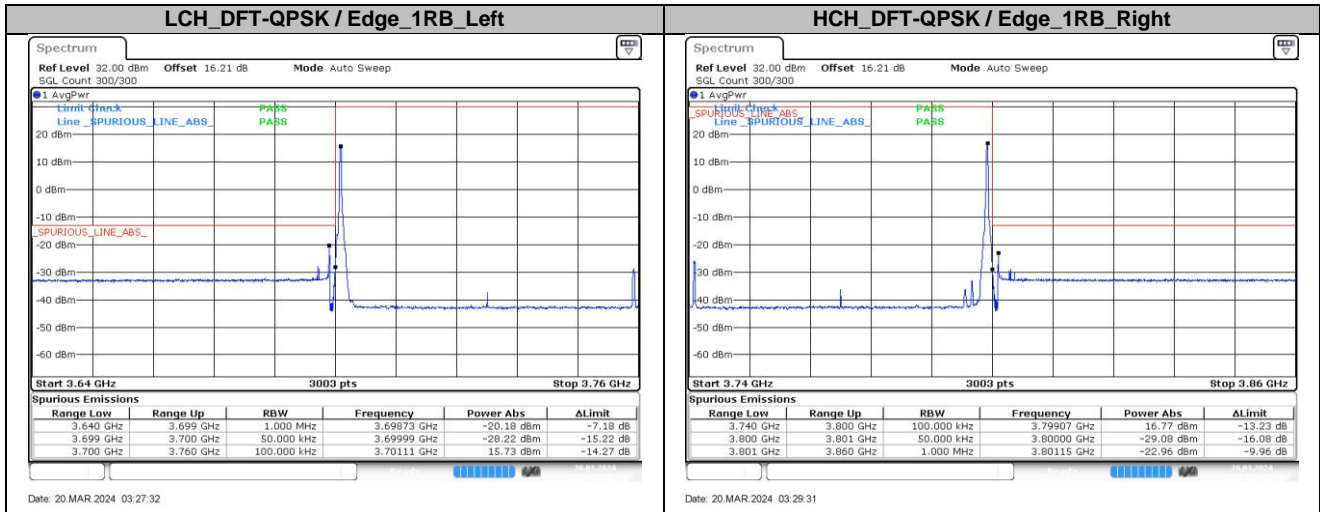


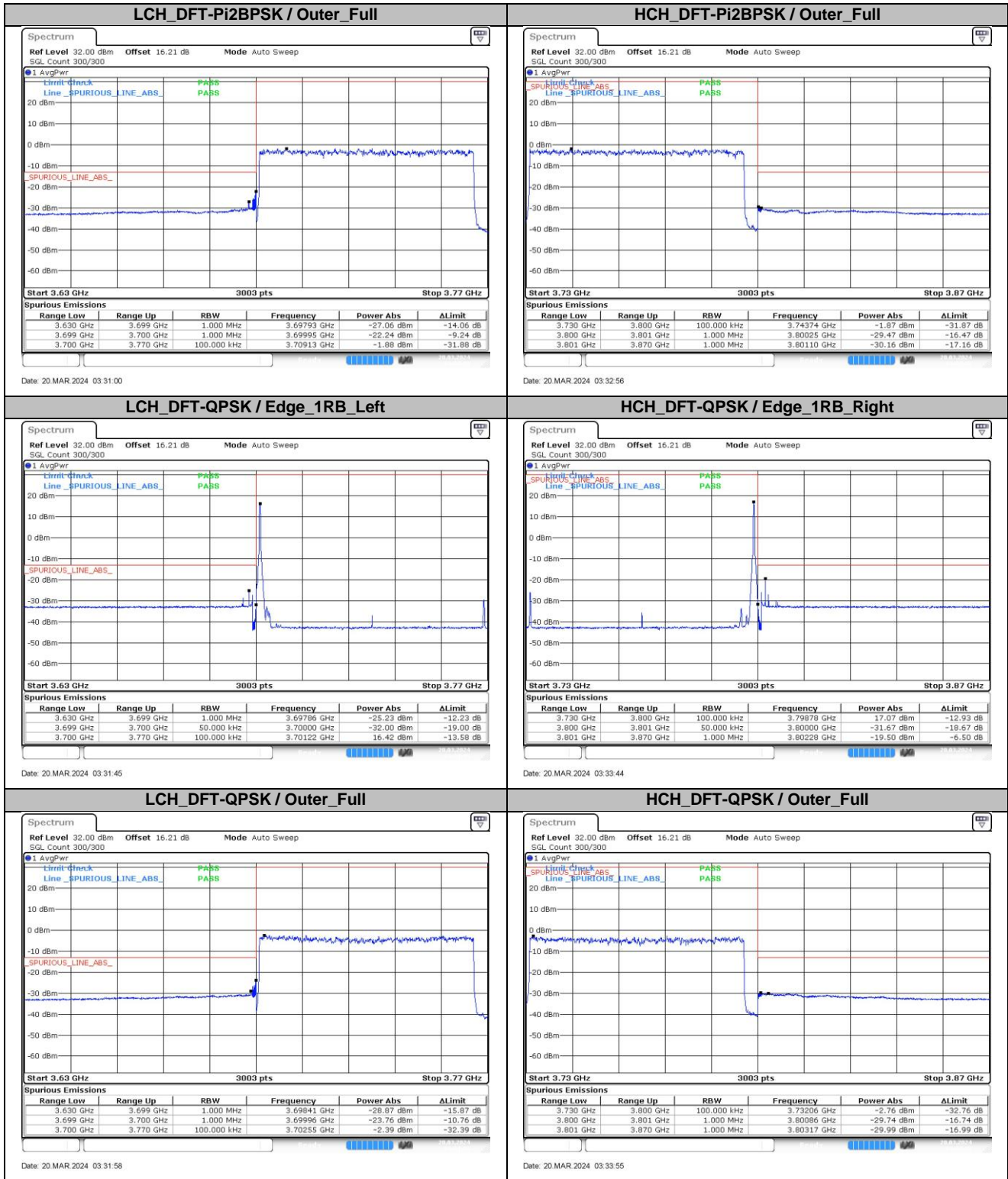






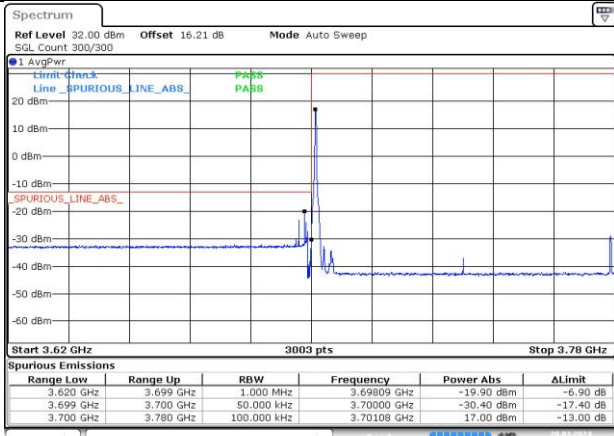






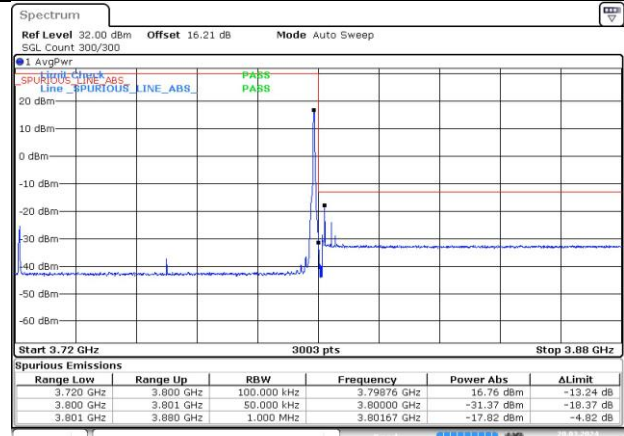
n78A / 30KHz / 80MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



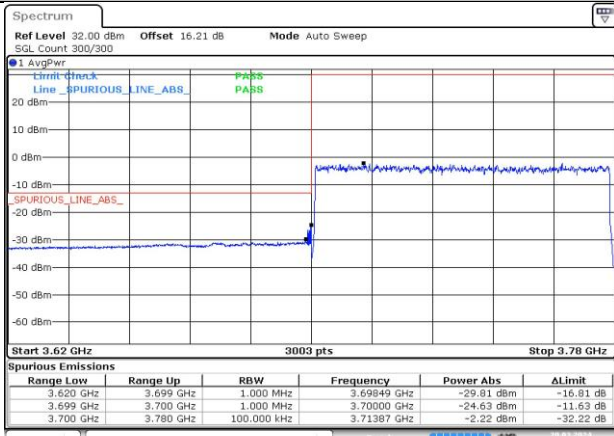
Date: 20 MAR 2024 03:34:56

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Right



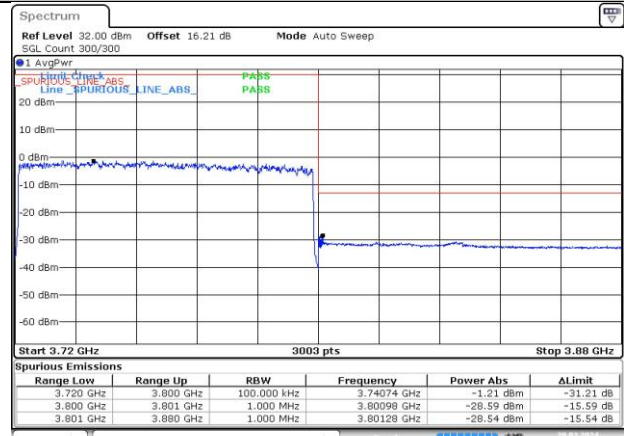
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LCH\_DFT-Pi2BPSK / Outer\_Full



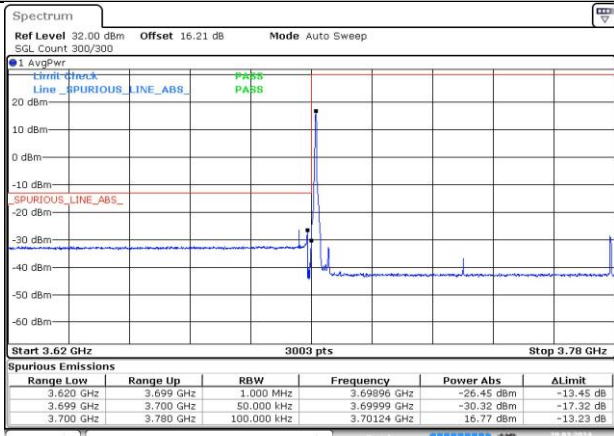
Date: 20 MAR 2024 03:35:12

HCH\_DFT-Pi2BPSK / Outer\_Full



Date: 20 MAR 2024 03:37:09

LCH\_DFT-QPSK / Edge\_1RB\_Left

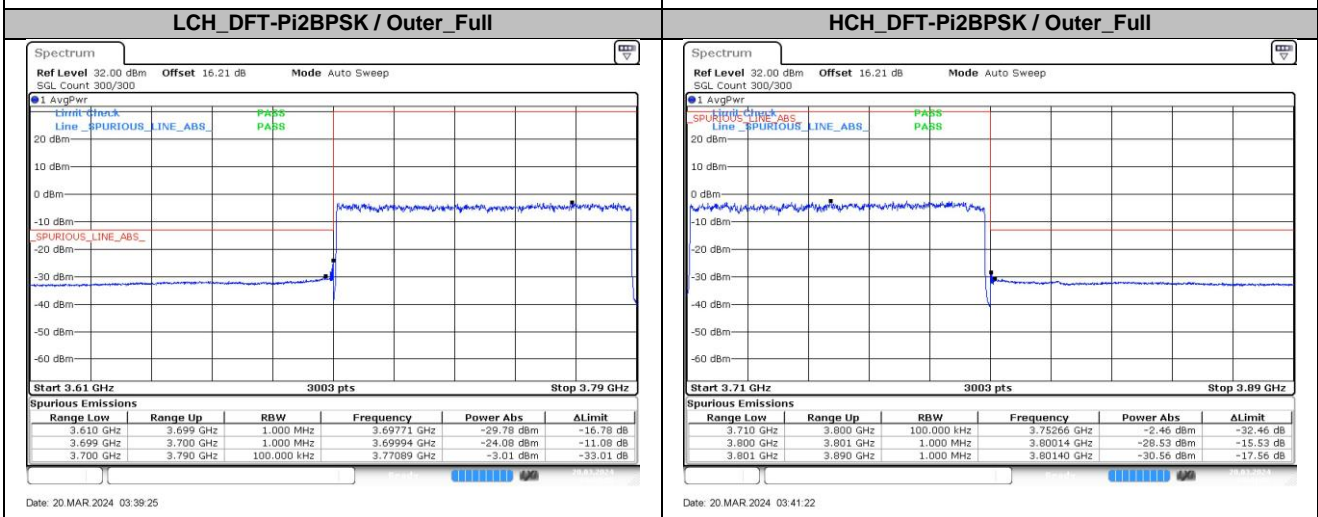
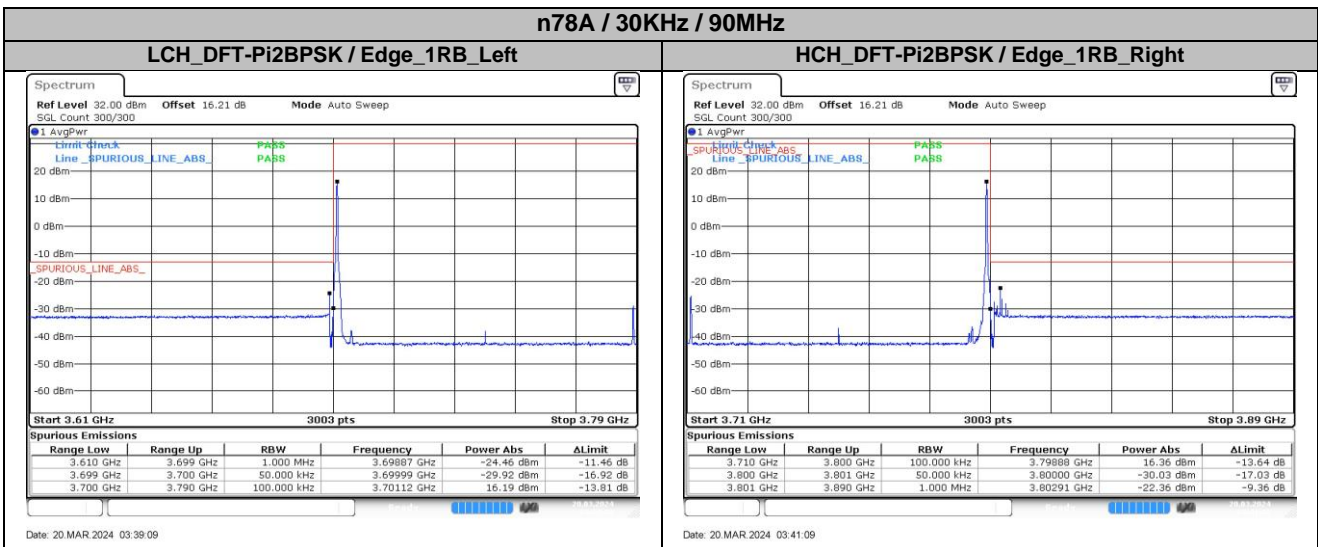
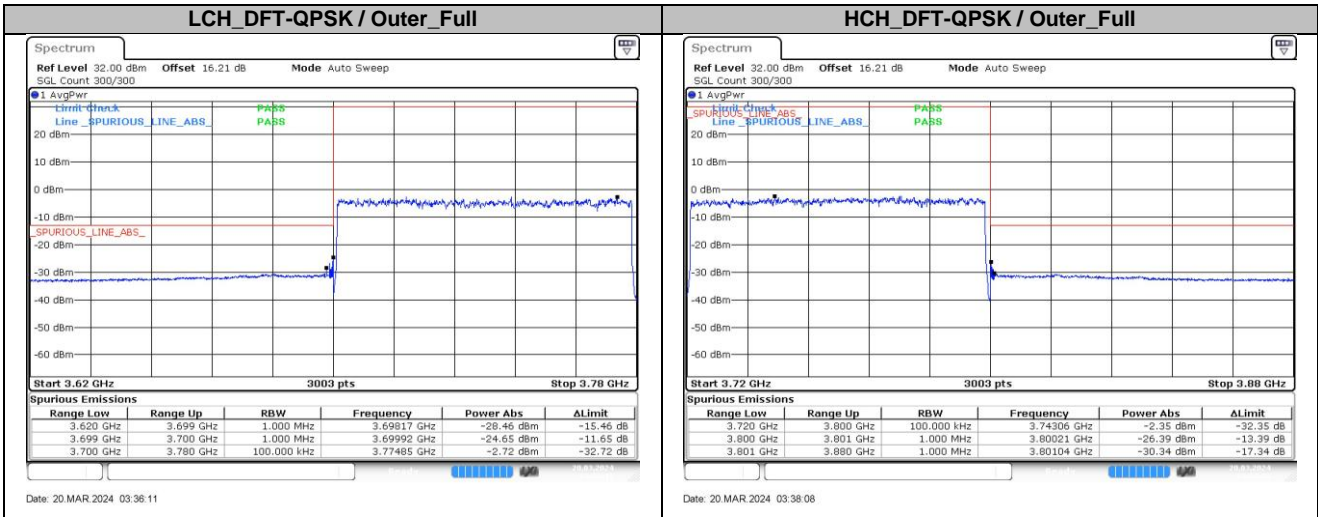


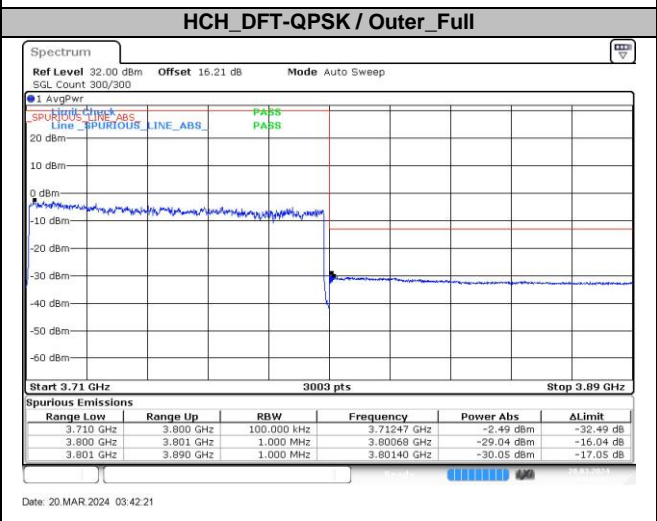
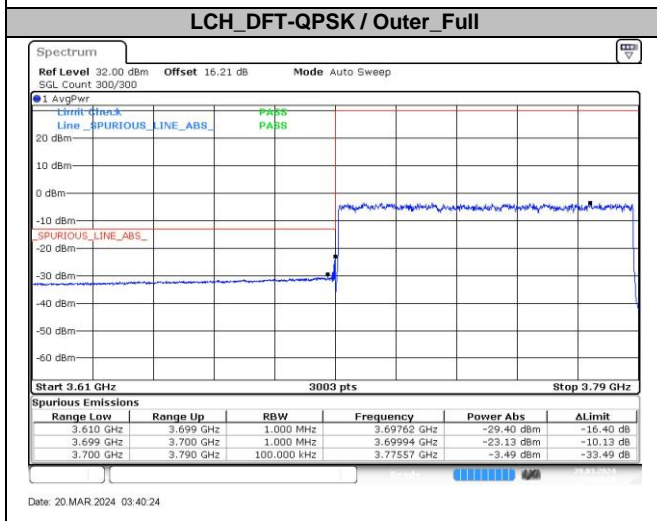
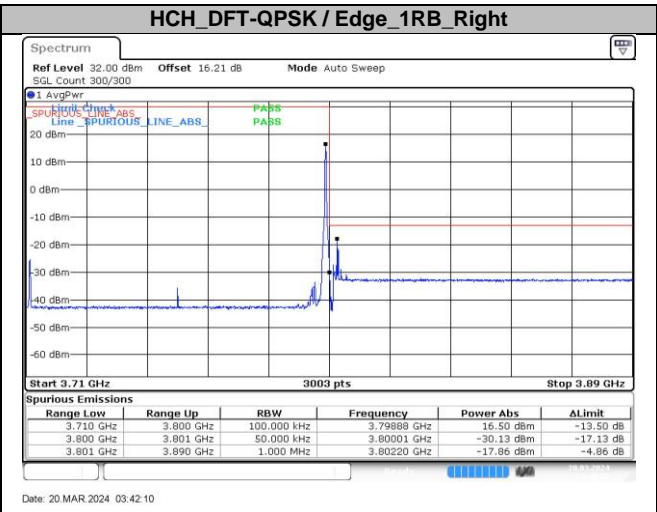
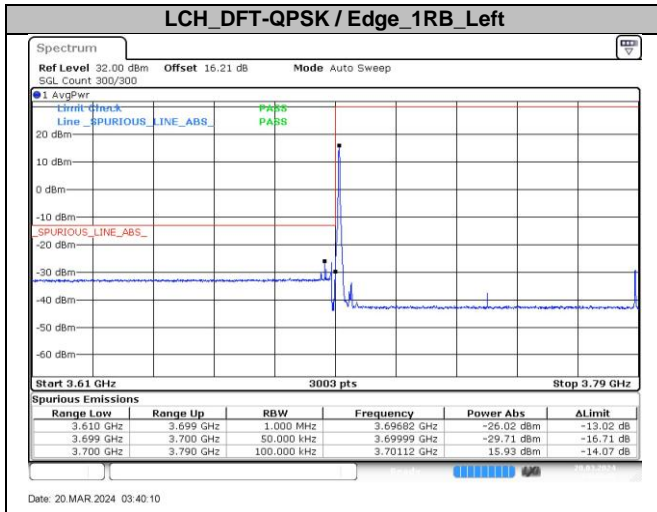
Date: 20 MAR 2024 03:35:57

HCH\_DFT-QPSK / Edge\_1RB\_Right

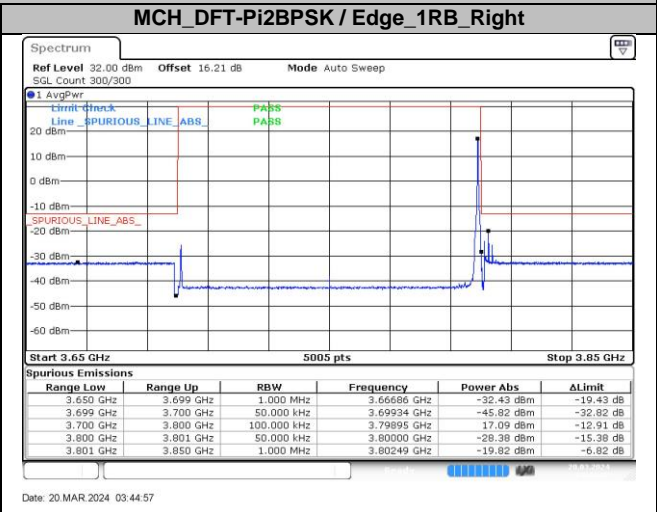
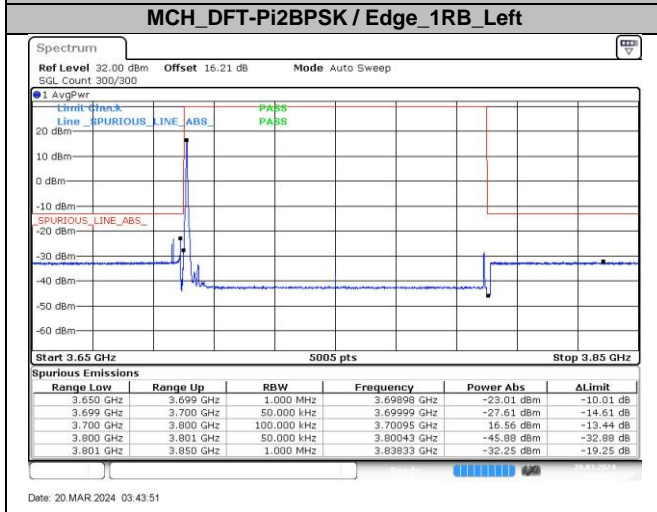


Date: 20 MAR 2024 03:37:57

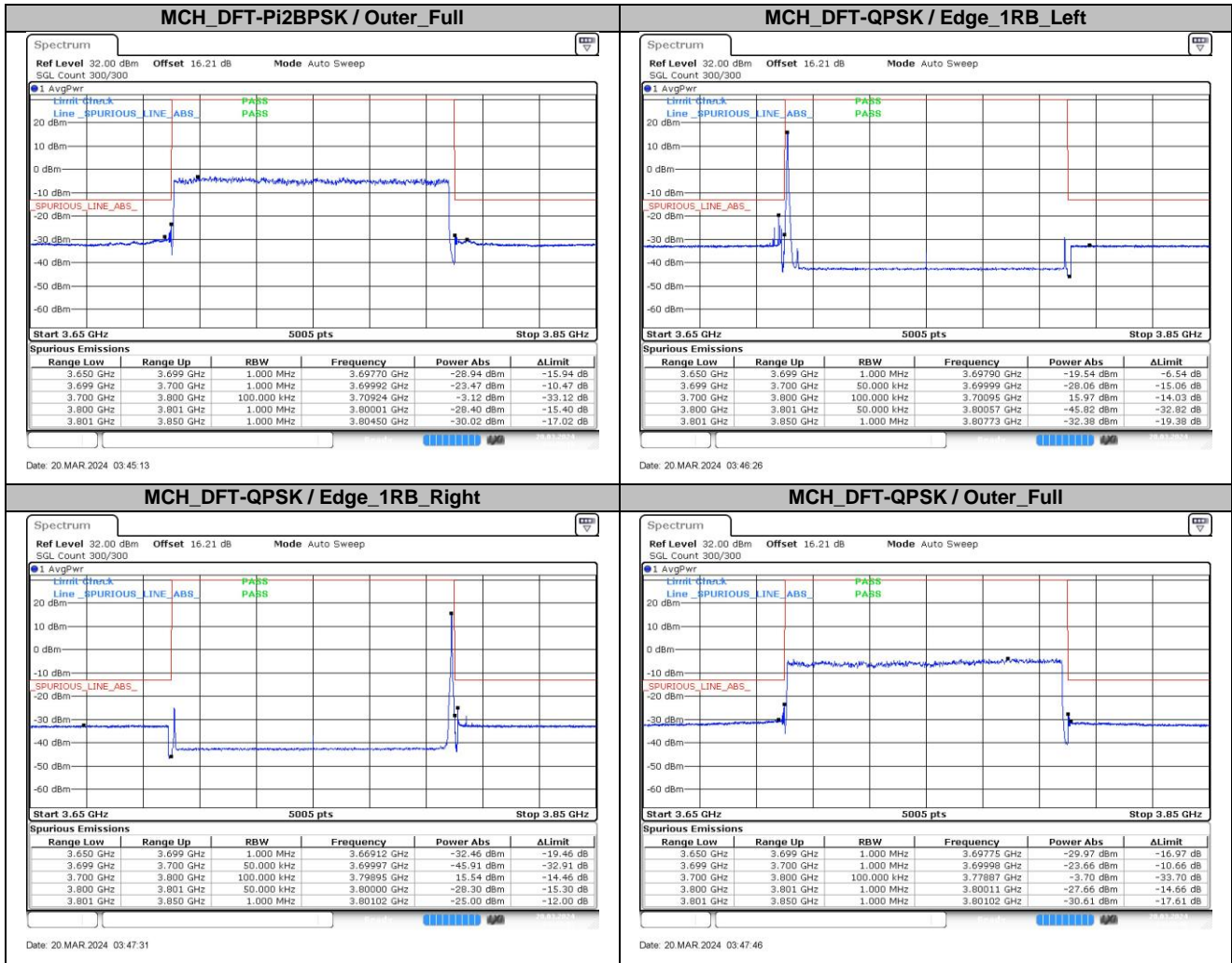




## n78A / 30KHz / 100MHz

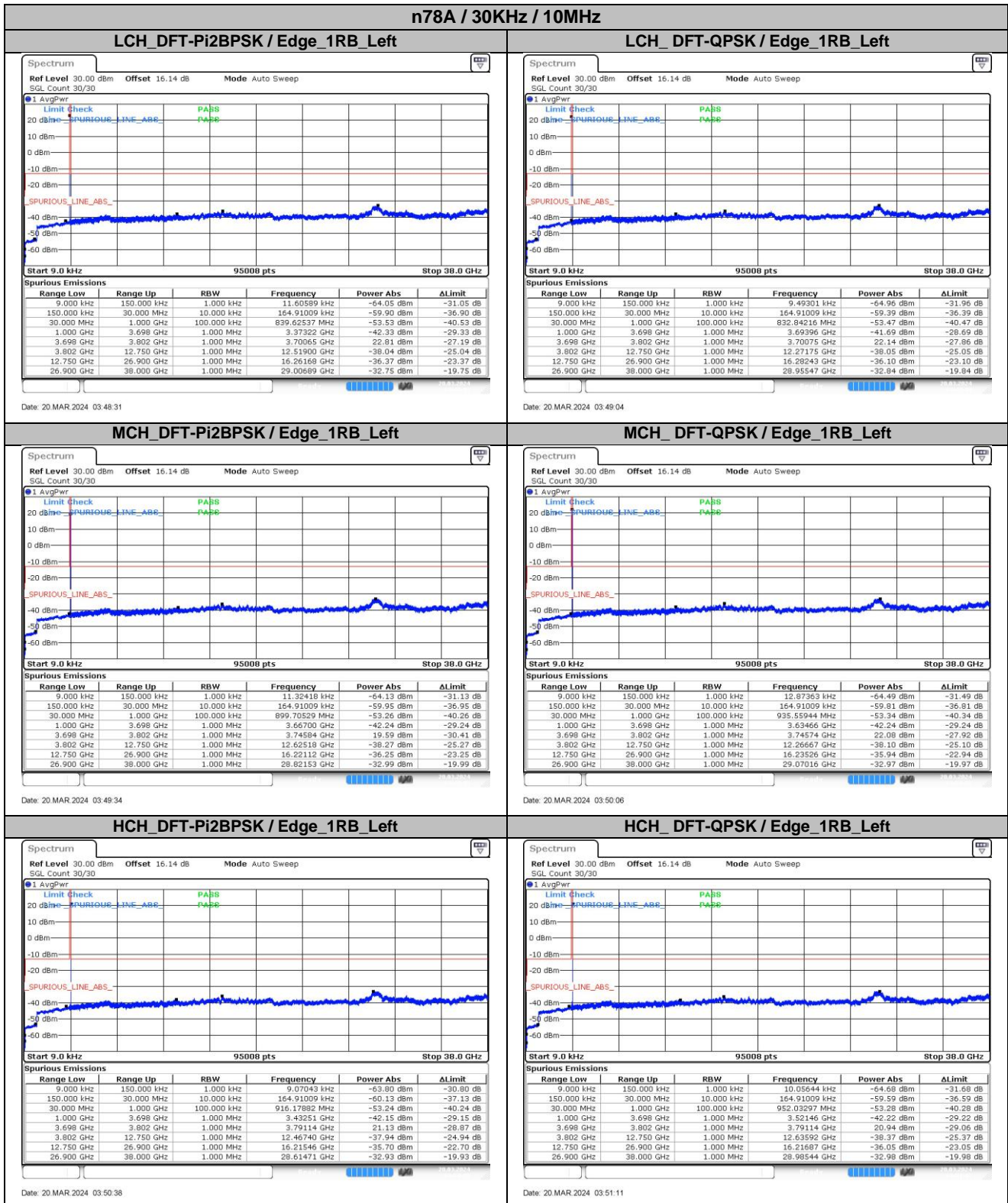






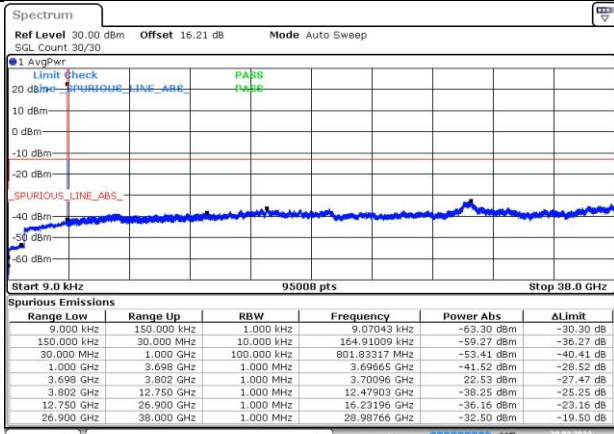
### 6. Conducted Spurious Emission

#### 6.1. Test Plots



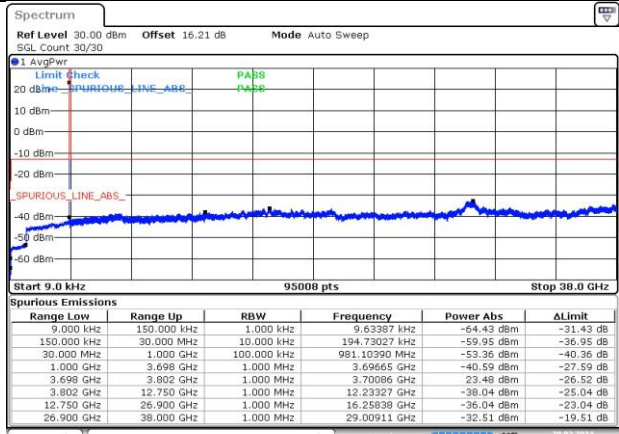
n78A / 30KHz / 15MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



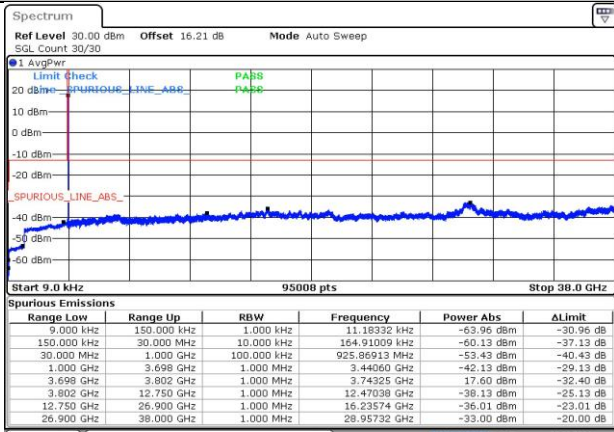
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LCH\_DFT-QPSK / Edge\_1RB\_Left



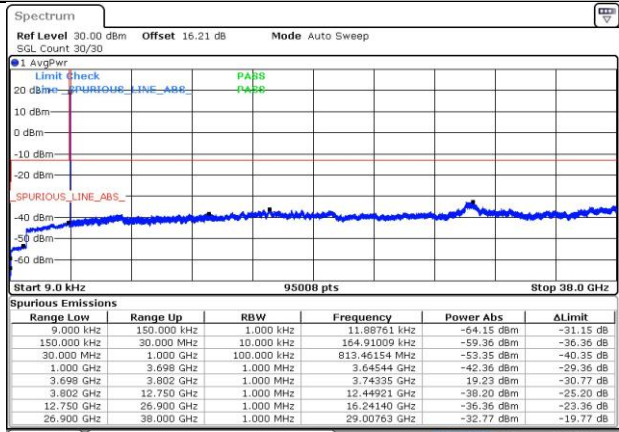
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MCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



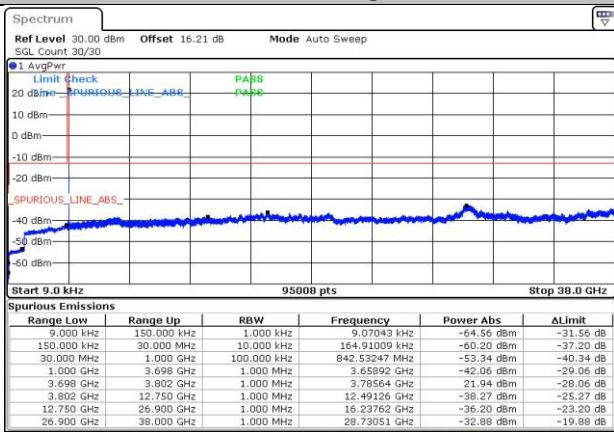
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MCH\_DFT-QPSK / Edge\_1RB\_Left



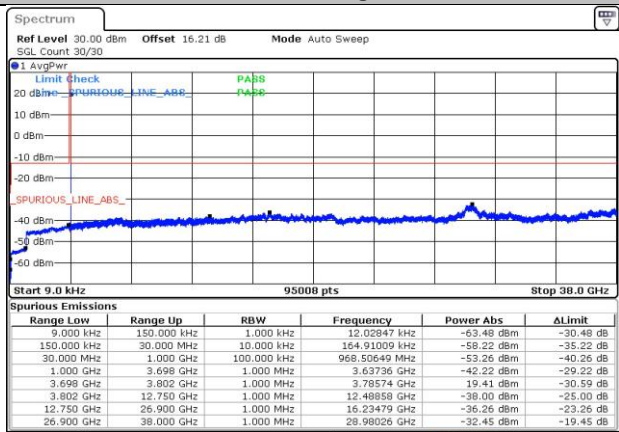
Date: 20.MAR.2024 03:53:42

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 20.MAR.2024 03:54:12

HCH\_DFT-QPSK / Edge\_1RB\_Left



Date: 20.MAR.2024 03:54:44