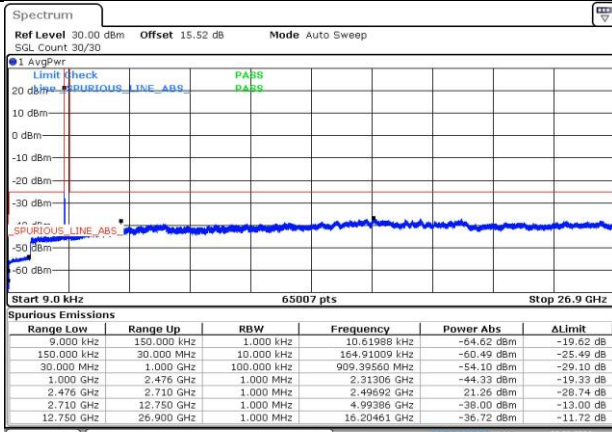


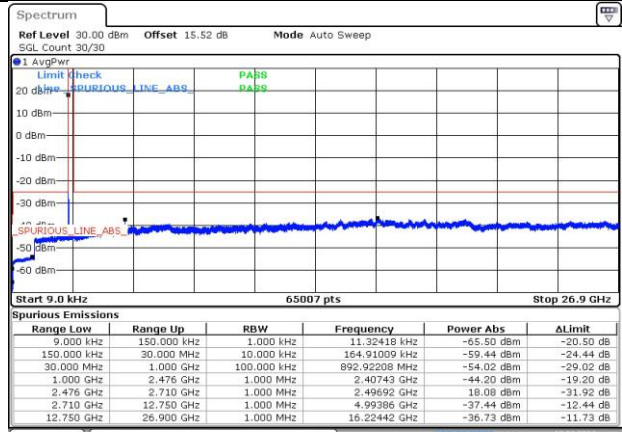
n41A / 30KHz / 15MHz

LCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



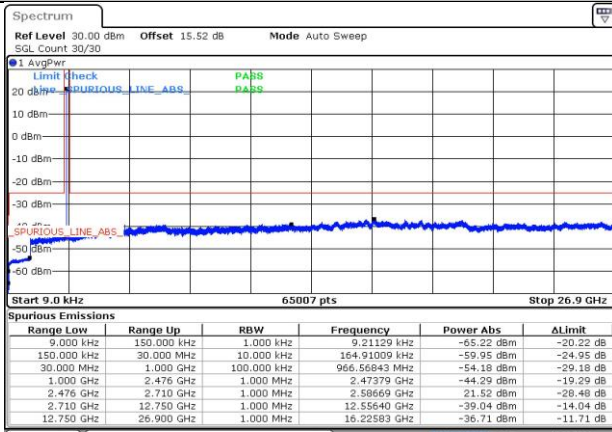
Date: 19 MAR 2024 19:34:06

LCH / DFT-QPSK\_Edge\_1RB\_Left



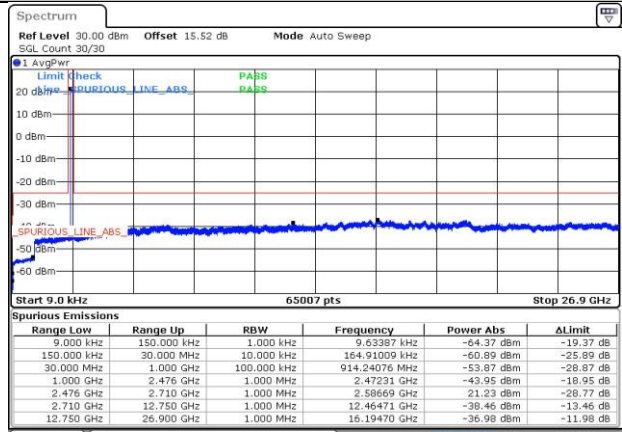
Date: 19 MAR 2024 19:34:35

MCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



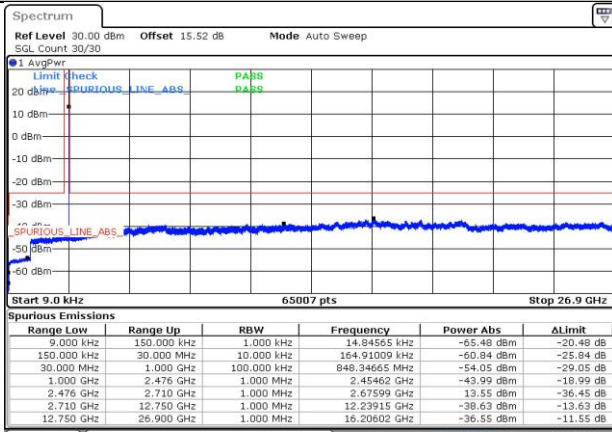
Date: 19 MAR 2024 19:35:00

MCH / DFT-QPSK\_Edge\_1RB\_Left



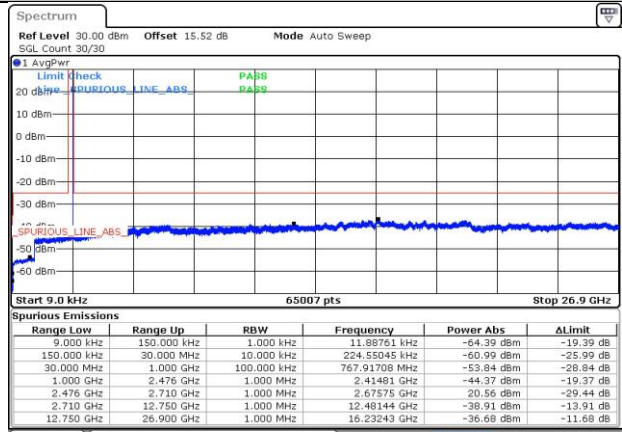
Date: 19 MAR 2024 19:35:30

HCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



Date: 19 MAR 2024 19:35:55

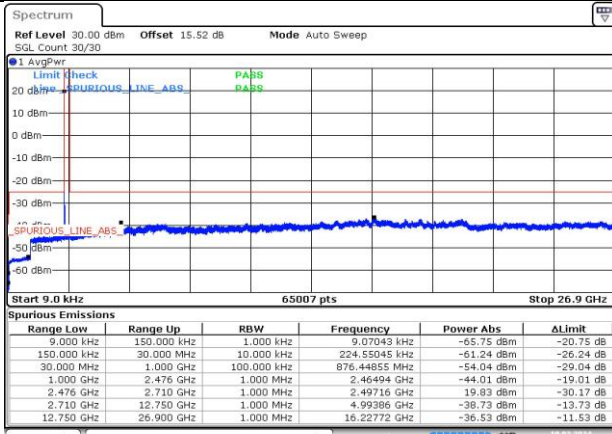
HCH / DFT-QPSK\_Edge\_1RB\_Left



Date: 19 MAR 2024 19:36:24

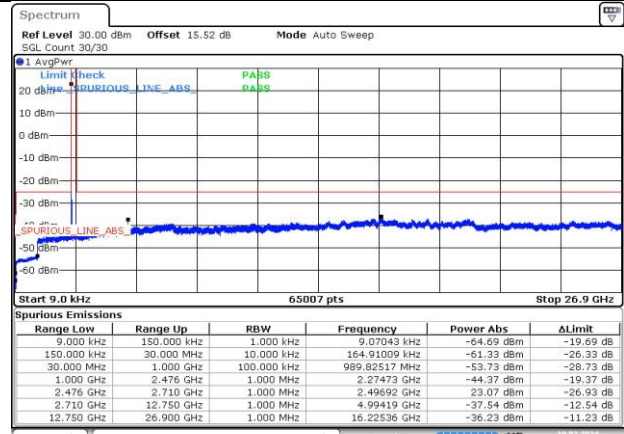
n41A / 30KHz / 20MHz

LCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



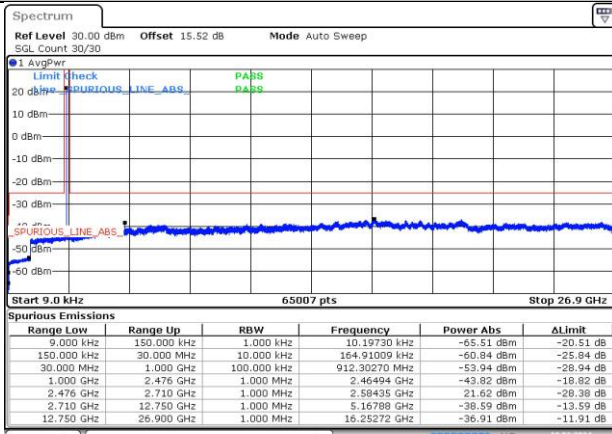
Date: 19 MAR 2024 19:02:56

LCH / DFT-QPSK\_Edge\_1RB\_Left



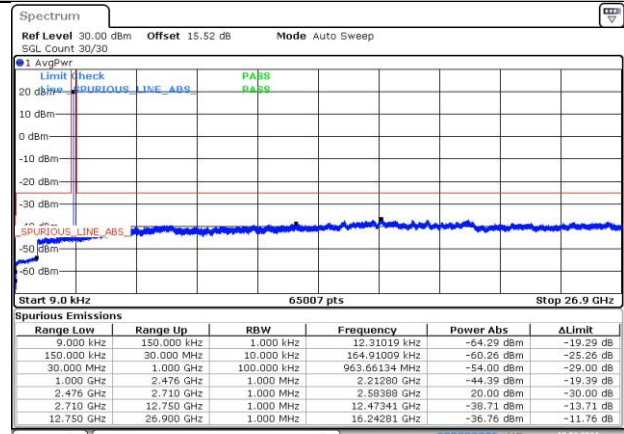
Date: 19 MAR 2024 19:03:26

MCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



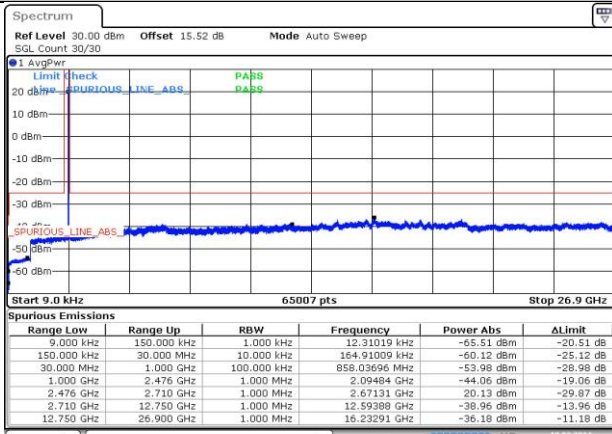
Date: 19 MAR 2024 19:03:56

MCH / DFT-QPSK\_Edge\_1RB\_Left



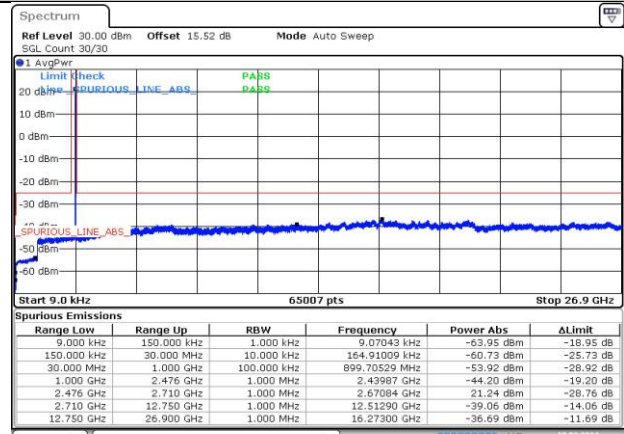
Date: 19 MAR 2024 19:04:25

HCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



Date: 19 MAR 2024 19:04:55

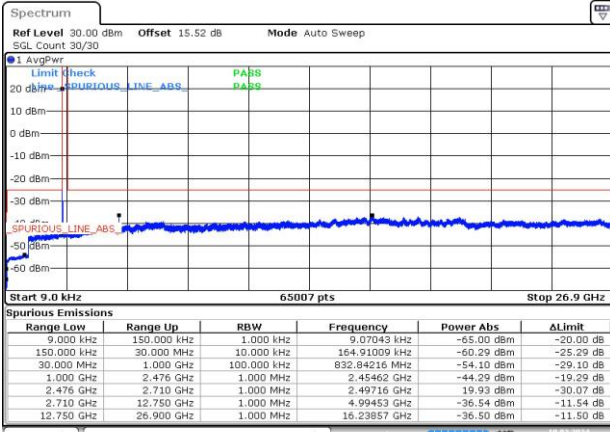
HCH / DFT-QPSK\_Edge\_1RB\_Left



Date: 19 MAR 2024 19:05:39

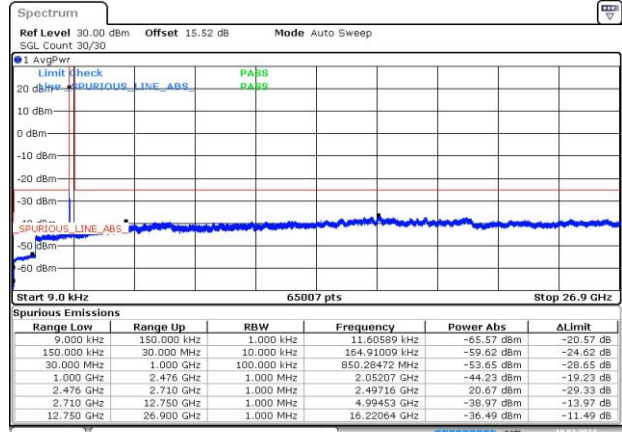
n41A / 30KHz / 30MHz

LCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



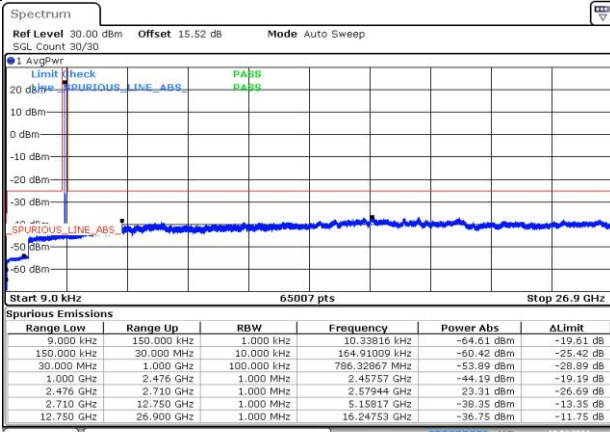
Date: 19 MAR 2024 19:06:23

LCH / DFT-QPSK\_Edge\_1RB\_Left



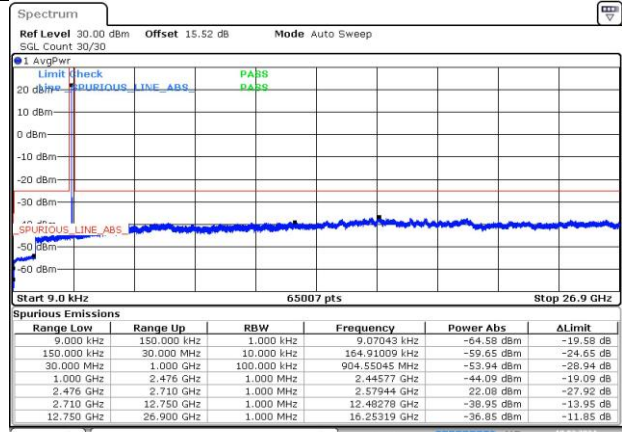
Date: 19 MAR 2024 19:06:53

MCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



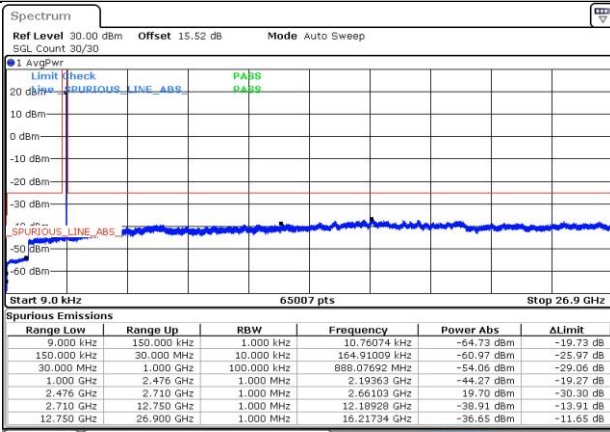
Date: 19 MAR 2024 19:07:19

MCH / DFT-QPSK\_Edge\_1RB\_Left



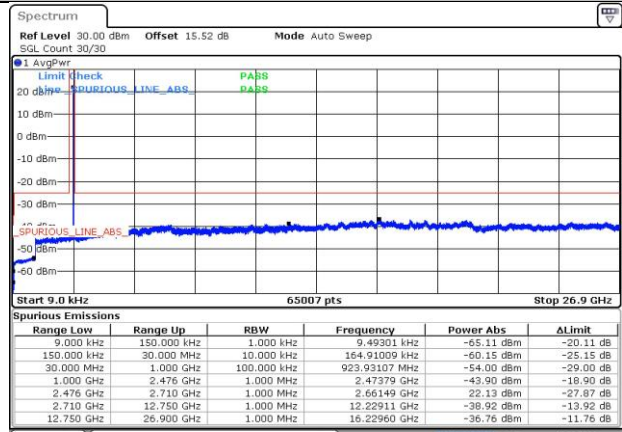
Date: 19 MAR 2024 19:07:48

HCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



Date: 19 MAR 2024 19:08:14

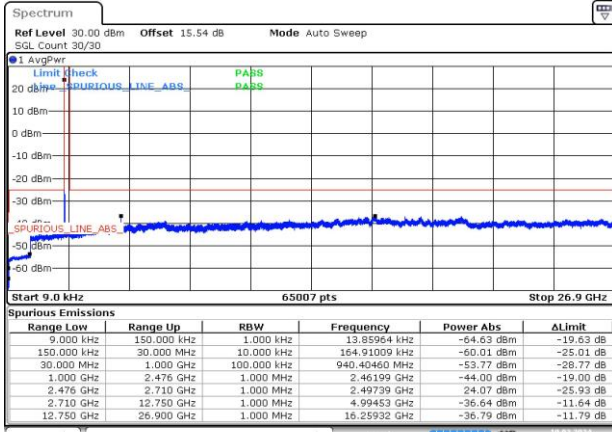
HCH / DFT-QPSK\_Edge\_1RB\_Left



Date: 19 MAR 2024 19:08:44

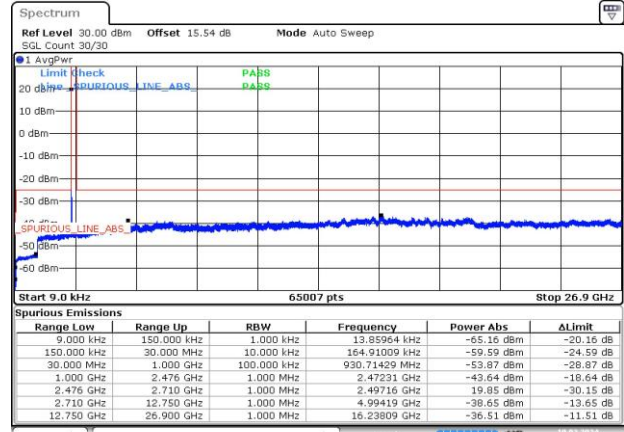
n41A / 30KHz / 40MHz

LCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



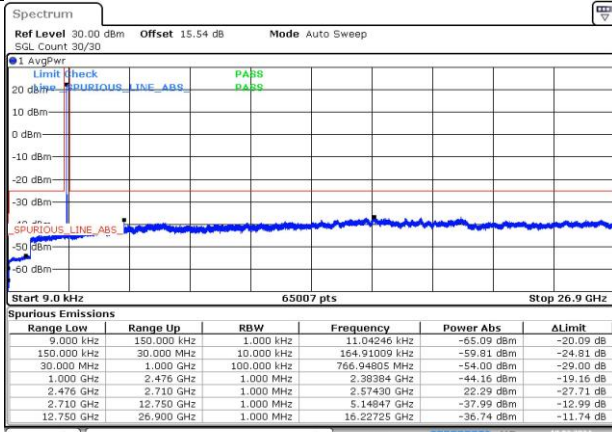
Date: 19 MAR 2024 19:09:29

LCH / DFT-QPSK\_Edge\_1RB\_Left



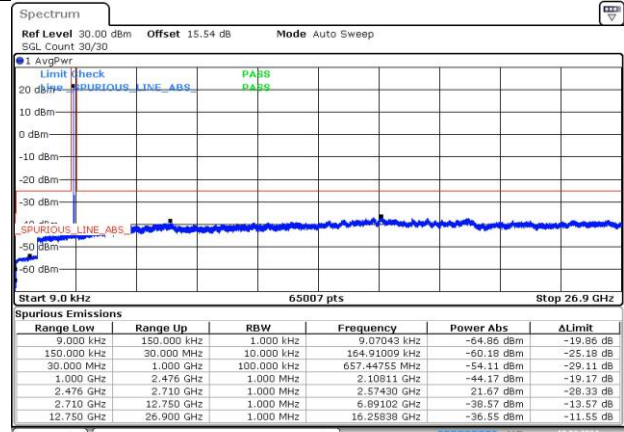
Date: 19 MAR 2024 19:09:59

MCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



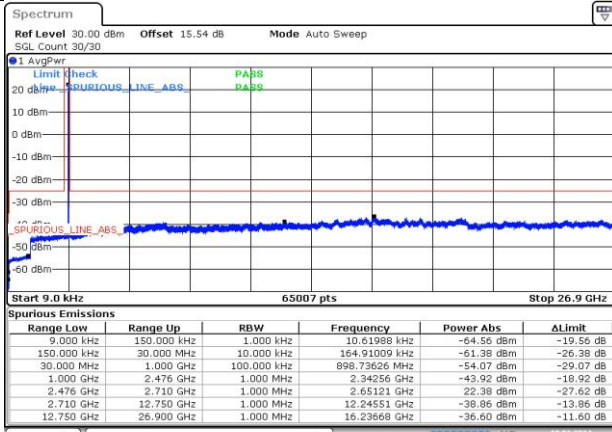
Date: 19 MAR 2024 19:10:29

MCH / DFT-QPSK\_Edge\_1RB\_Left



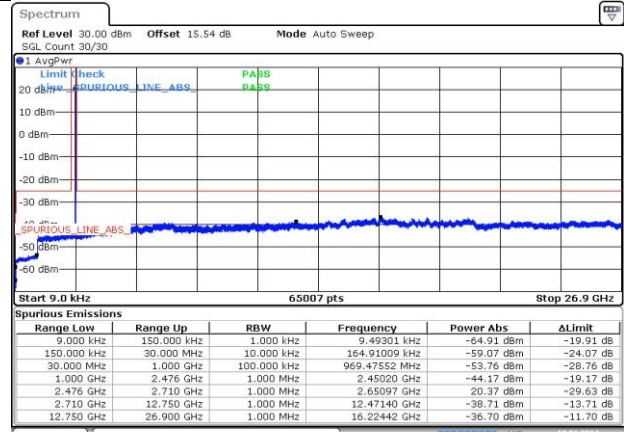
Date: 19 MAR 2024 19:10:58

HCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



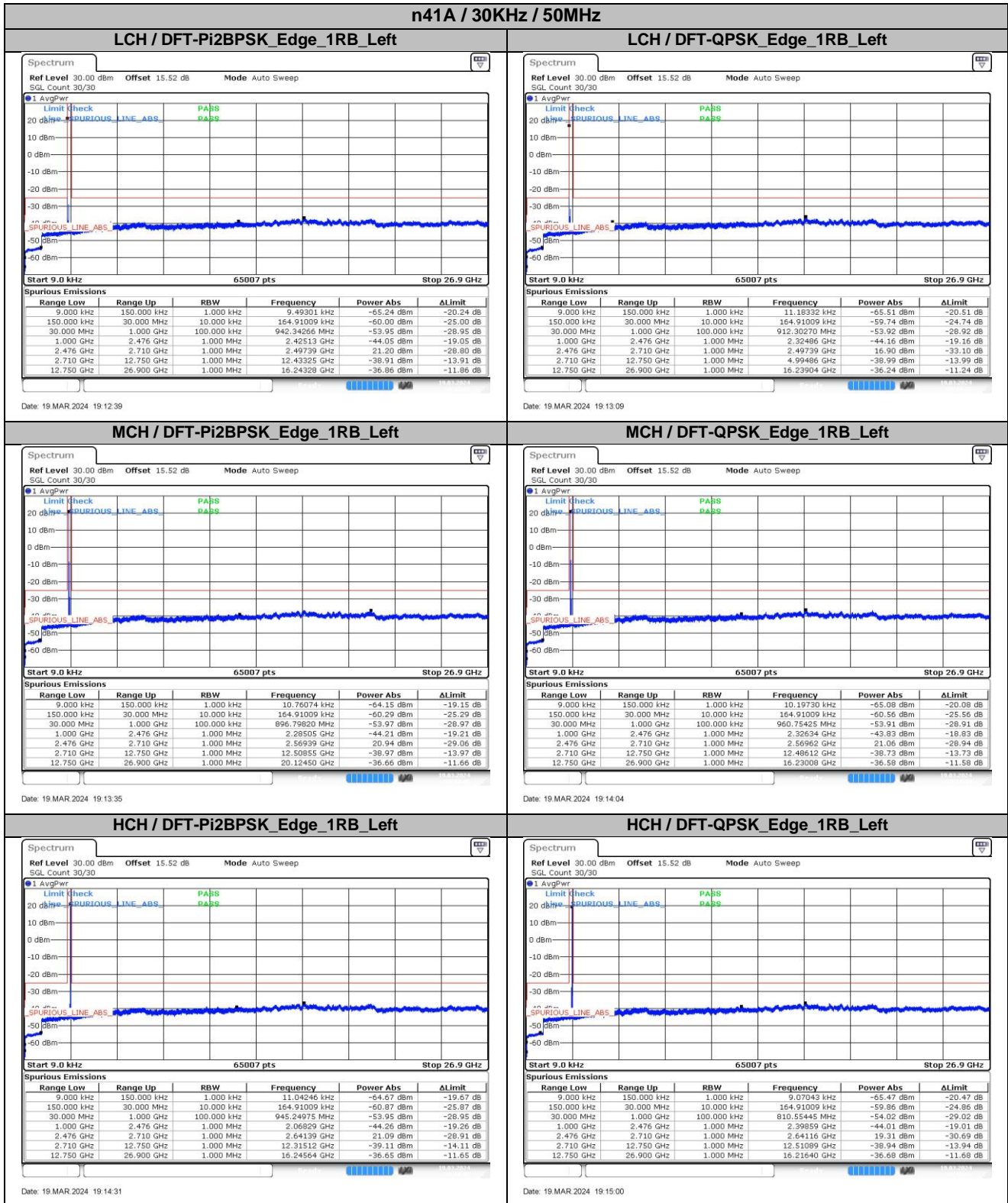
Date: 19 MAR 2024 19:11:24

HCH / DFT-QPSK\_Edge\_1RB\_Left



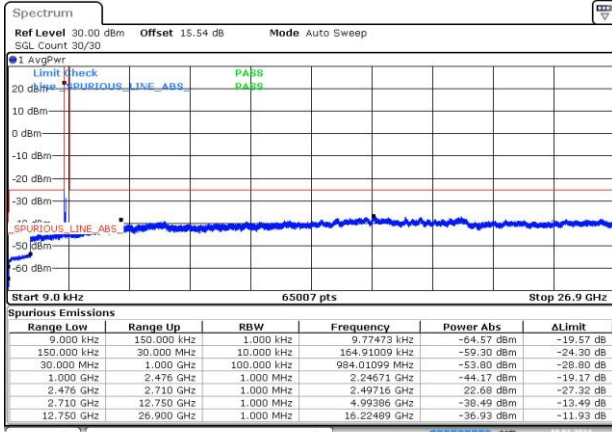
Date: 19 MAR 2024 19:11:54





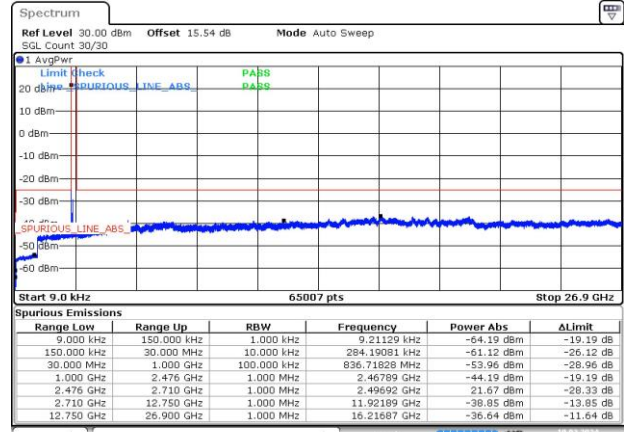
n41A / 30KHz / 60MHz

LCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



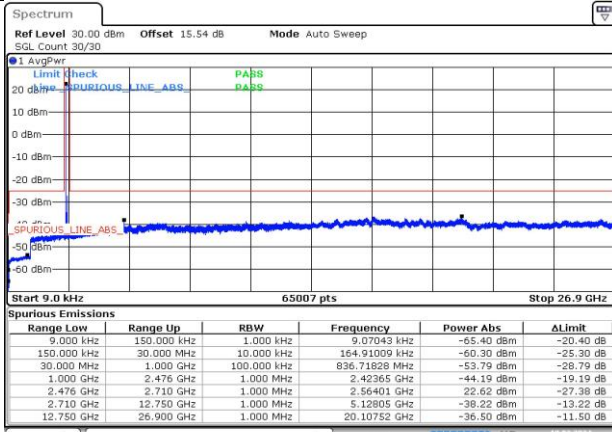
Date: 19 MAR 2024 19:15:45

LCH / DFT-QPSK\_Edge\_1RB\_Left



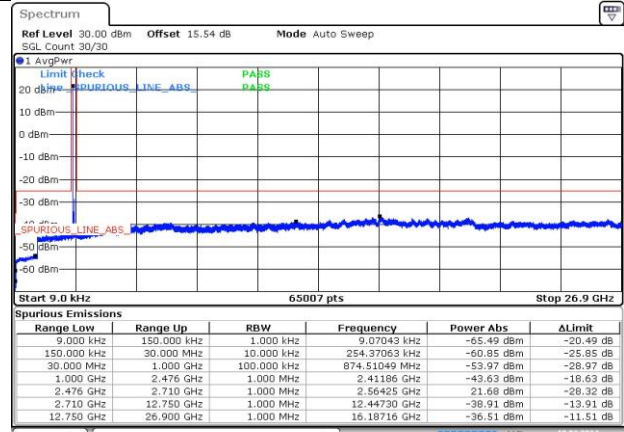
Date: 19 MAR 2024 19:16:14

MCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



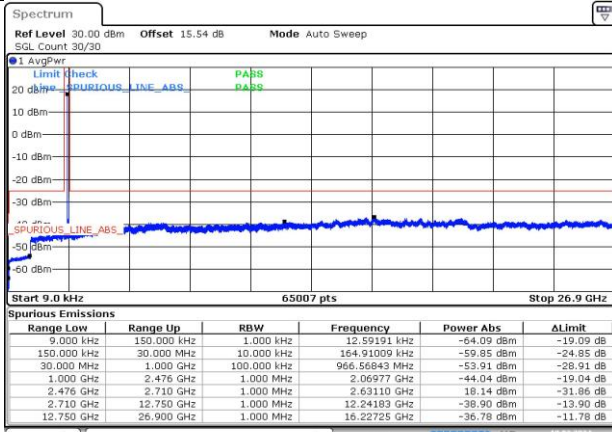
Date: 19 MAR 2024 19:16:44

MCH / DFT-QPSK\_Edge\_1RB\_Left



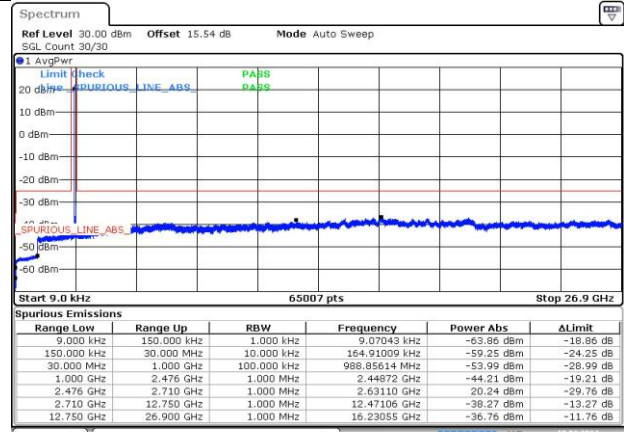
Date: 19 MAR 2024 19:17:13

HCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



Date: 19 MAR 2024 19:17:39

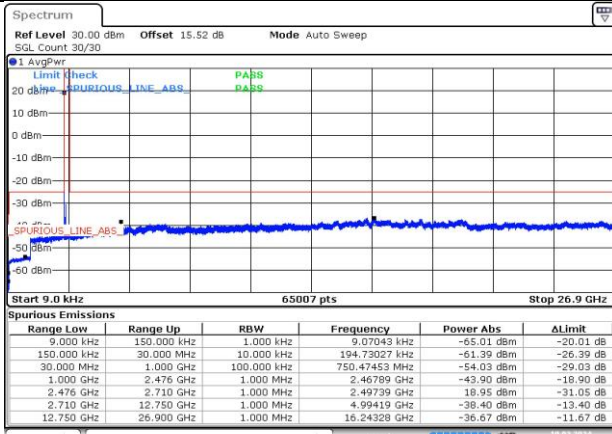
HCH / DFT-QPSK\_Edge\_1RB\_Left



Date: 19 MAR 2024 19:18:08

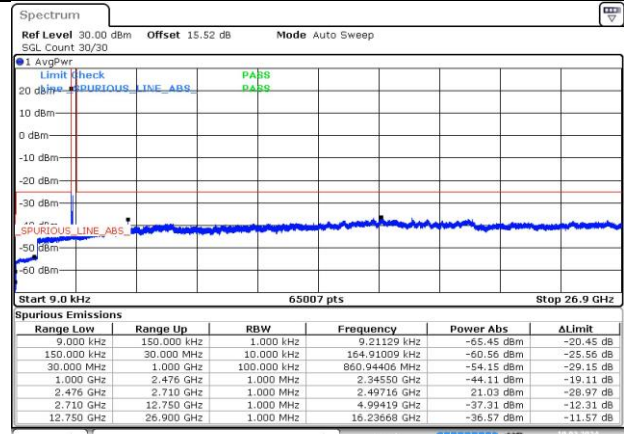
n41A / 30KHz / 70MHz

LCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



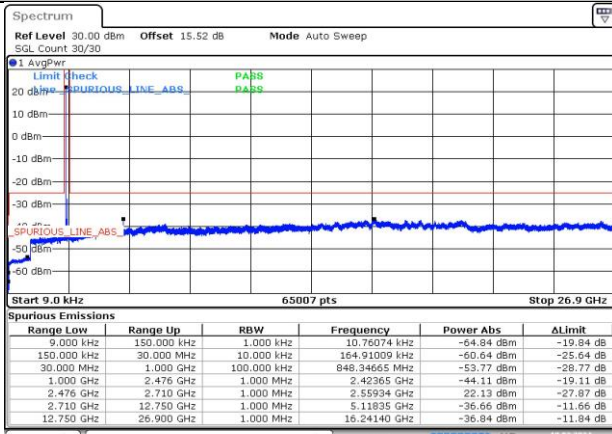
Date: 19 MAR 2024 19:18:54

LCH / DFT-QPSK\_Edge\_1RB\_Left



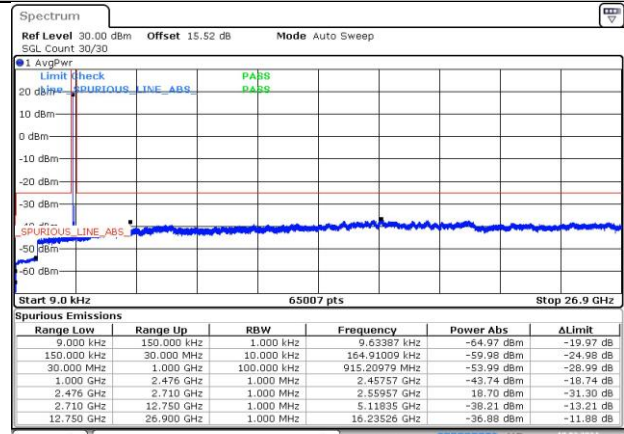
Date: 19 MAR 2024 19:19:23

MCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



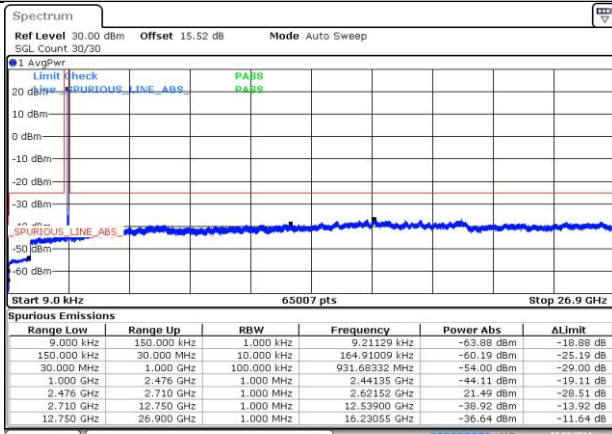
Date: 19 MAR 2024 19:19:49

MCH / DFT-QPSK\_Edge\_1RB\_Left



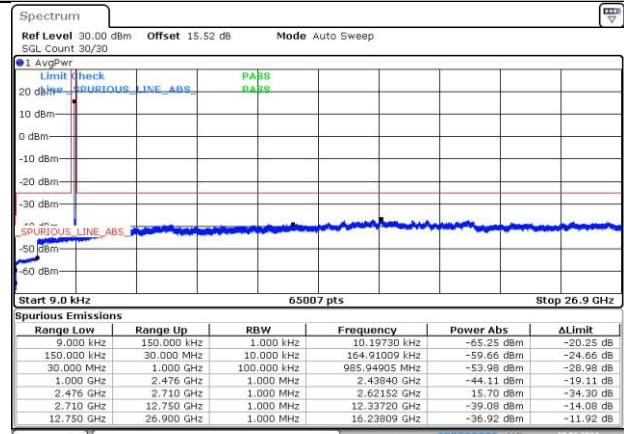
Date: 19 MAR 2024 19:20:18

HCH / DFT-Pi2BPSK\_Edge\_1RB\_Left

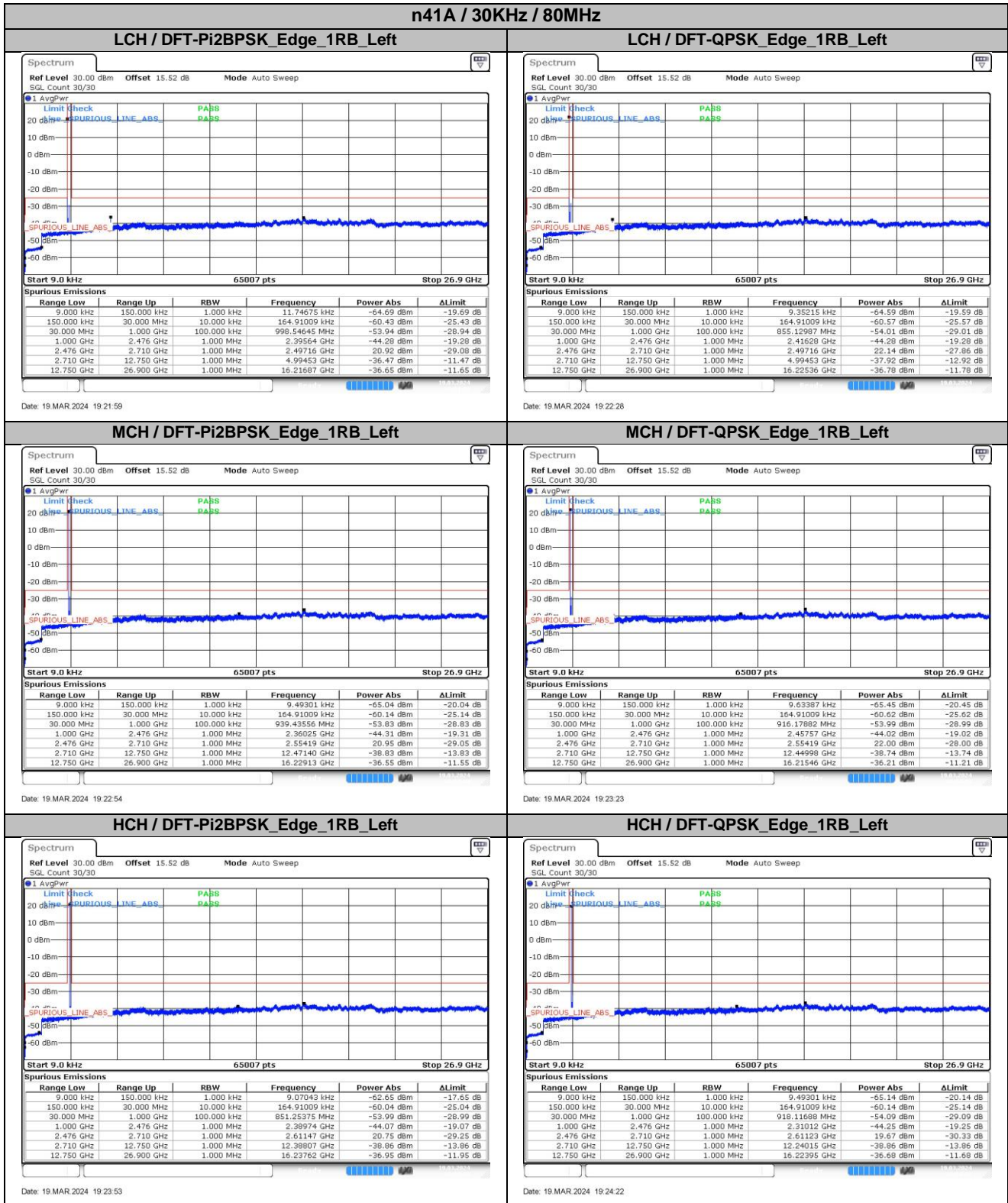


Date: 19 MAR 2024 19:20:44

HCH / DFT-QPSK\_Edge\_1RB\_Left



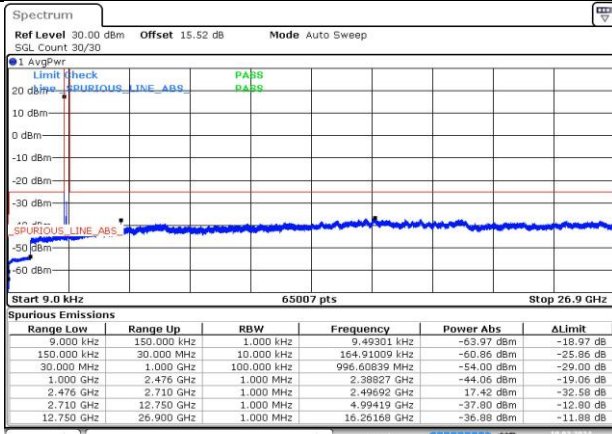
Date: 19 MAR 2024 19:21:14





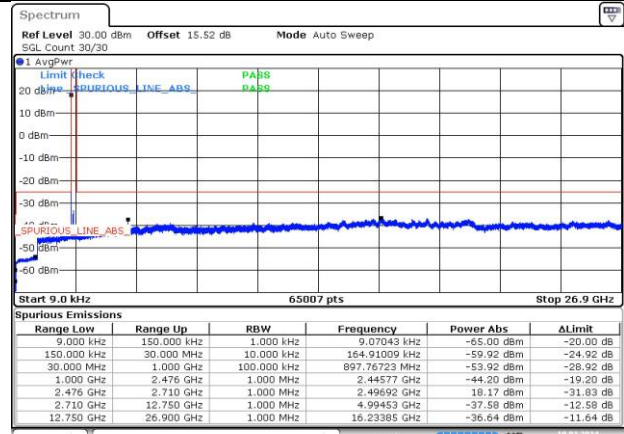
n41A / 30KHz / 90MHz

LCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



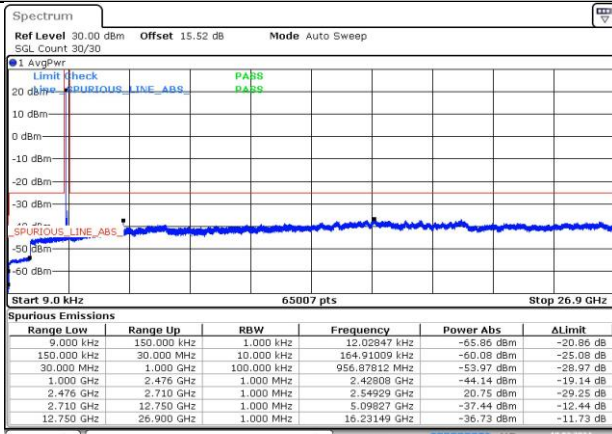
Date: 19 MAR 2024 19:25:07

LCH / DFT-QPSK\_Edge\_1RB\_Left



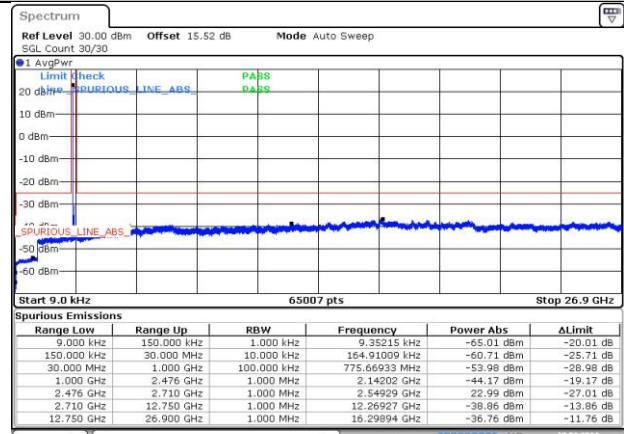
Date: 19 MAR 2024 19:25:36

MCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



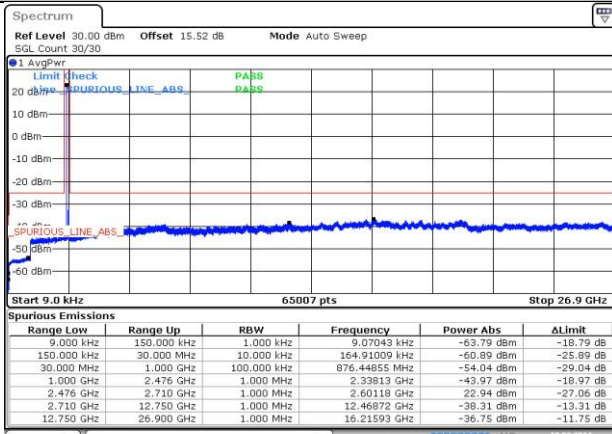
Date: 19 MAR 2024 19:26:02

MCH / DFT-QPSK\_Edge\_1RB\_Left



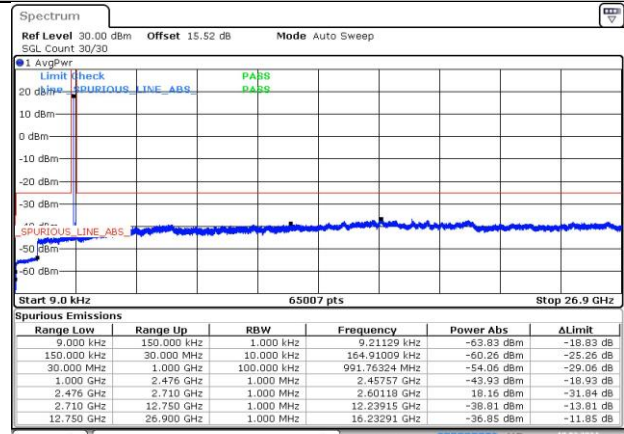
Date: 19 MAR 2024 19:26:31

HCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



Date: 19 MAR 2024 19:26:56

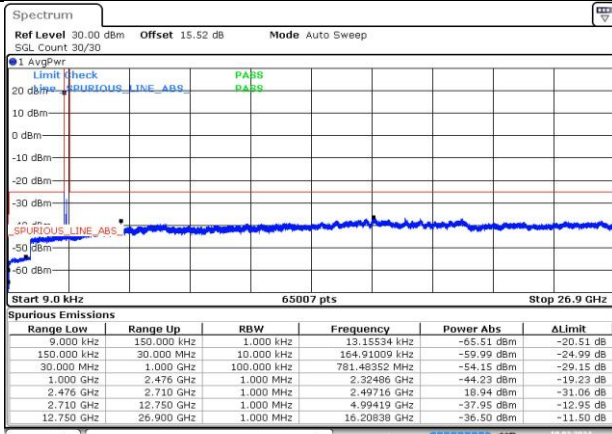
HCH / DFT-QPSK\_Edge\_1RB\_Left



Date: 19 MAR 2024 19:27:26

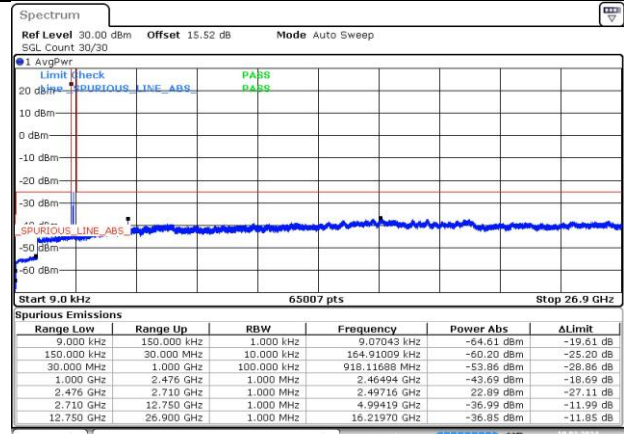
n41A / 30KHz / 100MHz

LCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



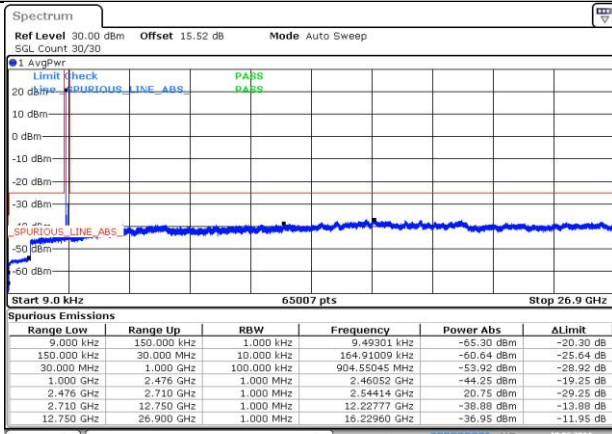
Date: 19 MAR 2024 19:28:11

LCH / DFT-QPSK\_Edge\_1RB\_Left



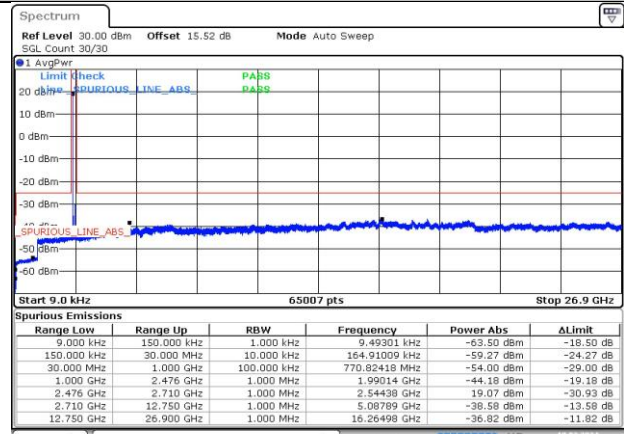
Date: 19 MAR 2024 19:28:40

MCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



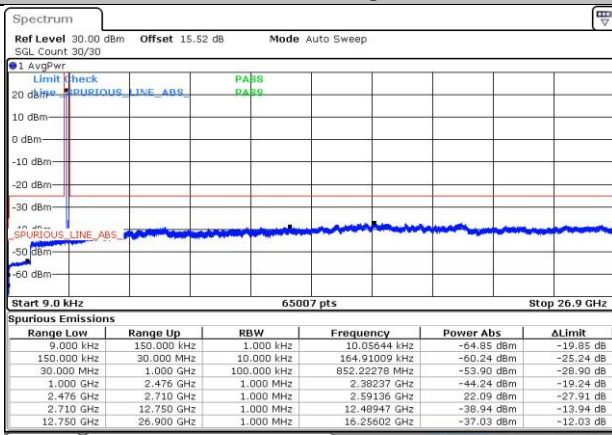
Date: 19 MAR 2024 19:29:05

MCH / DFT-QPSK\_Edge\_1RB\_Left



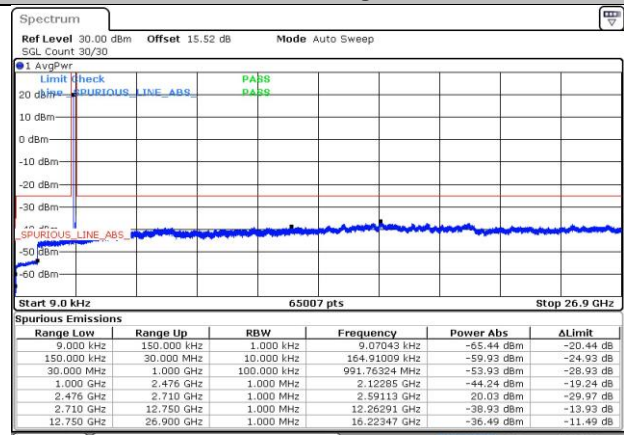
Date: 19 MAR 2024 19:29:35

HCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



Date: 19 MAR 2024 19:30:05

HCH / DFT-QPSK\_Edge\_1RB\_Left



Date: 19 MAR 2024 19:30:34

## 7. Frequency Stability

### 7.1. Test Results

#### 7.1.1. Frequency Error Vs Voltage

SCS	Bandwidth	Channel	RB Config	Modulation	Temperature	Voltage	Deviation Result		Verdict
							(Hz)	(ppm)	
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	LV	-7.80	-0.003008	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	NV	-7.30	-0.002815	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	HV	-2.00	-0.000771	Pass

#### 7.1.2. Frequency Error Vs Temperature

SCS	Bandwidth	Channel	RB Config	Modulation	Temperature	Voltage	Deviation Result		Verdict
							(Hz)	(ppm)	
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-30°C	NV	-6.50	-0.002507	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-20°C	NV	-12.80	-0.004936	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-10°C	NV	-5.60	-0.002160	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	0°C	NV	-7.40	-0.002854	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	10°C	NV	-2.80	-0.001080	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	20°C	NV	0.60	0.000231	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	30°C	NV	-5.00	-0.001928	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	40°C	NV	2.80	0.001080	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	50°C	NV	-4.80	-0.001851	Pass

The End