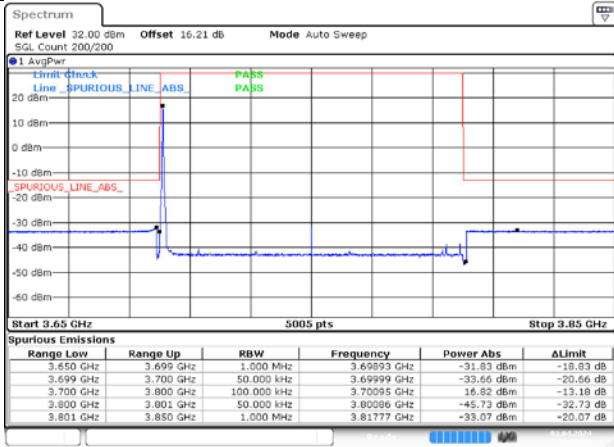


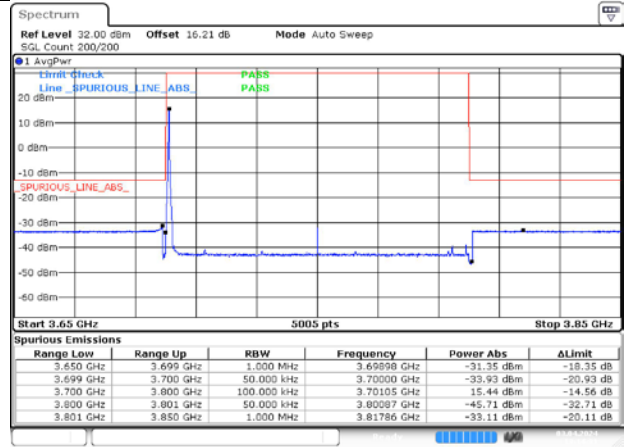
DC_41A_n78A / 30KHz / 100MHz

MCH / DFT-Pi2BPSK / Edge_1RB_Left



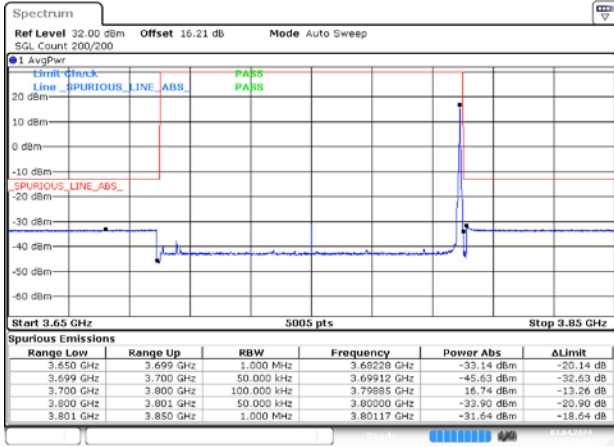
Date: 3 APR 2024 13:12:38

MCH / DFT-QPSK / Edge_1RB_Left



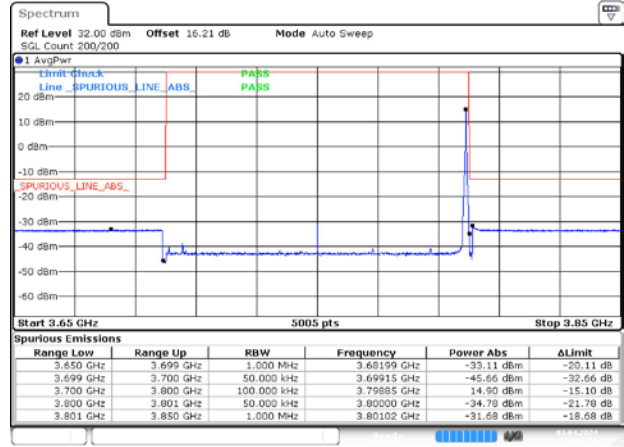
Date: 3 APR 2024 13:14:32

MCH / DFT-Pi2BPSK / Edge_1RB_Right



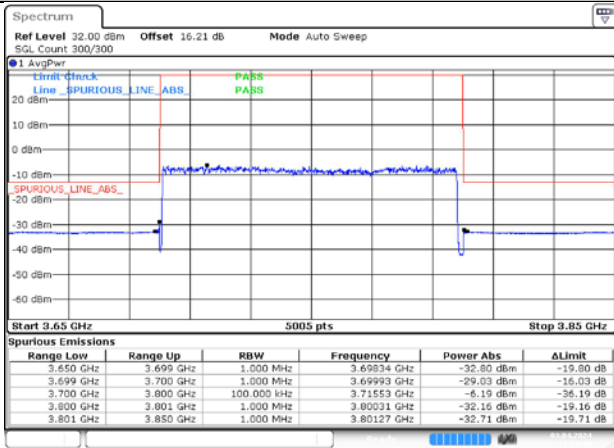
Date: 3 APR 2024 13:13:25

MCH / DFT-QPSK / Edge_1RB_Right



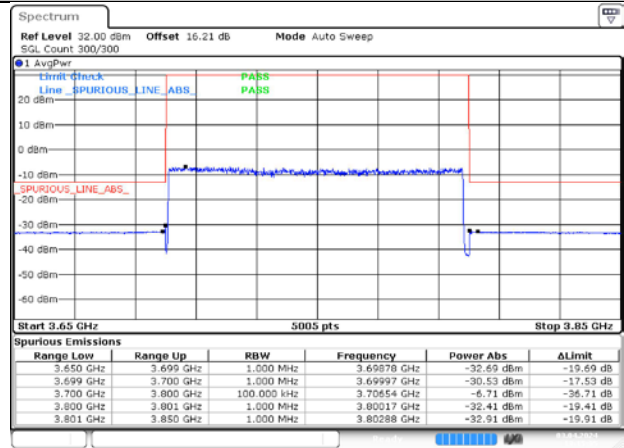
Date: 3 APR 2024 13:15:16

MCH / DFT-Pi2BPSK / Outer_Full



Date: 3 APR 2024 13:13:44

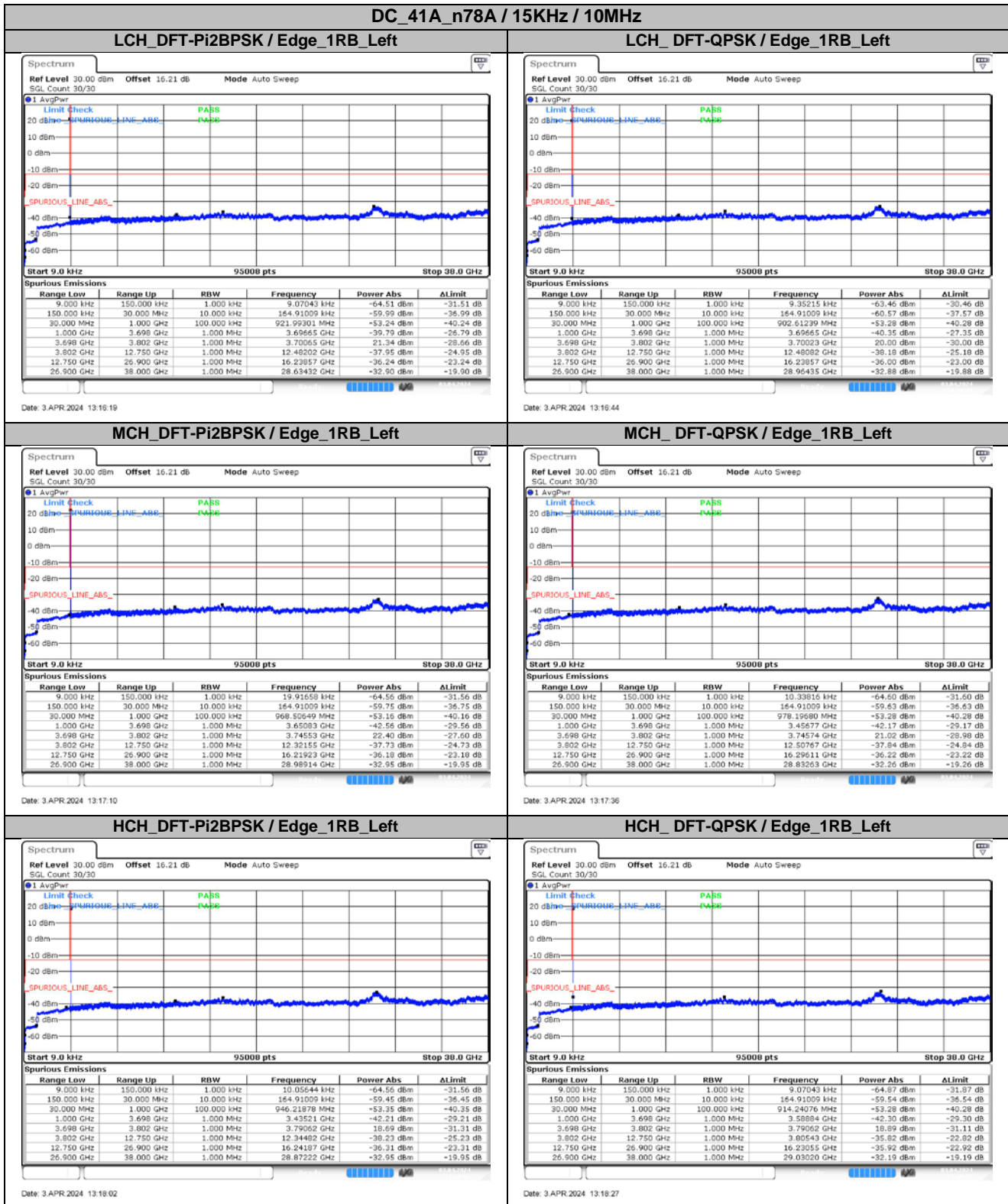
MCH / DFT-QPSK / Outer_Full



Date: 3 APR 2024 13:15:36

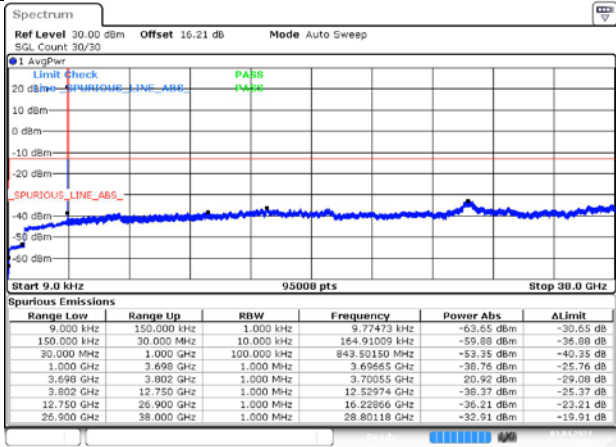
6. Conducted Spurious Emission

6.1. Test Plots



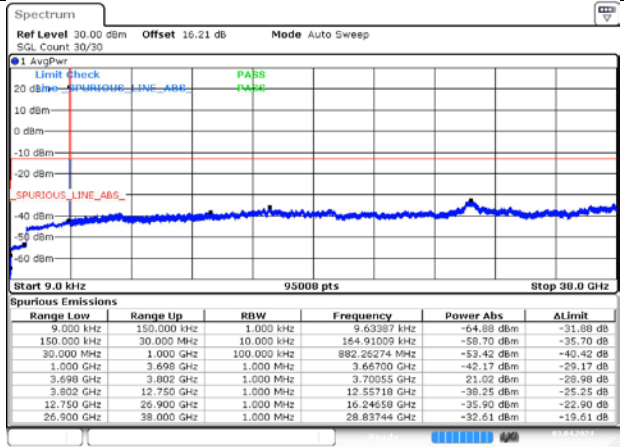
DC_41A_n78A / 15KHz / 15MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



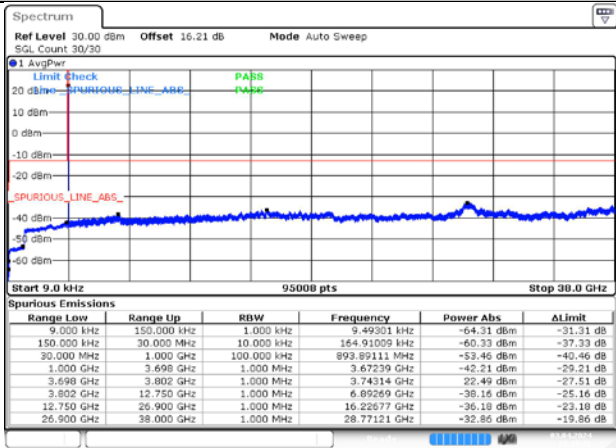
Date: 3 APR 2024 13:19:06

LCH_DFT-QPSK / Edge_1RB_Left



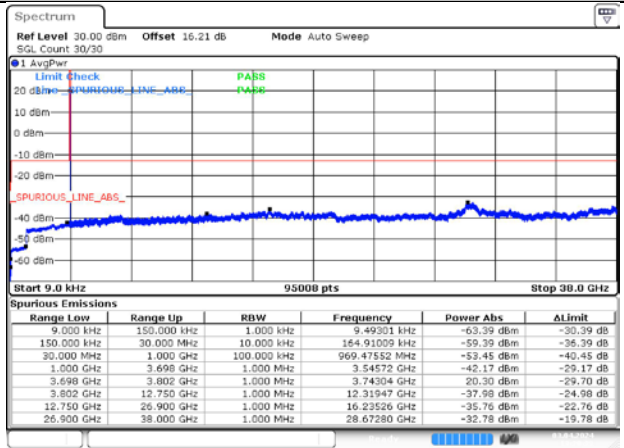
Date: 3 APR 2024 13:19:31

MCH_DFT-Pi2BPSK / Edge_1RB_Left



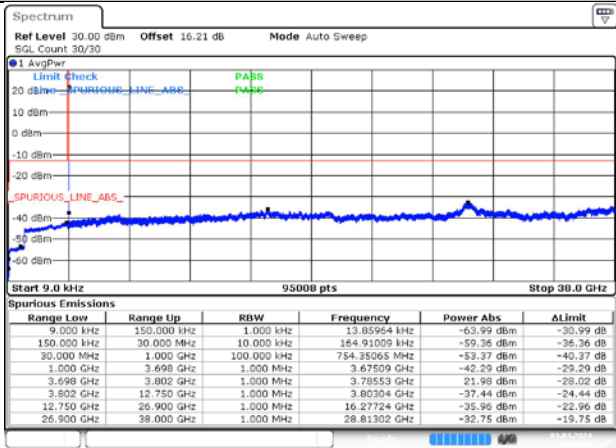
Date: 3 APR 2024 13:19:57

MCH_DFT-QPSK / Edge_1RB_Left



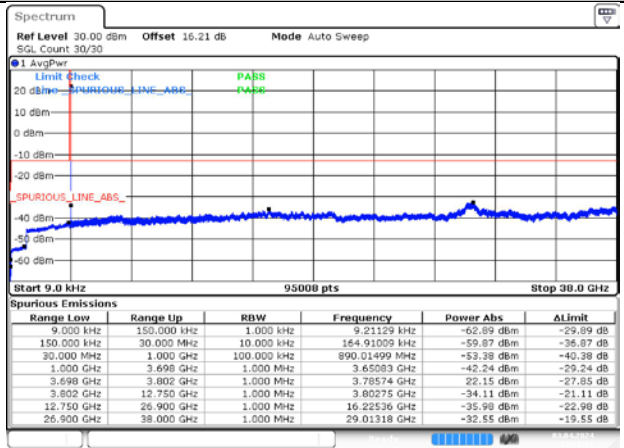
Date: 3 APR 2024 13:20:22

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 3 APR 2024 13:20:48

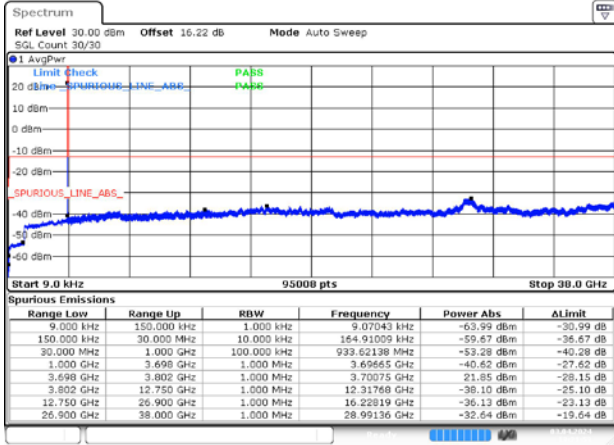
HCH_DFT-QPSK / Edge_1RB_Left



Date: 3 APR 2024 13:21:13

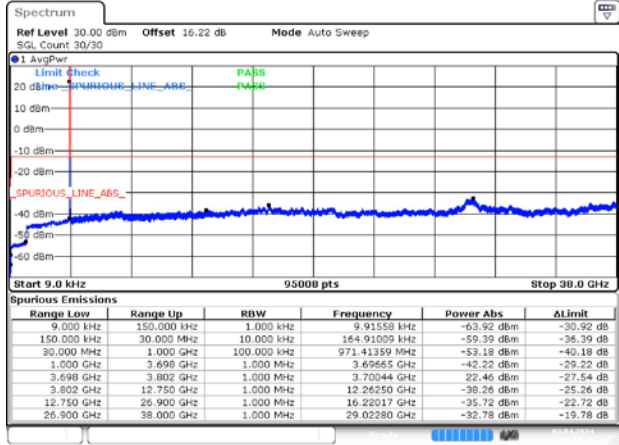
DC_41A_n78A / 15KHz / 20MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



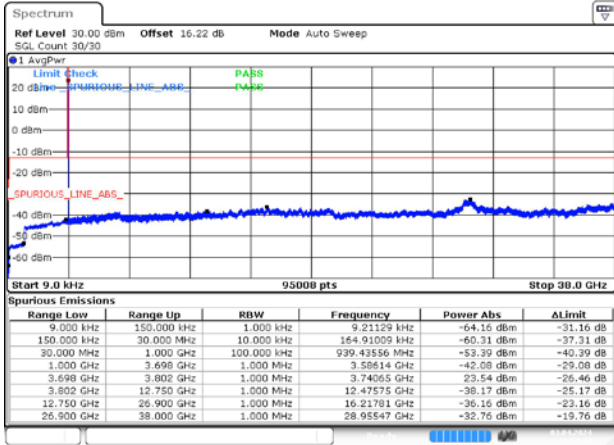
Date: 3 APR 2024 13:21:52

LCH_DFT-QPSK / Edge_1RB_Left



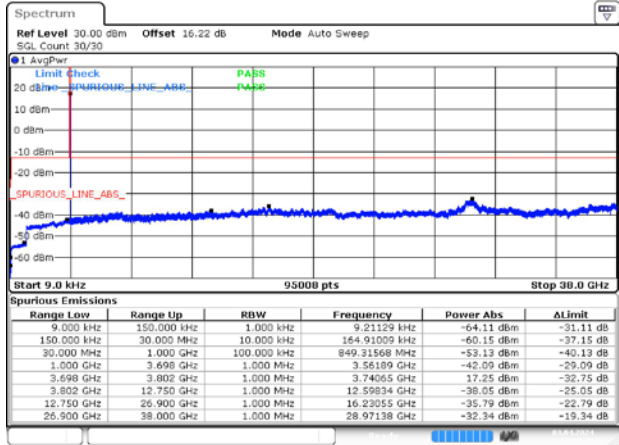
Date: 3 APR 2024 13:22:18

MCH_DFT-Pi2BPSK / Edge_1RB_Left



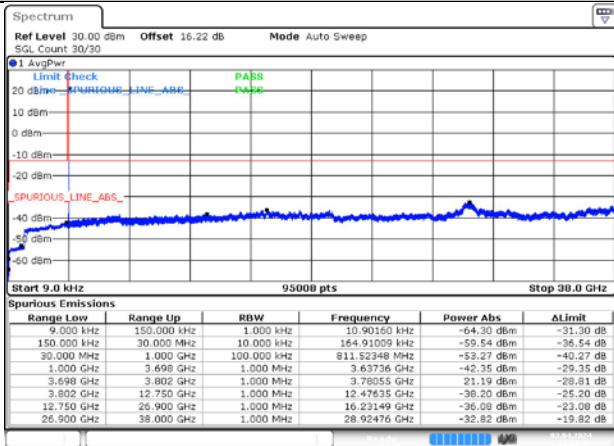
Date: 3 APR 2024 13:22:44

MCH_DFT-QPSK / Edge_1RB_Left



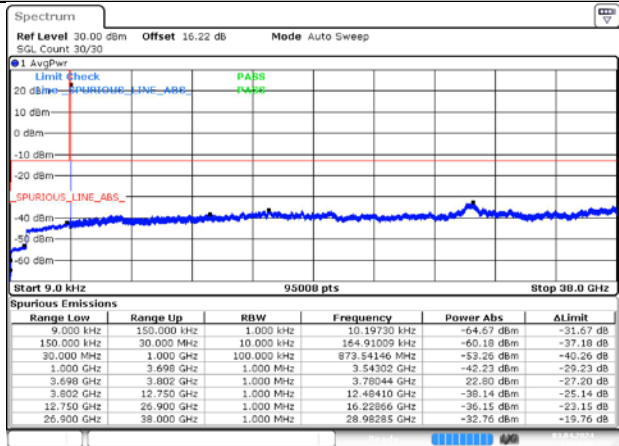
Date: 3 APR 2024 13:23:09

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 3 APR 2024 13:23:35

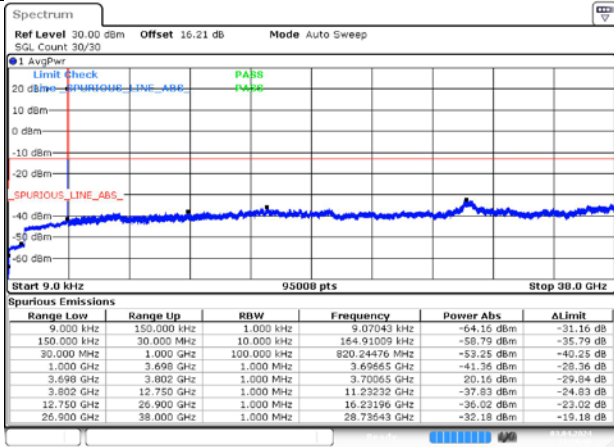
HCH_DFT-QPSK / Edge_1RB_Left



Date: 3 APR 2024 13:24:00

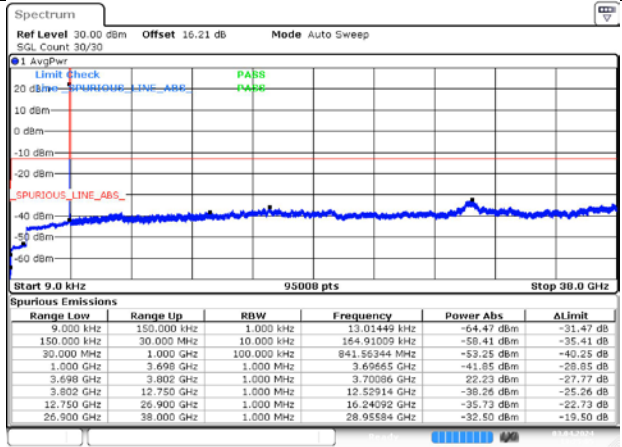
DC_41A_n78A / 15KHz / 25MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



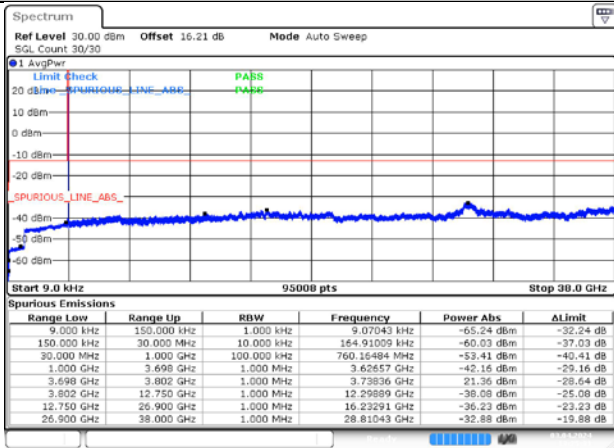
Date: 3 APR 2024 13:24:40

LCH_DFT-QPSK / Edge_1RB_Left



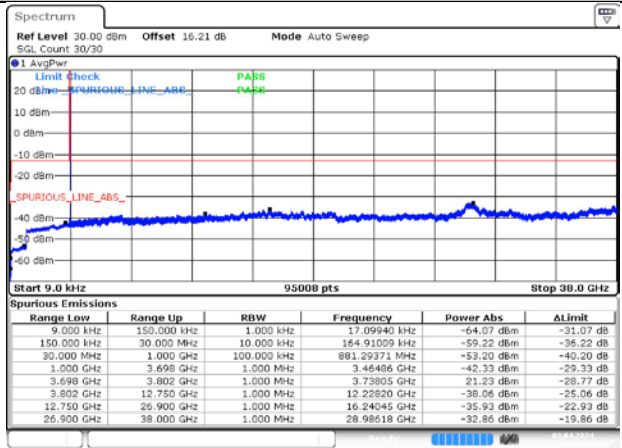
Date: 3 APR 2024 13:25:06

MCH_DFT-Pi2BPSK / Edge_1RB_Left



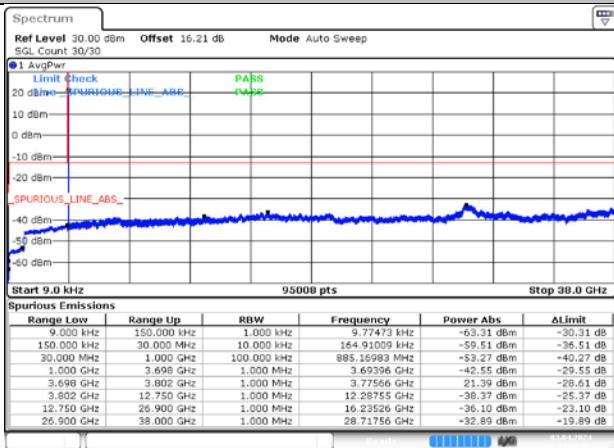
Date: 3 APR 2024 13:25:31

MCH_DFT-QPSK / Edge_1RB_Left



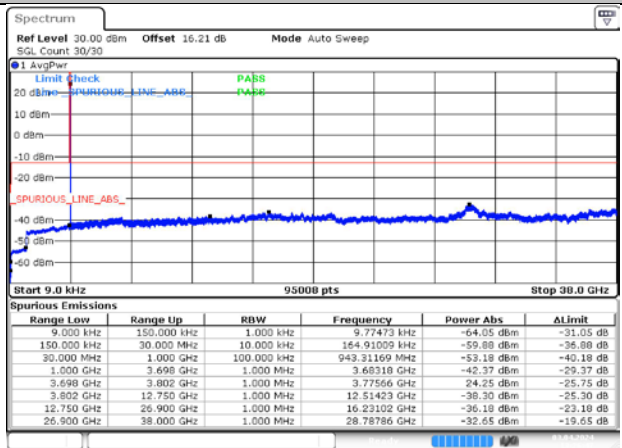
Date: 3 APR 2024 13:25:56

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 3 APR 2024 13:26:22

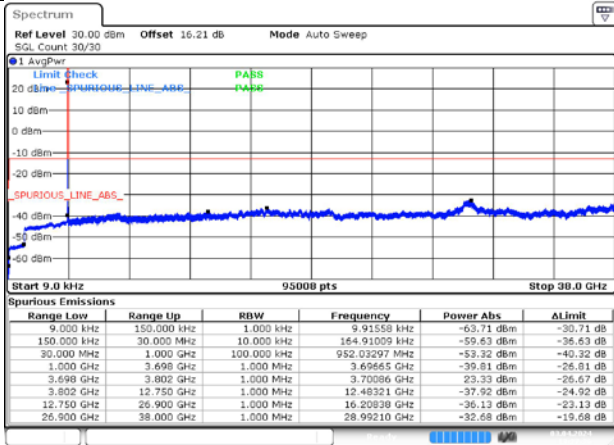
HCH_DFT-QPSK / Edge_1RB_Left



Date: 3 APR 2024 13:26:46

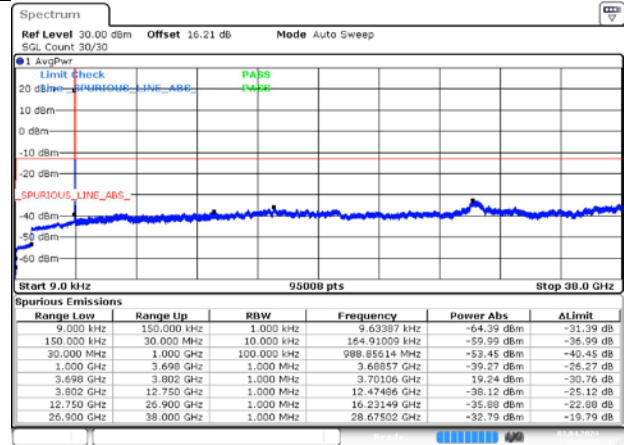
DC_41A_n78A / 15KHz / 30MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



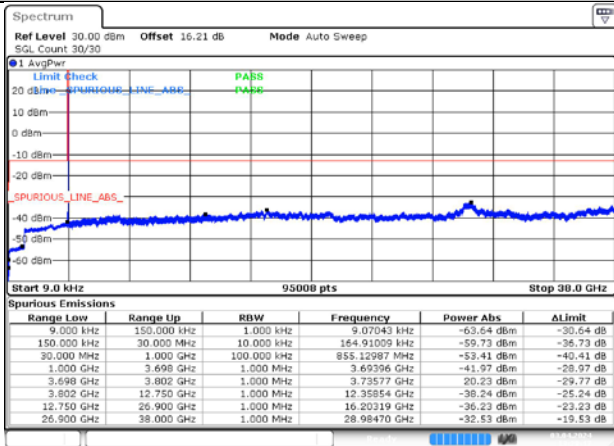
Date: 3 APR 2024 13:27:26

LCH_DFT-QPSK / Edge_1RB_Left



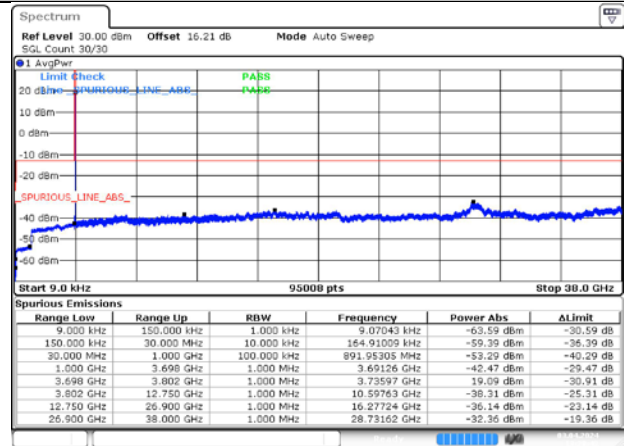
Date: 3 APR 2024 13:27:50

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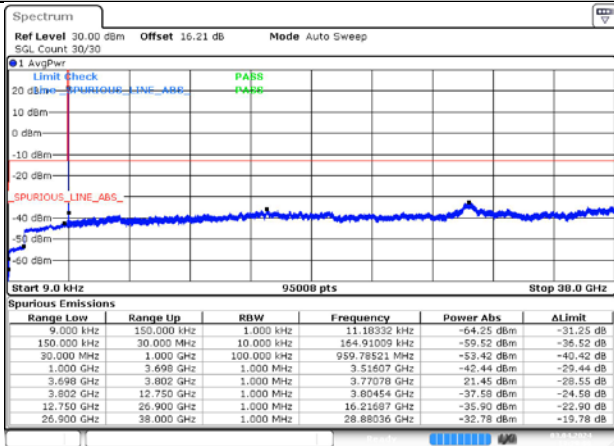
Date: 3 APR 2024 13:28:17

MCH_DFT-QPSK / Edge_1RB_Left



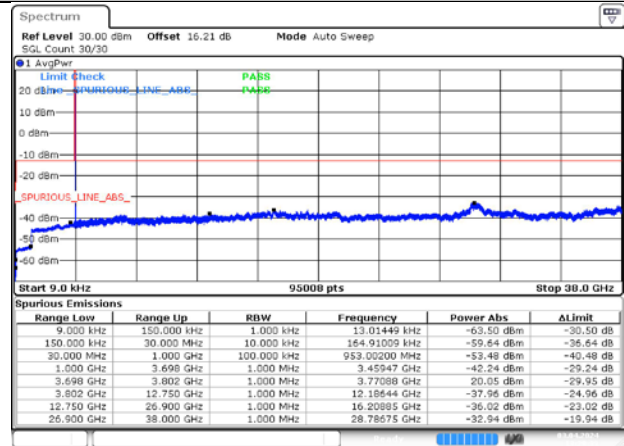
Date: 3 APR 2024 13:28:42

HCH_DFT-Pi2BPSK / Edge_1RB_Left

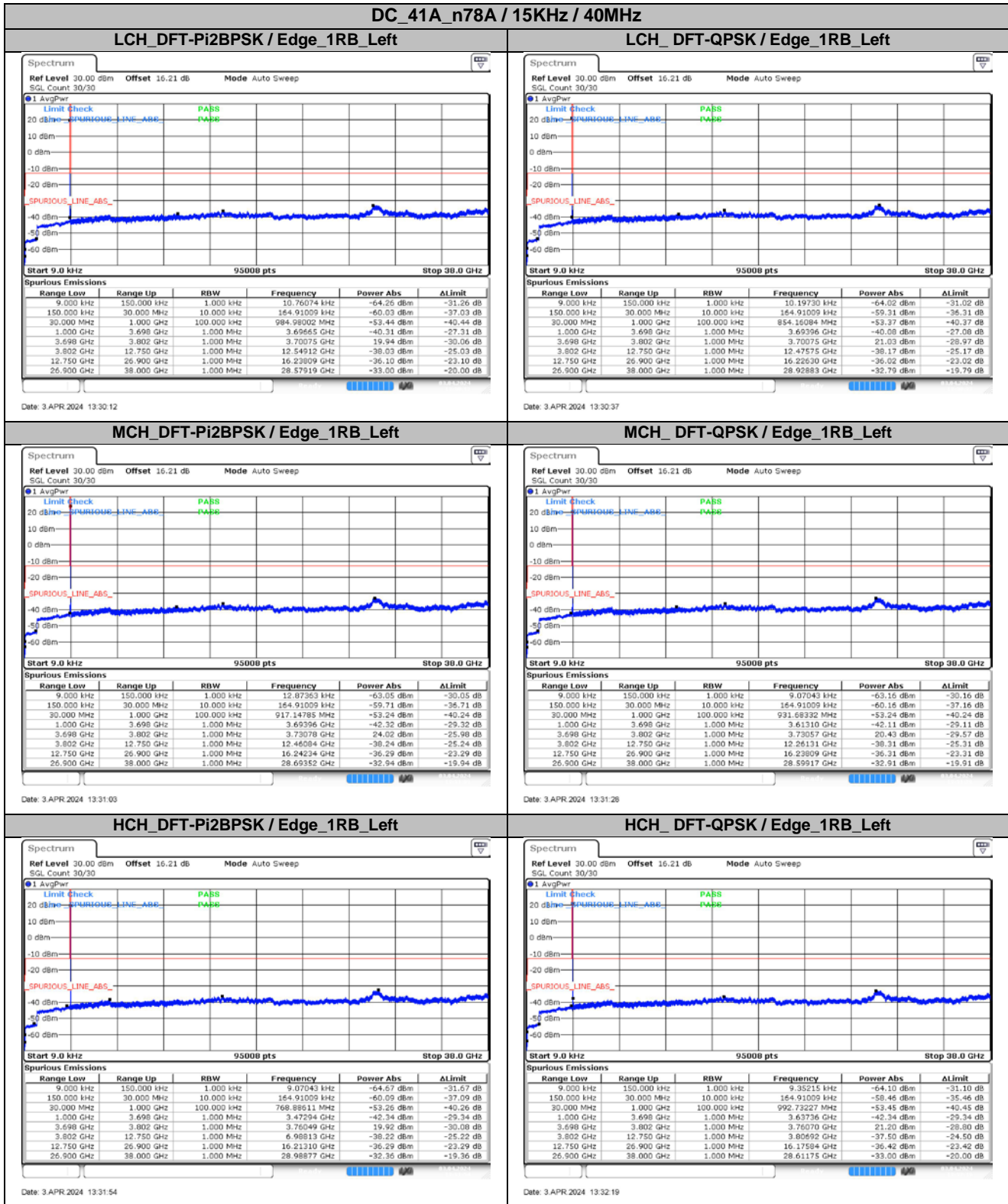


Date: 3 APR 2024 13:28:06

HCH_DFT-QPSK / Edge_1RB_Left

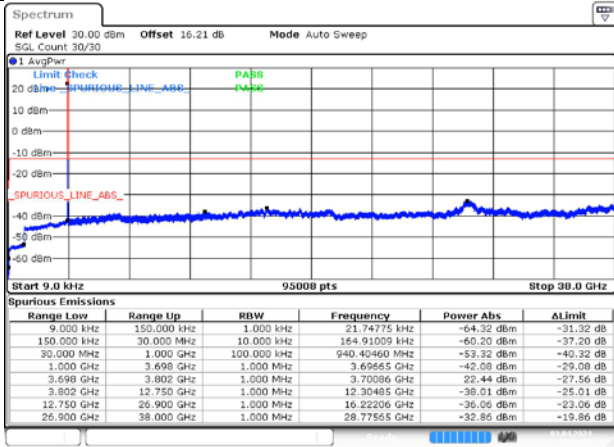


Date: 3 APR 2024 13:28:32



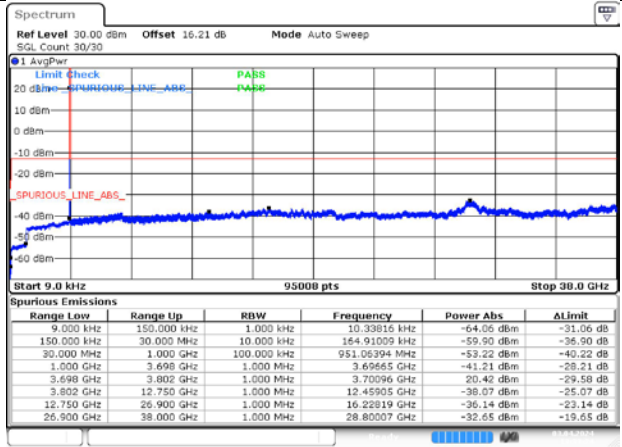
DC_41A_n78A / 15KHz / 50MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



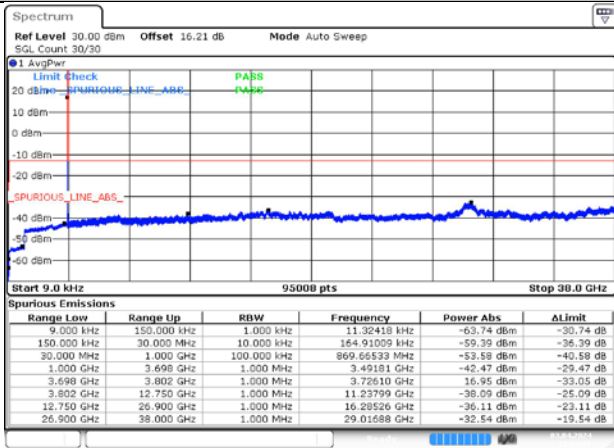
Date: 3 APR 2024 13:32:58

LCH_DFT-QPSK / Edge_1RB_Left



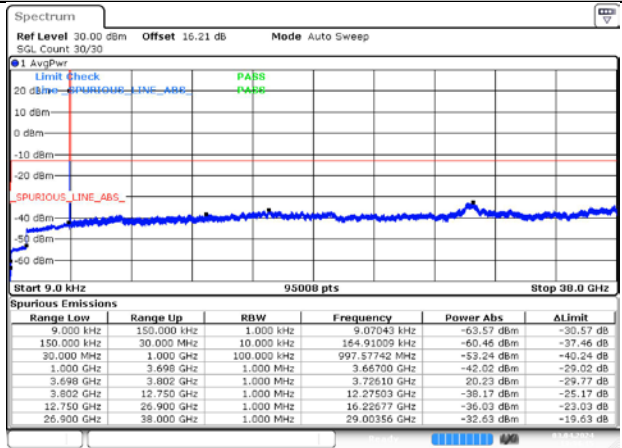
Date: 3 APR 2024 13:33:24

MCH_DFT-Pi2BPSK / Edge_1RB_Left



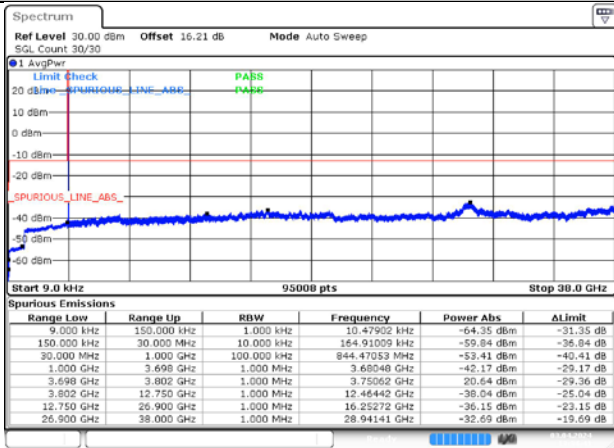
Date: 3 APR 2024 13:33:50

MCH_DFT-QPSK / Edge_1RB_Left



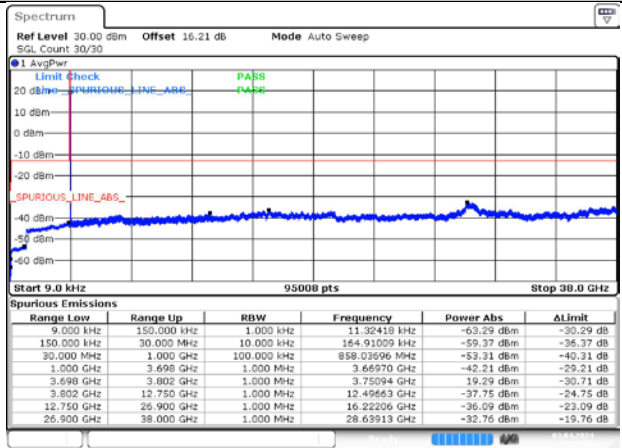
Date: 3 APR 2024 13:34:15

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 3 APR 2024 13:34:41

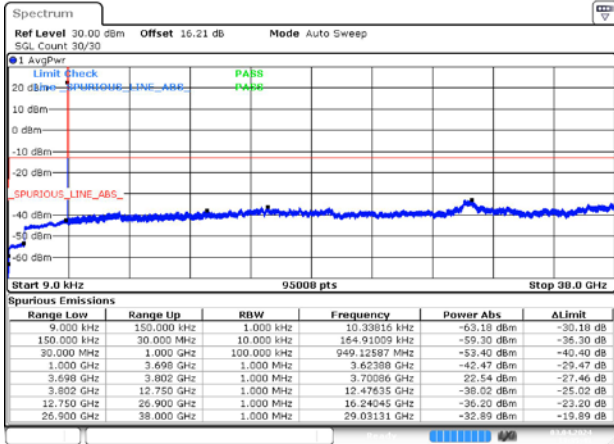
HCH_DFT-QPSK / Edge_1RB_Left



Date: 3 APR 2024 13:35:06

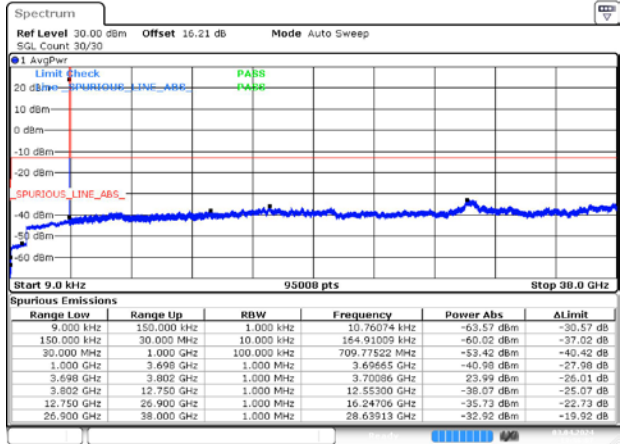
DC_41A_n78A / 30KHz / 10MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



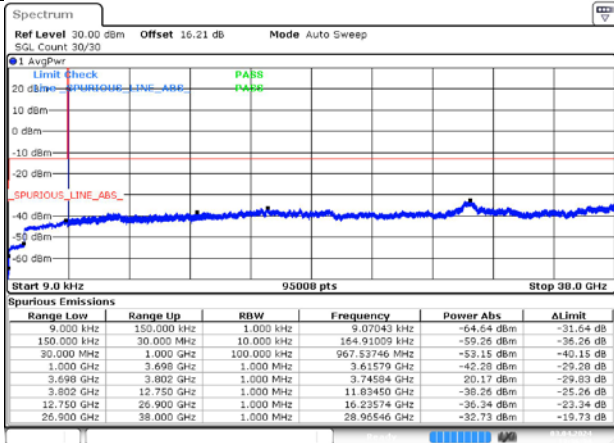
Date: 3 APR 2024 13:35:43

LCH_DFT-QPSK / Edge_1RB_Left



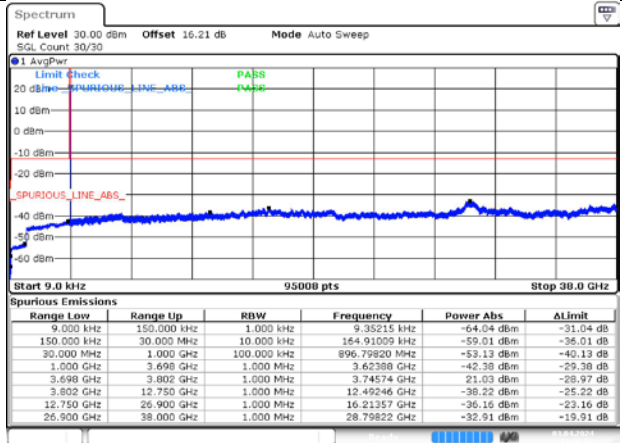
Date: 3 APR 2024 13:36:07

MCH_DFT-Pi2BPSK / Edge_1RB_Left



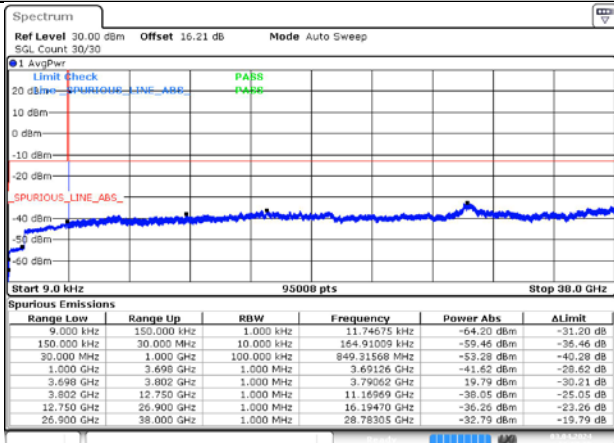
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MCH_DFT-QPSK / Edge_1RB_Left



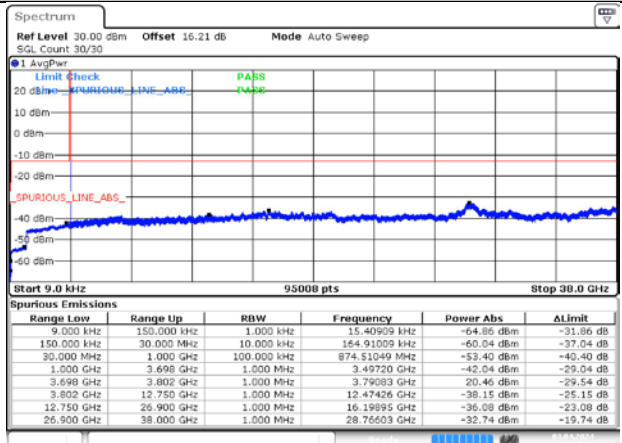
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HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 3 APR 2024 13:37:24

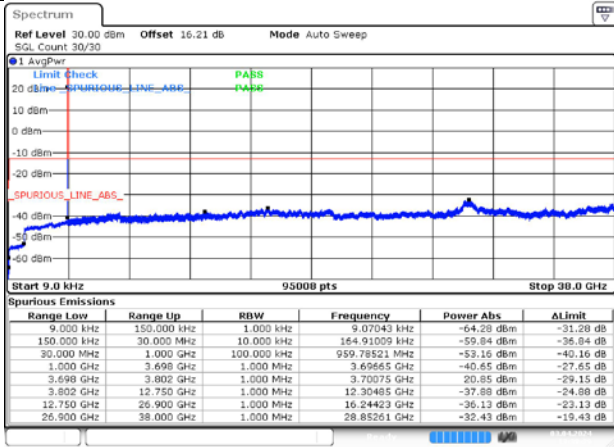
HCH_DFT-QPSK / Edge_1RB_Left



Date: 3 APR 2024 13:37:49

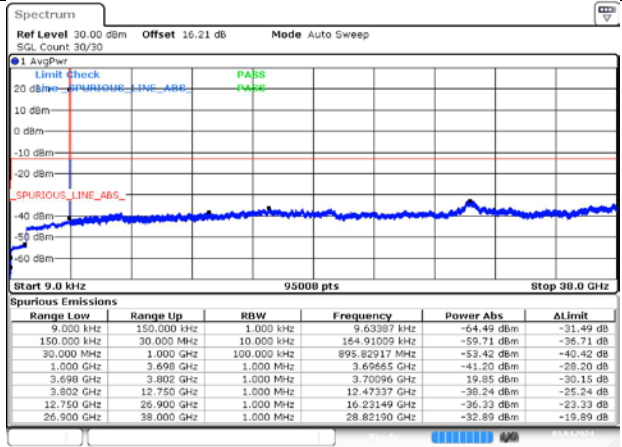
DC_41A_n78A / 30KHz / 15MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



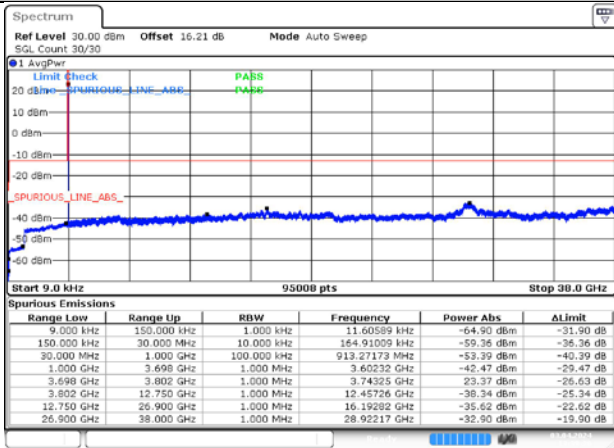
Date: 3 APR 2024 13:38:29

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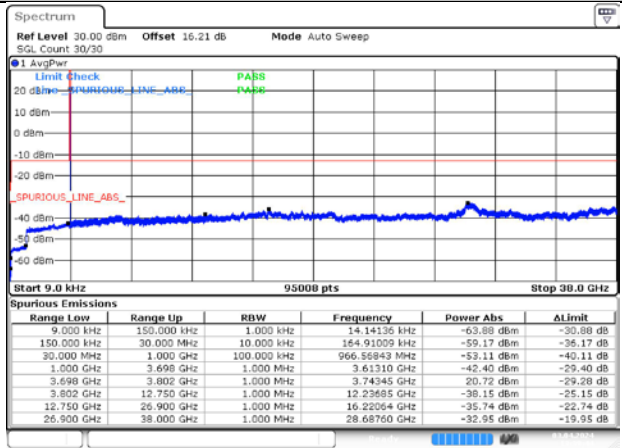
Date: 3 APR 2024 13:38:53

MCH_DFT-Pi2BPSK / Edge_1RB_Left



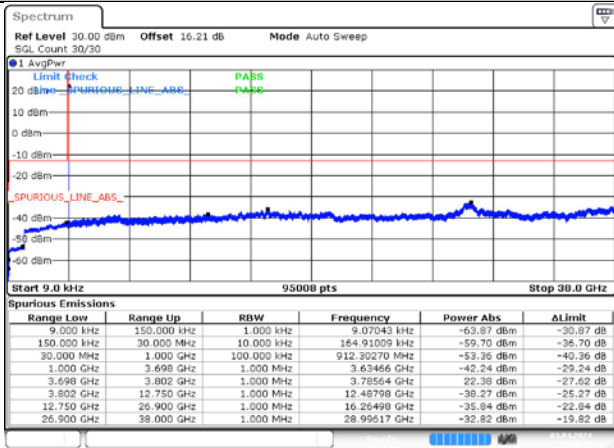
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MCH_DFT-QPSK / Edge_1RB_Left



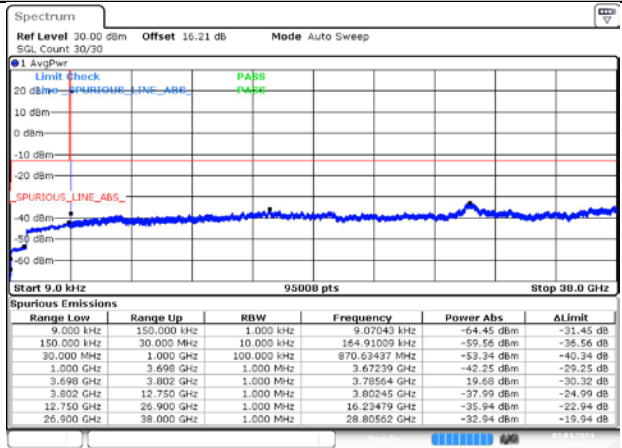
Date: 3 APR 2024 13:38:44

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 3 APR 2024 13:40:10

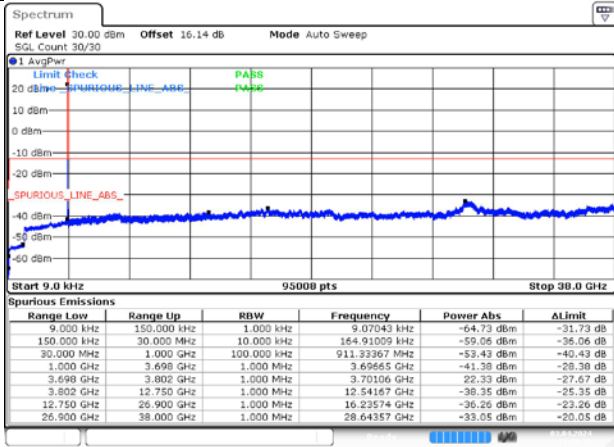
HCH_DFT-QPSK / Edge_1RB_Left



Date: 3 APR 2024 13:40:35

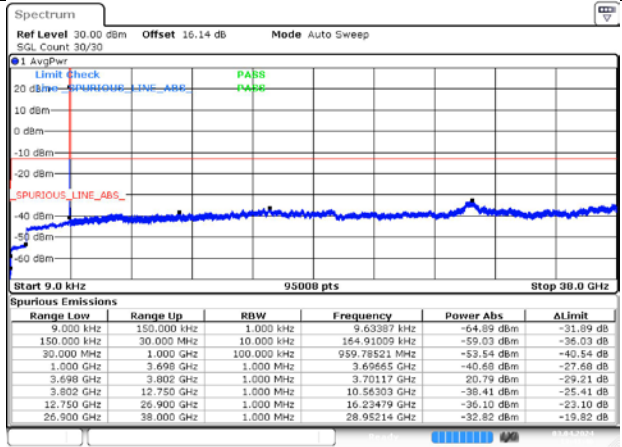
DC_41A_n78A / 30KHz / 20MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



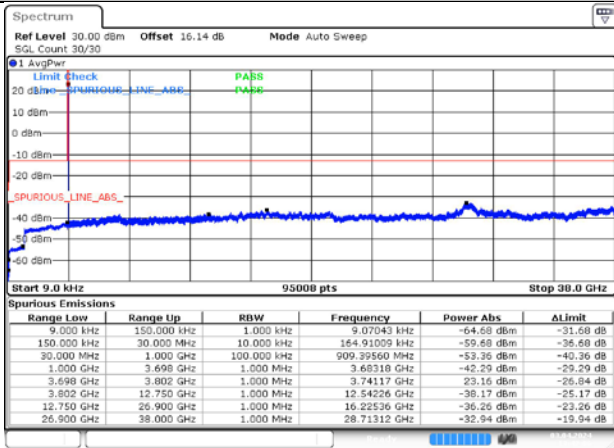
Date: 3 APR 2024 13:41:15

LCH_DFT-QPSK / Edge_1RB_Left



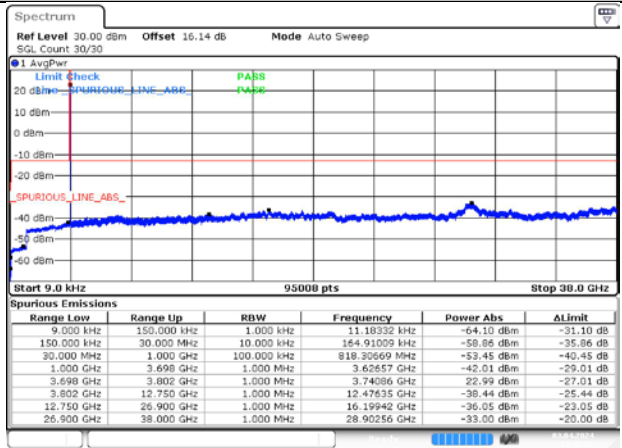
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MCH_DFT-Pi2BPSK / Edge_1RB_Left



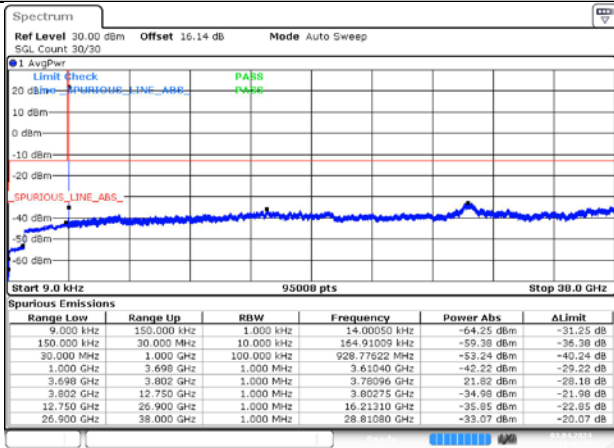
Date: 3 APR 2024 13:42:06

MCH_DFT-QPSK / Edge_1RB_Left



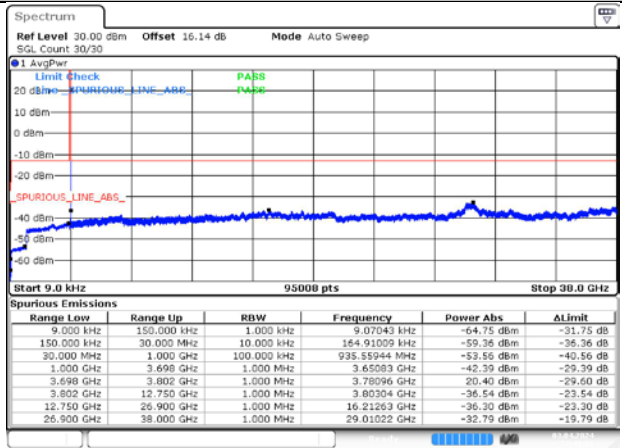
Date: 3 APR 2024 13:42:31

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 3 APR 2024 13:42:57

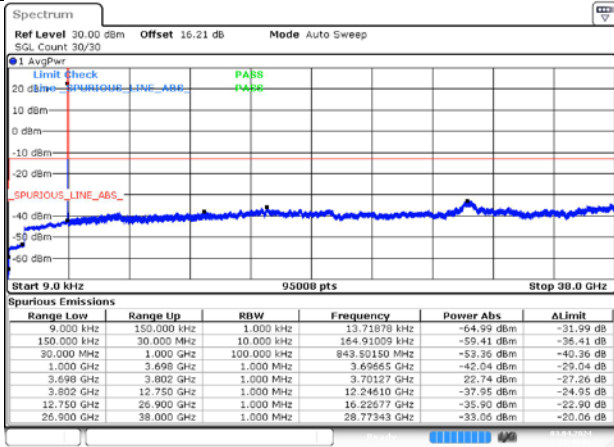
HCH_DFT-QPSK / Edge_1RB_Left



Date: 3 APR 2024 13:43:22

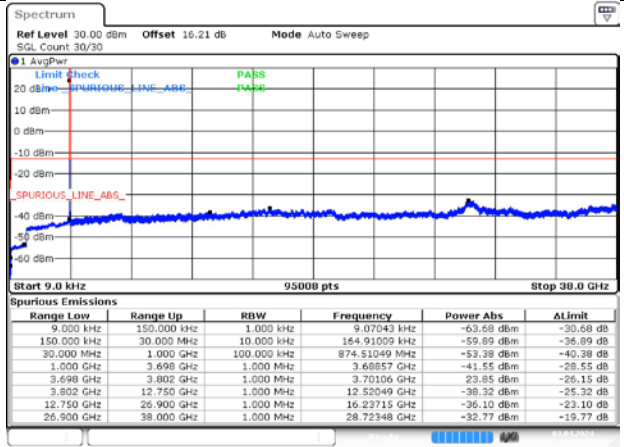
DC_41A_n78A / 30KHz / 25MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



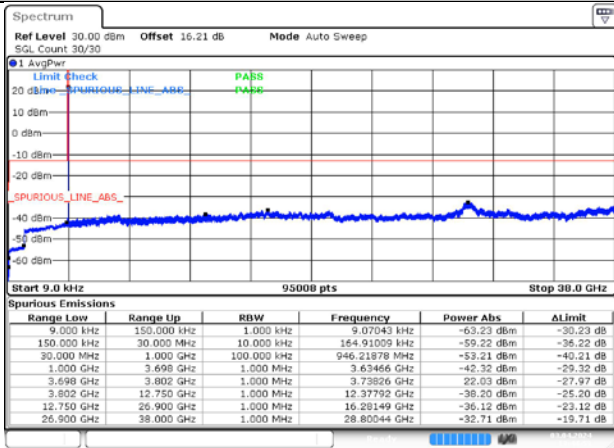
Date: 3 APR 2024 13:44:01

LCH_DFT-QPSK / Edge_1RB_Left



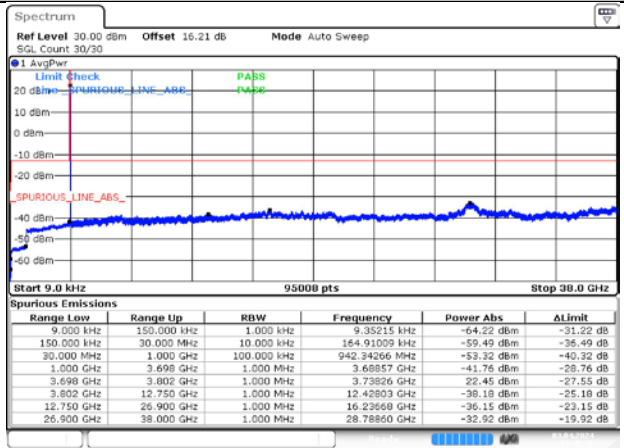
Date: 3 APR 2024 13:44:26

MCH_DFT-Pi2BPSK / Edge_1RB_Left



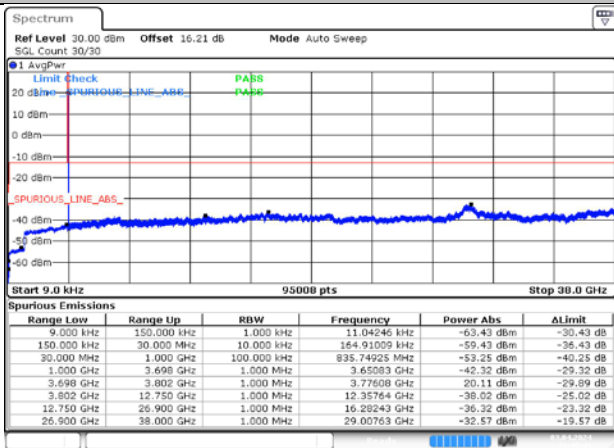
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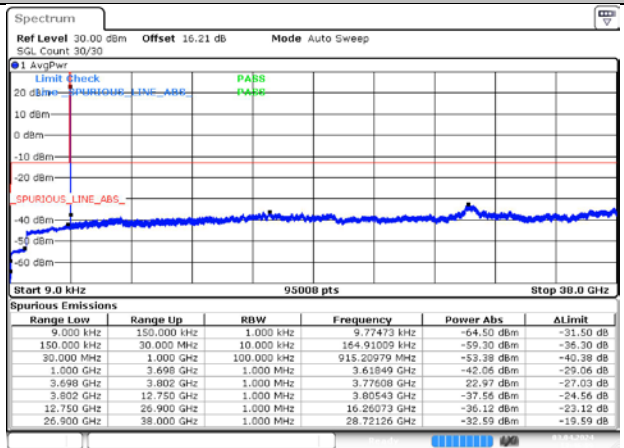
Date: 3 APR 2024 13:45:17

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 3 APR 2024 13:45:43

HCH_DFT-QPSK / Edge_1RB_Left



Date: 3 APR 2024 13:46:06