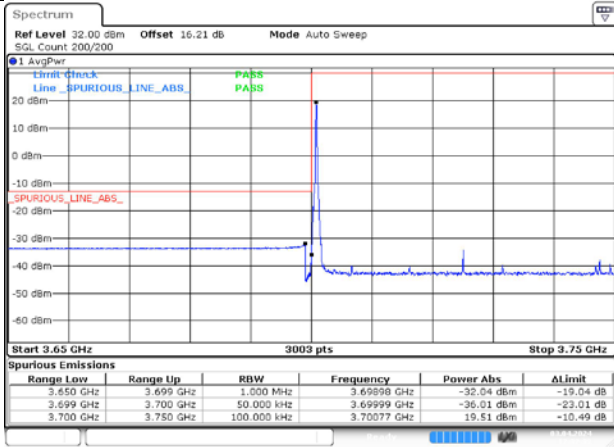


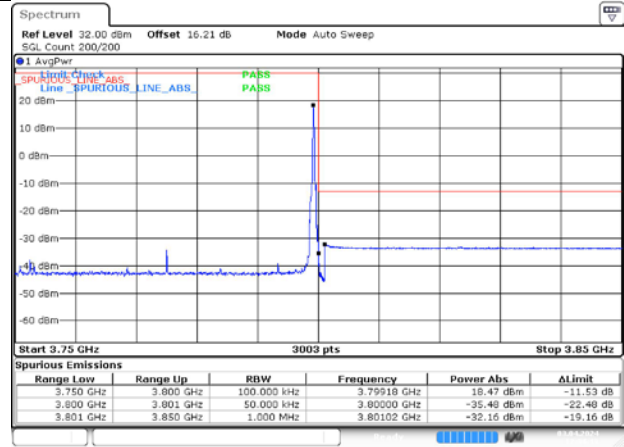
DC_41A_n78A / 15KHz / 50MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



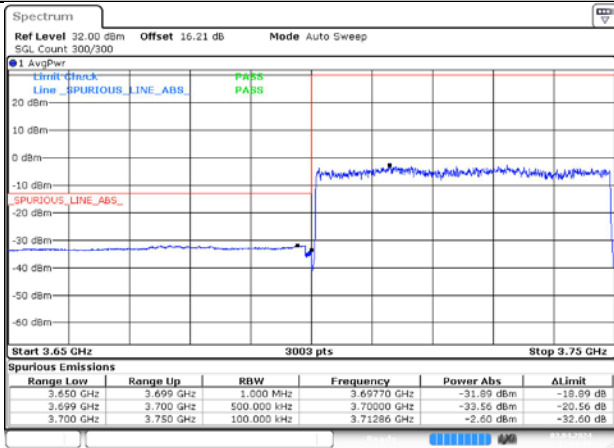
Date: 3 APR 2024 12:32:32

HCH_DFT-Pi2BPSK / Edge_1RB_Right



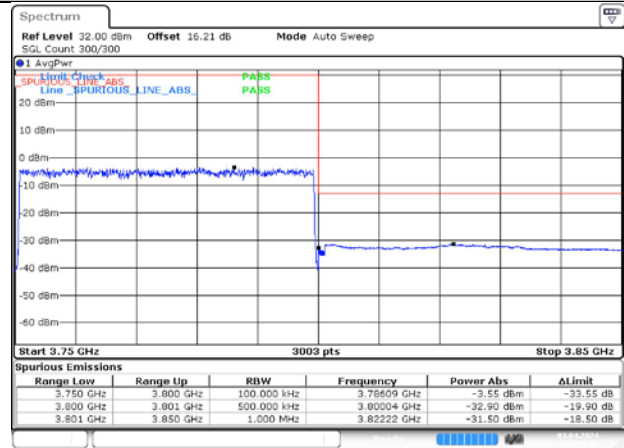
Date: 3 APR 2024 12:34:13

LCH_DFT-Pi2BPSK / Outer_Full



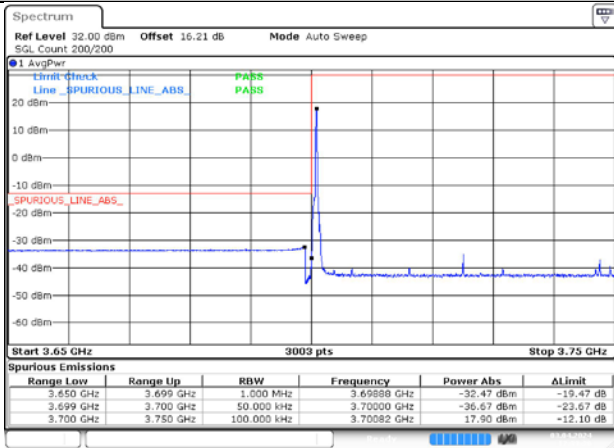
Date: 3 APR 2024 12:32:51

HCH_DFT-Pi2BPSK / Outer_Full



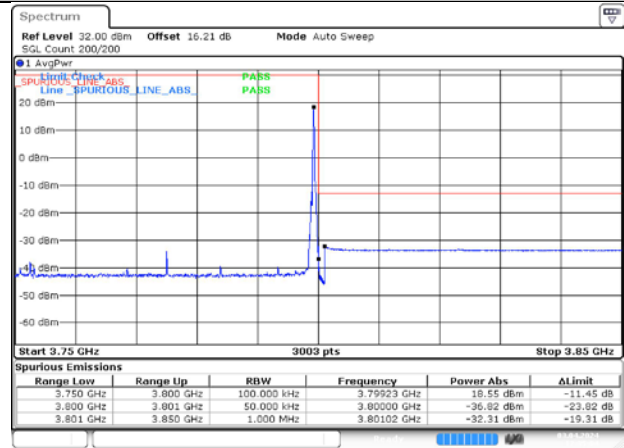
Date: 3 APR 2024 12:34:31

LCH_DFT-QPSK / Edge_1RB_Left

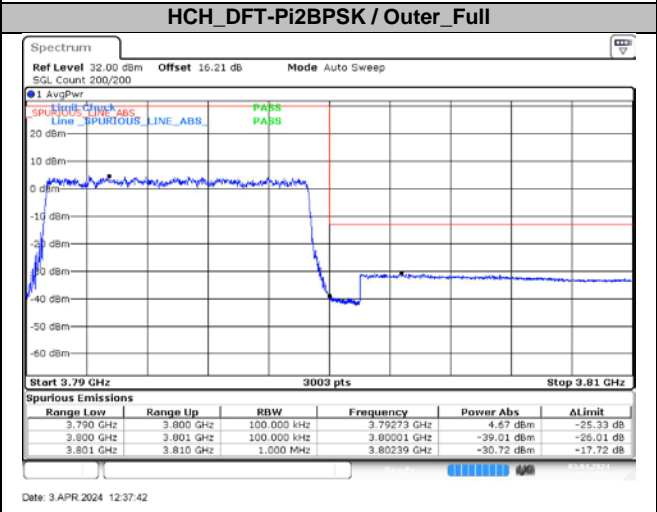
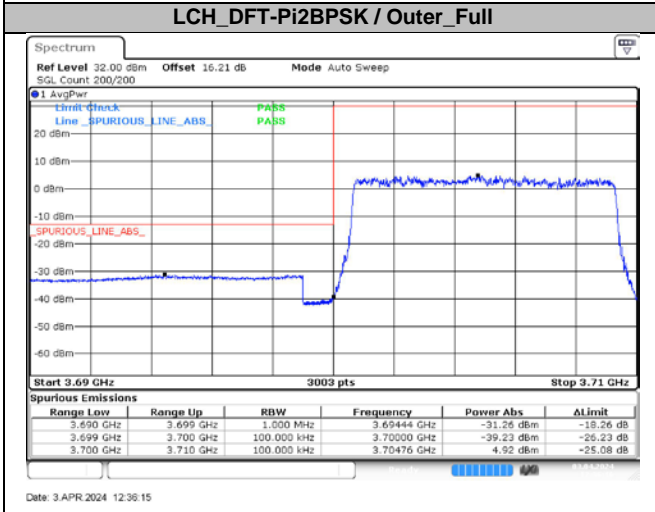
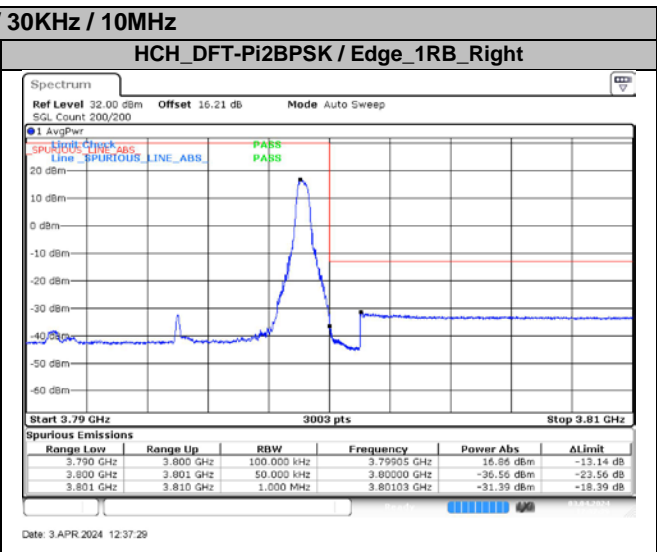
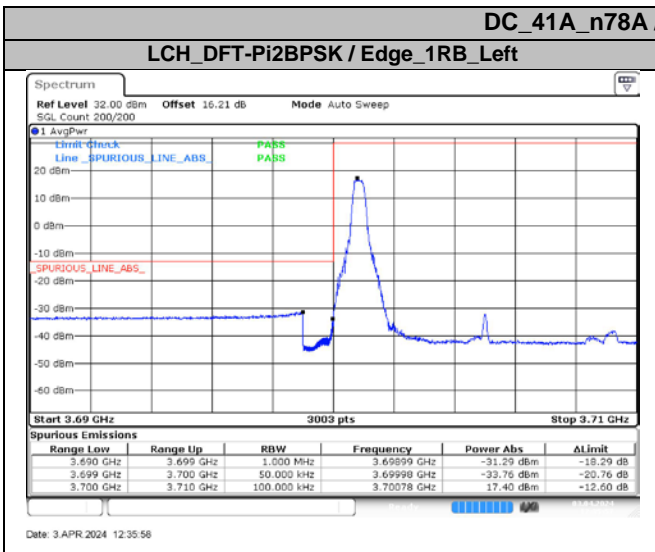
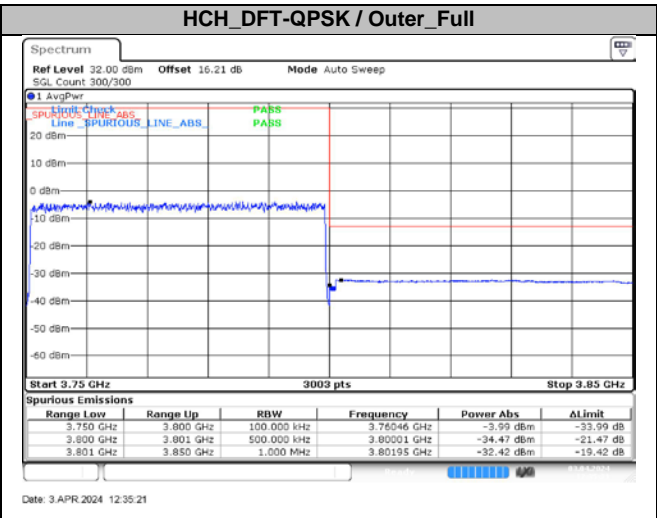
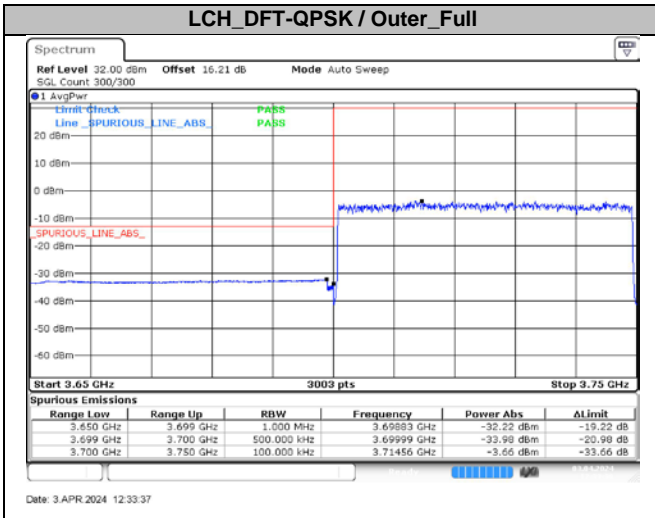


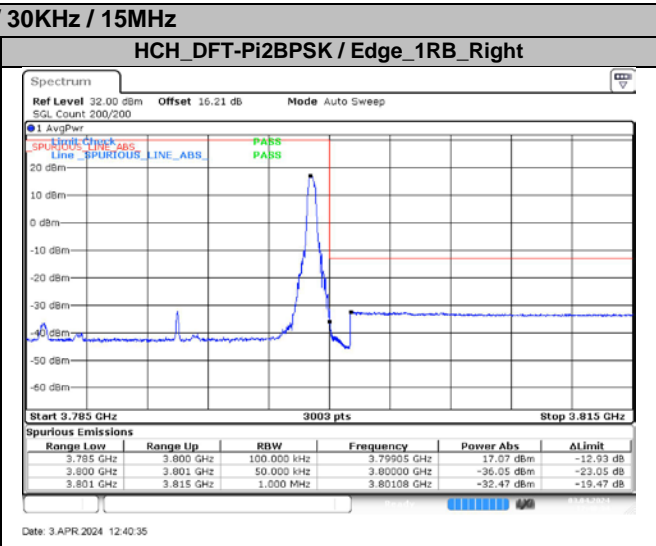
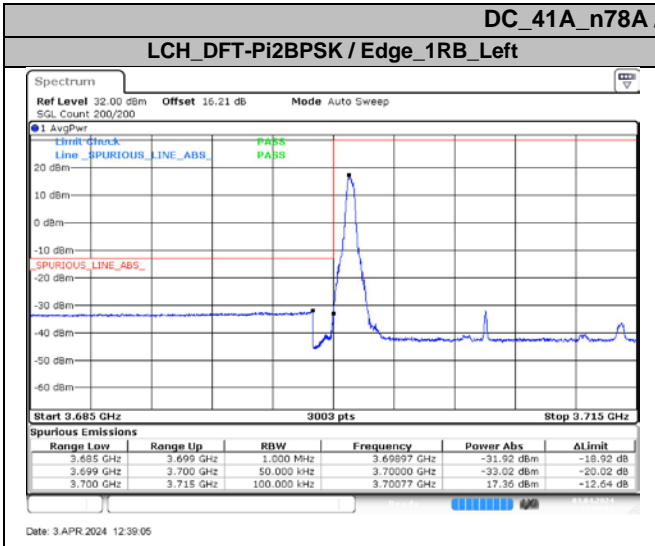
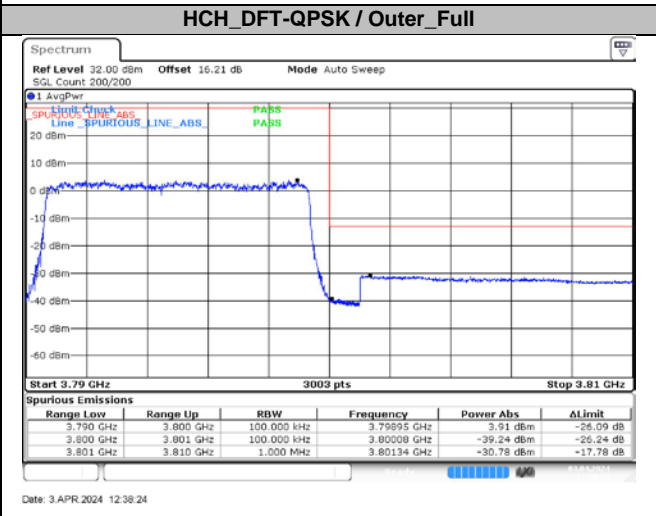
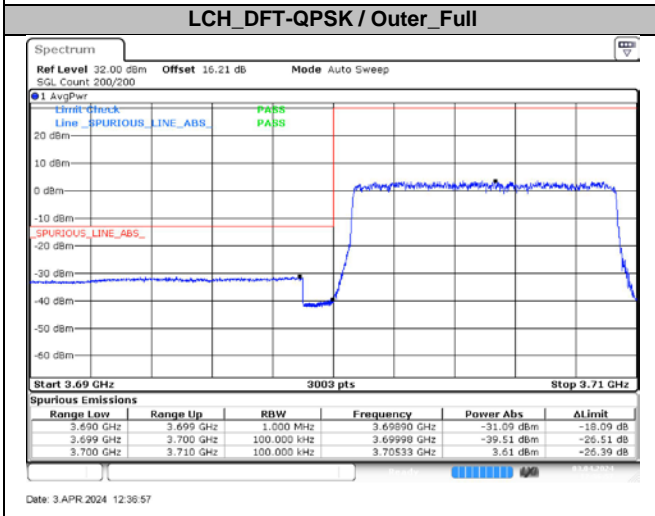
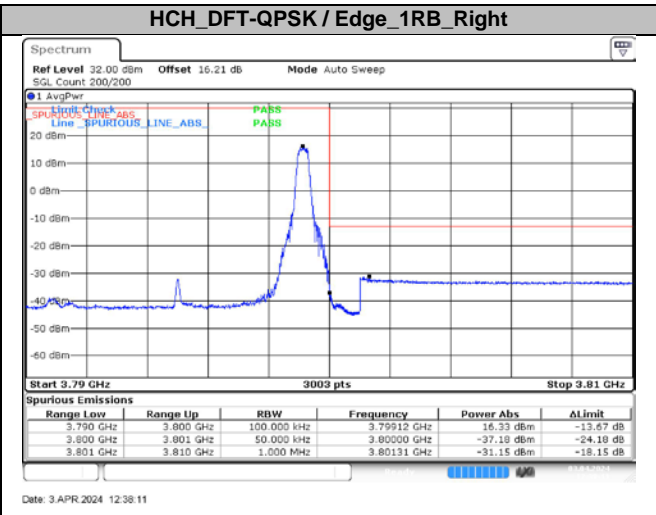
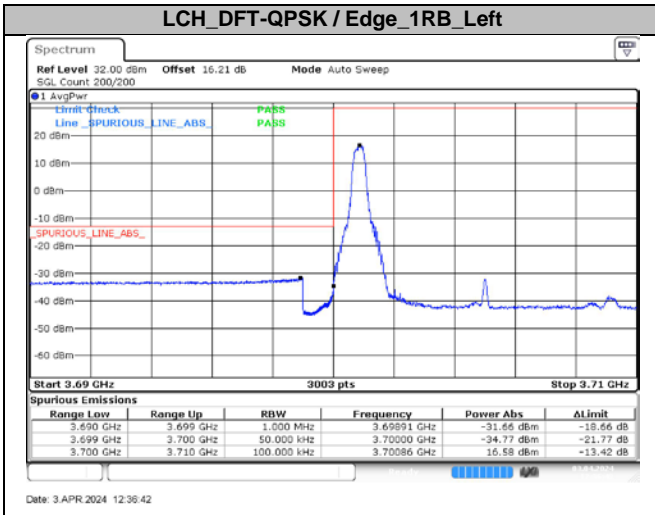
Date: 3 APR 2024 12:33:20

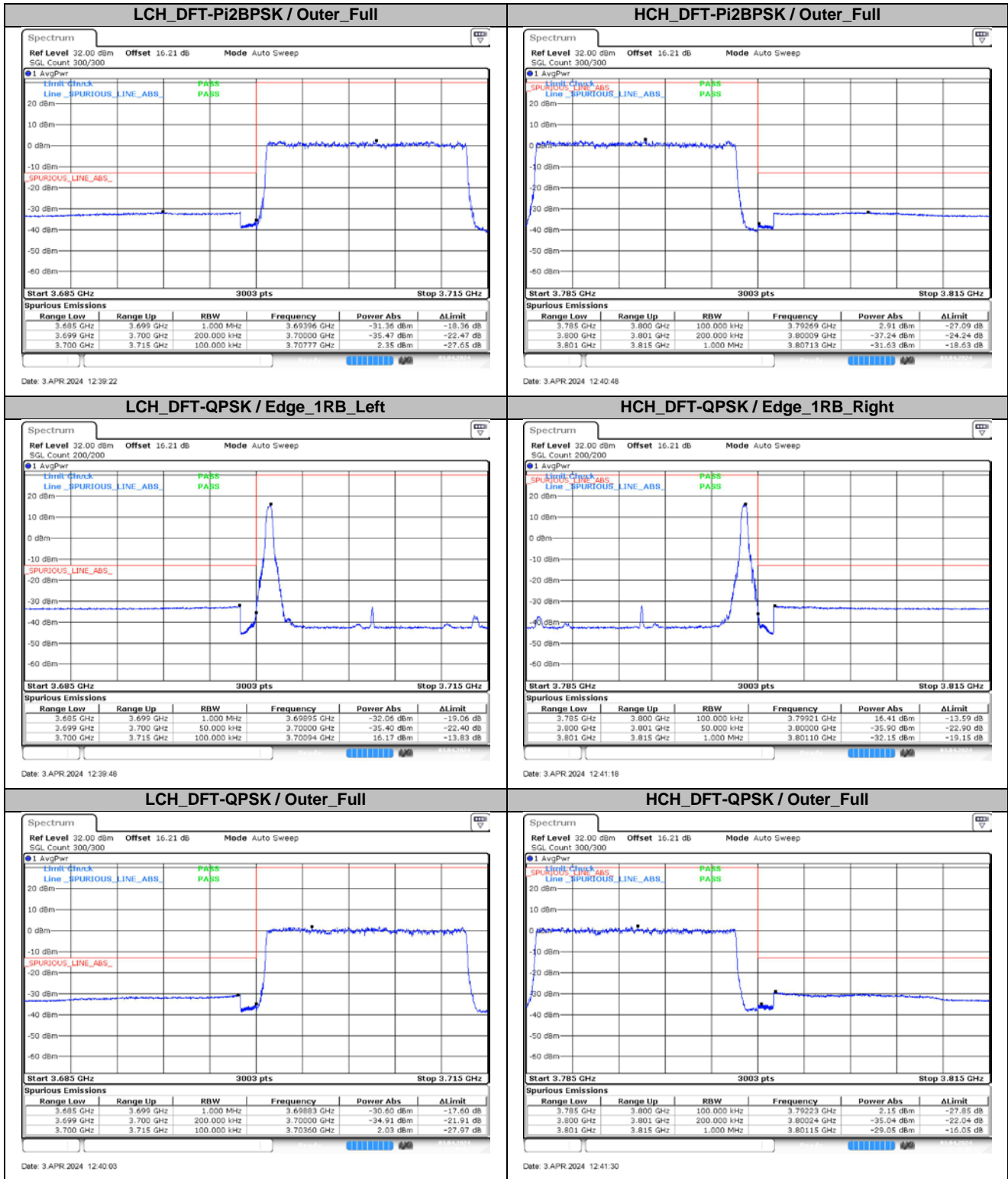
HCH_DFT-QPSK / Edge_1RB_Right



Date: 3 APR 2024 12:35:04

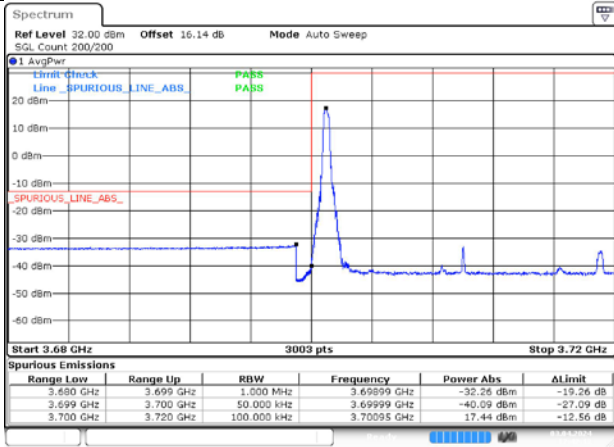






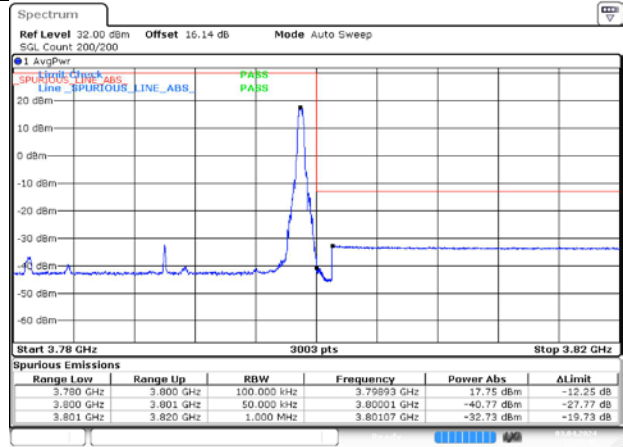
DC_41A_n78A / 30KHz / 20MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



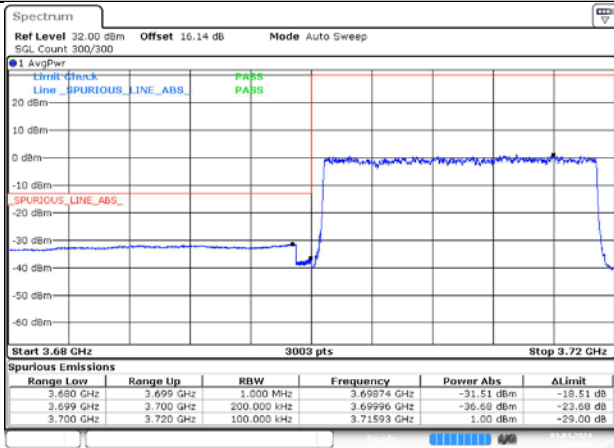
Date: 3 APR 2024 12:42:11

HCH_DFT-Pi2BPSK / Edge_1RB_Right



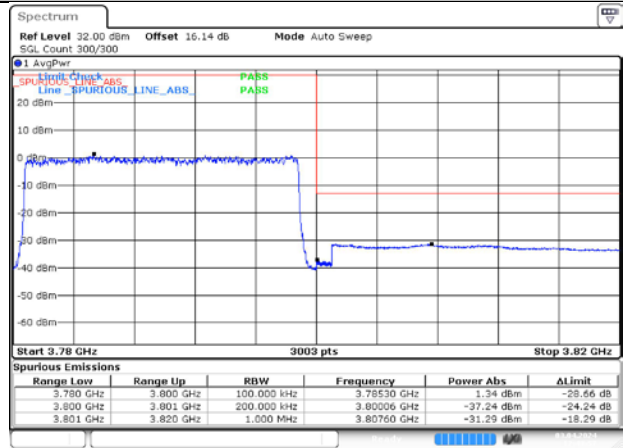
Date: 3 APR 2024 12:43:43

LCH_DFT-Pi2BPSK / Outer_Full



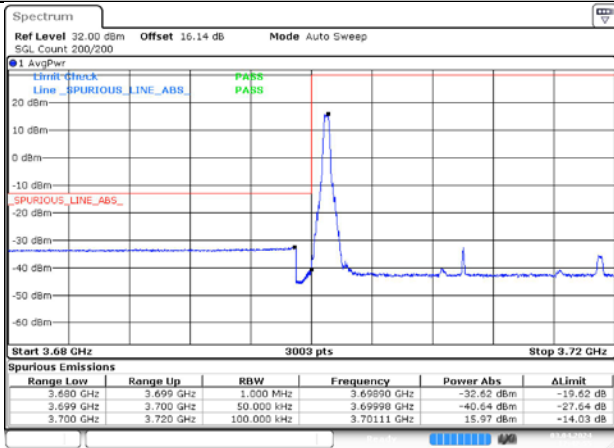
Date: 3 APR 2024 12:42:28

HCH_DFT-Pi2BPSK / Outer_Full



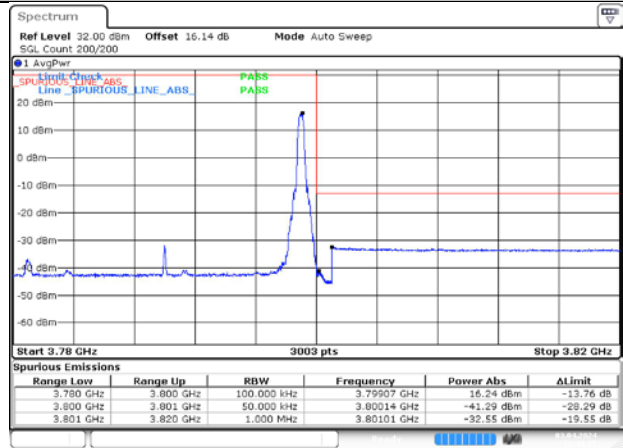
Date: 3 APR 2024 12:43:57

LCH_DFT-QPSK / Edge_1RB_Left

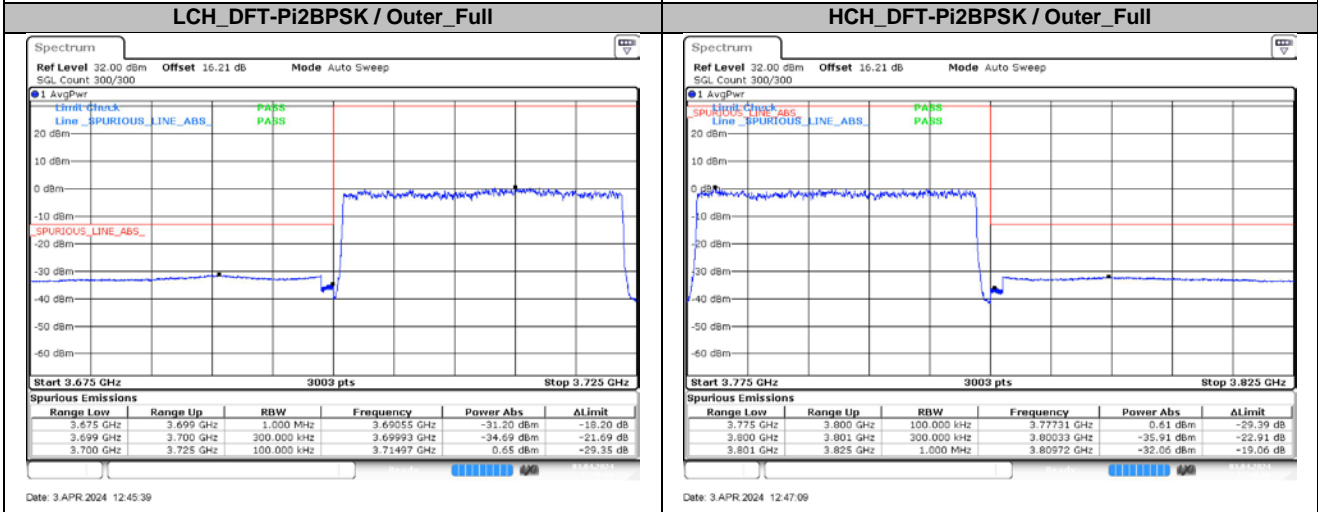
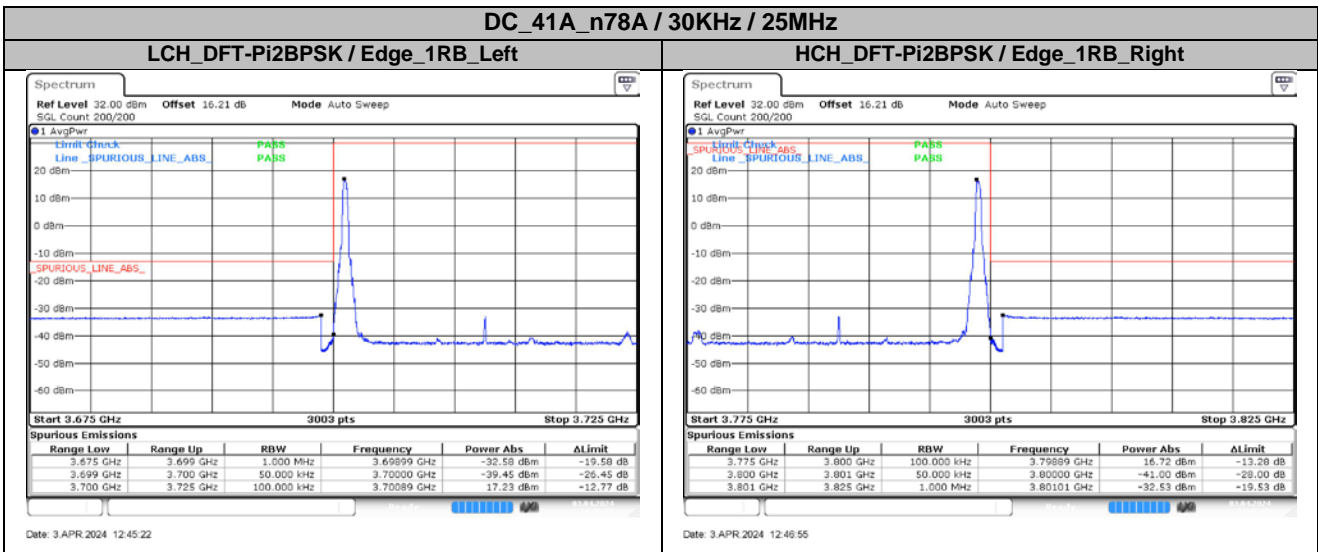
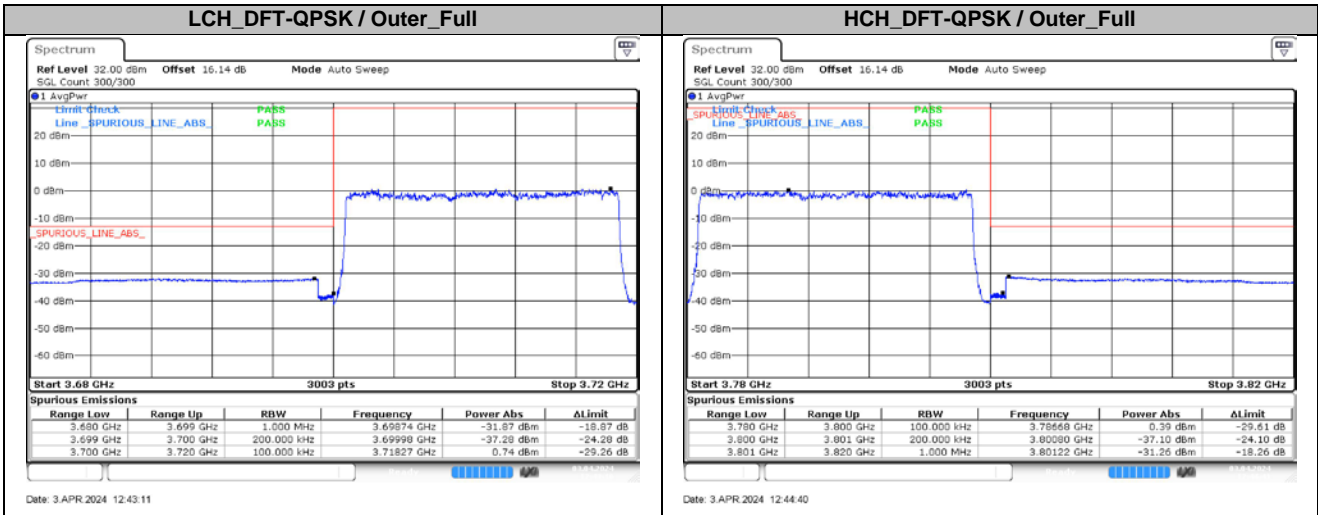


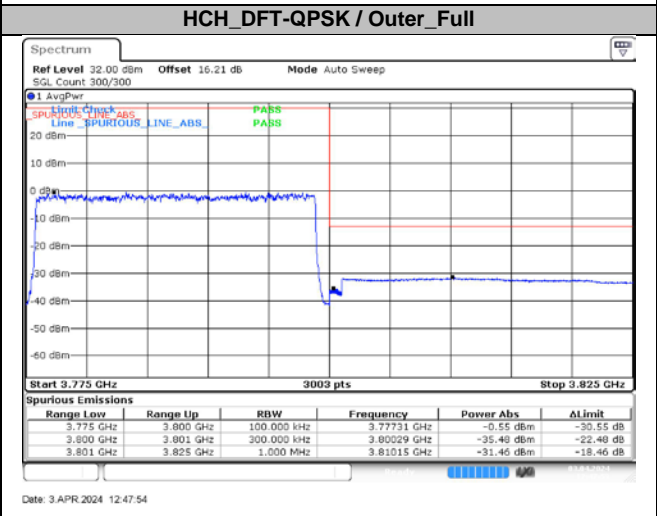
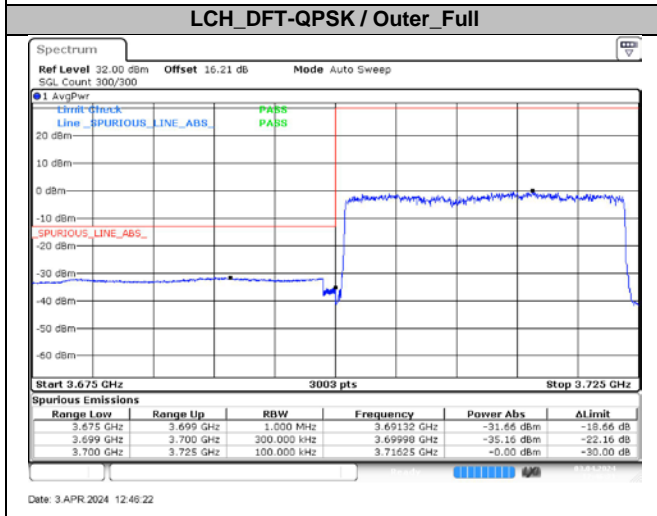
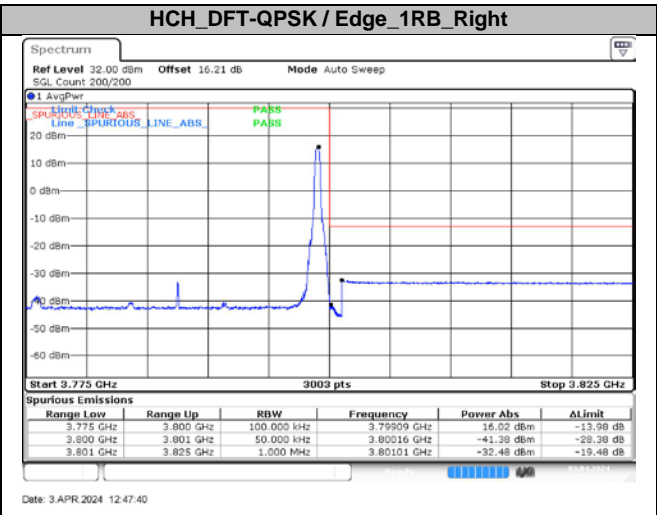
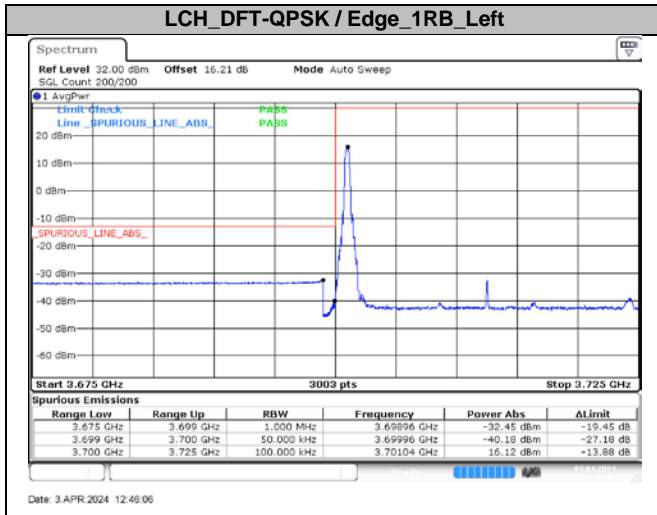
Date: 3 APR 2024 12:42:56

HCH_DFT-QPSK / Edge_1RB_Right

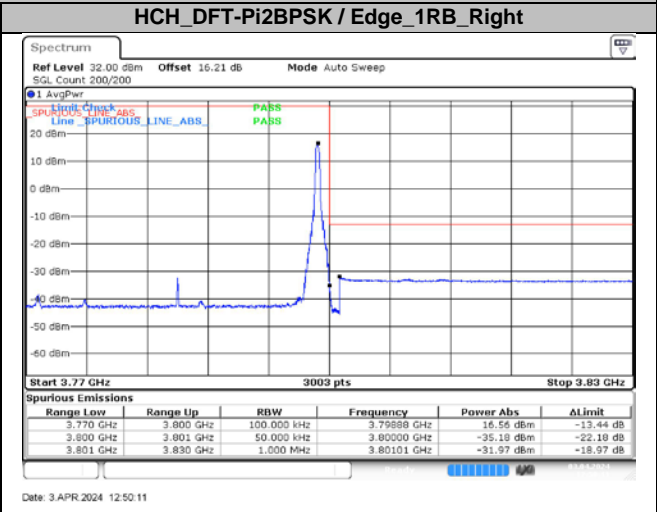
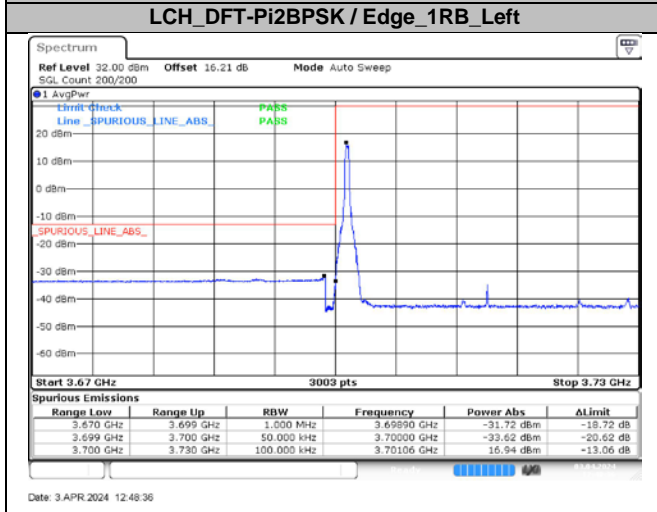


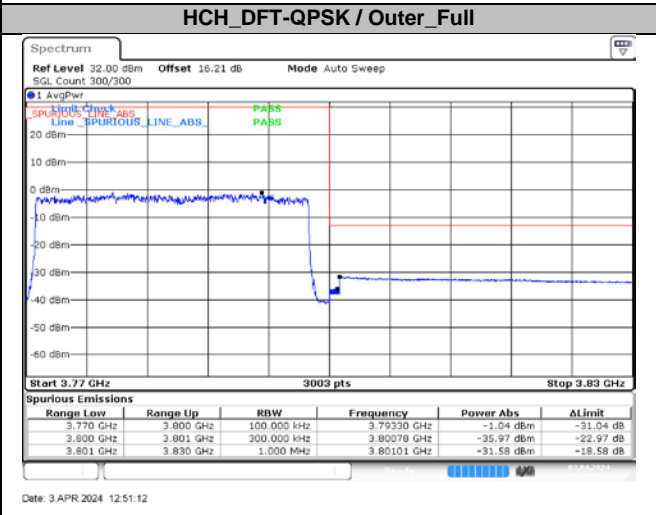
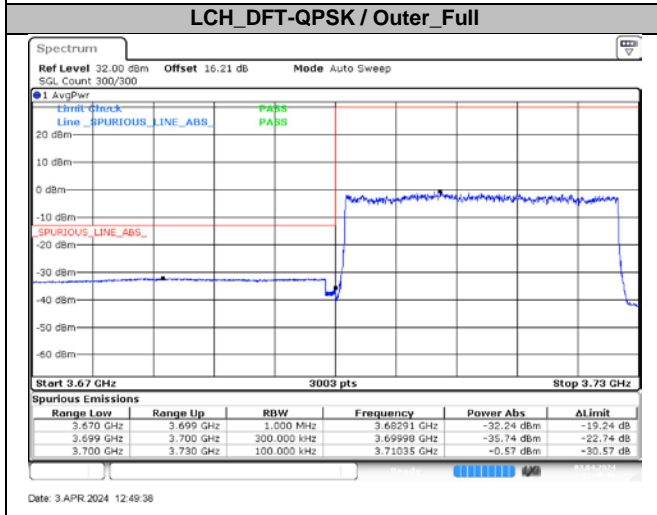
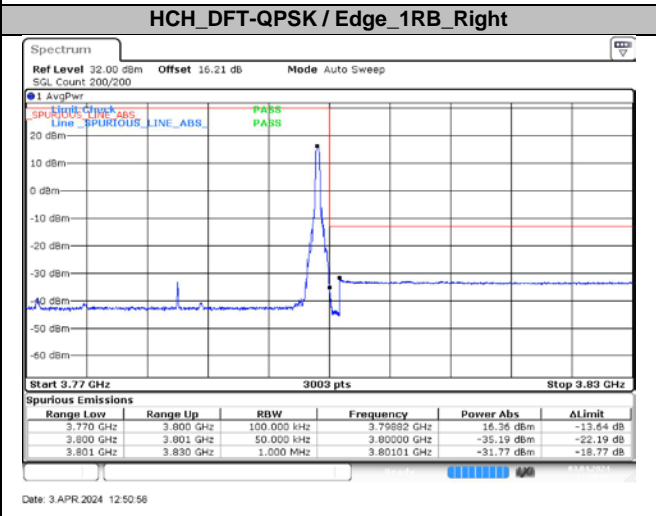
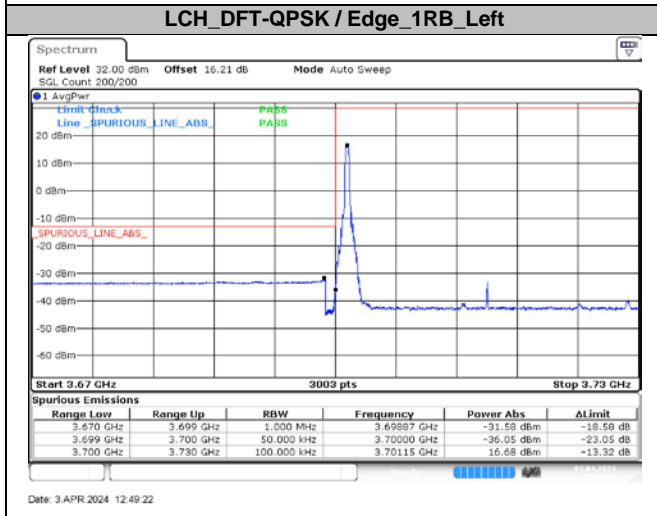
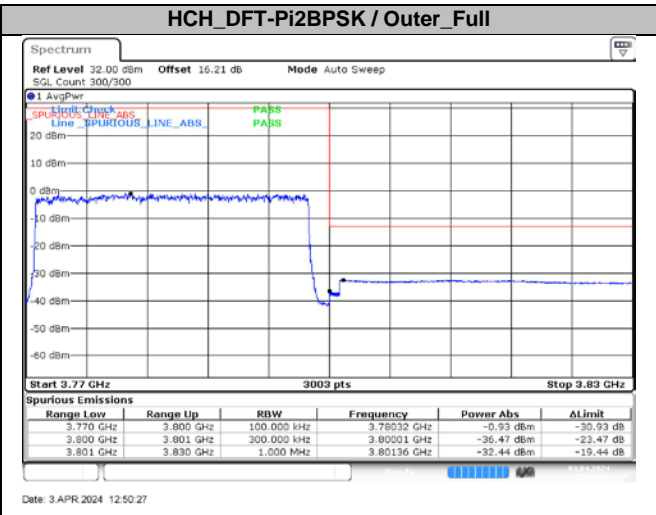
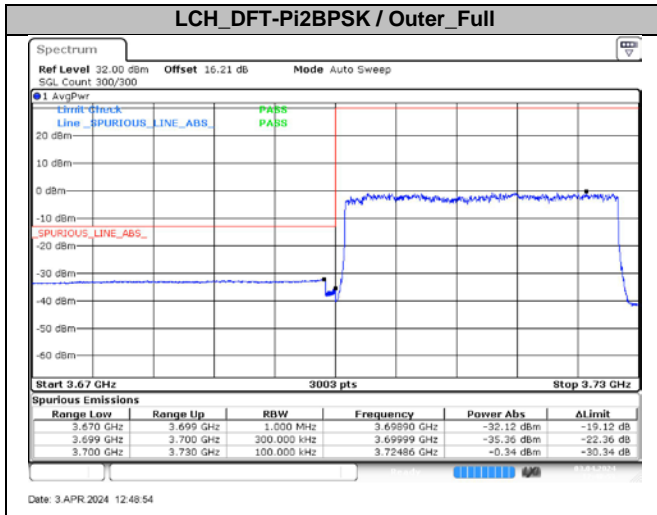
Date: 3 APR 2024 12:44:27





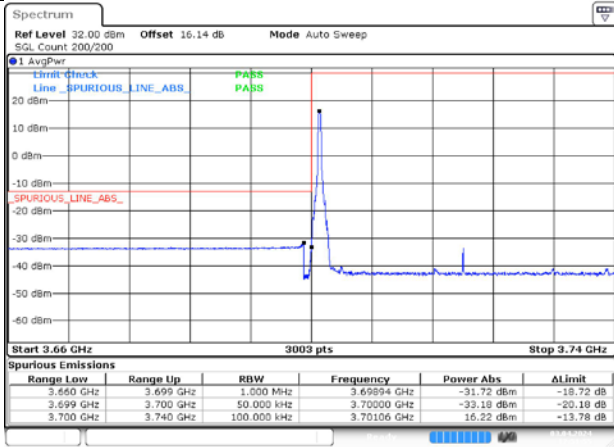
DC_41A_n78A / 30KHz / 30MHz





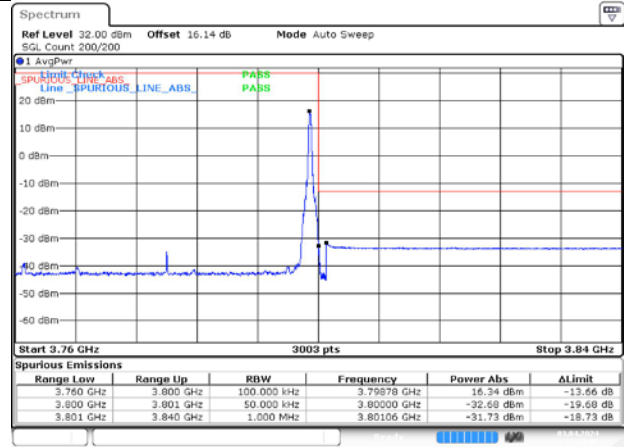
DC_41A_n78A / 30KHz / 40MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



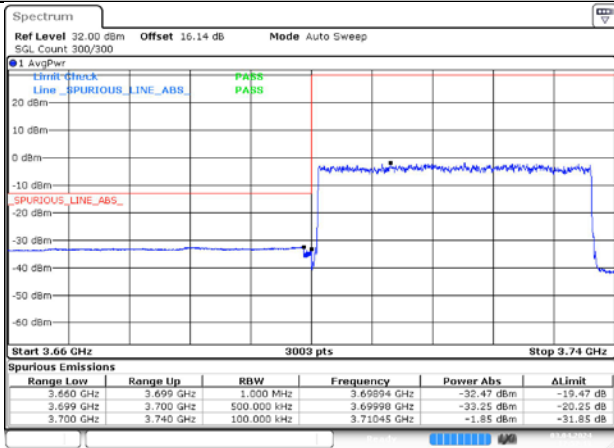
Date: 3 APR 2024 12:51:55

HCH_DFT-Pi2BPSK / Edge_1RB_Right



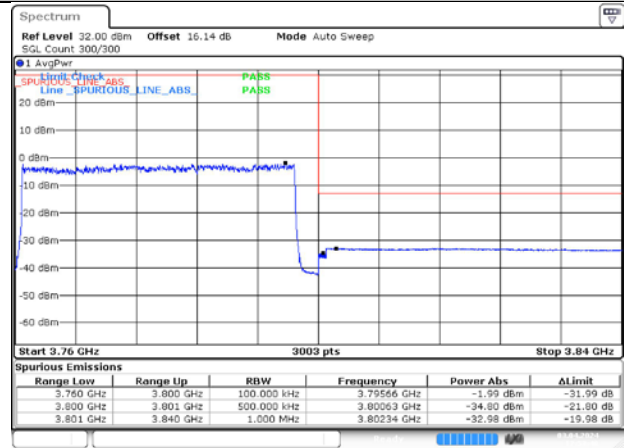
Date: 3 APR 2024 12:53:32

LCH_DFT-Pi2BPSK / Outer_Full



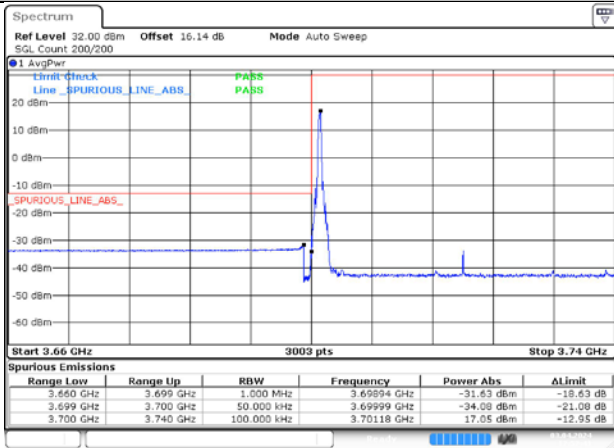
Date: 3 APR 2024 12:52:13

HCH_DFT-Pi2BPSK / Outer_Full



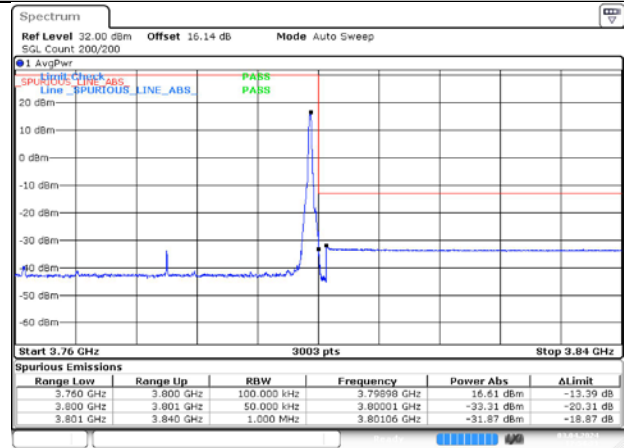
Date: 3 APR 2024 12:53:49

LCH_DFT-QPSK / Edge_1RB_Left

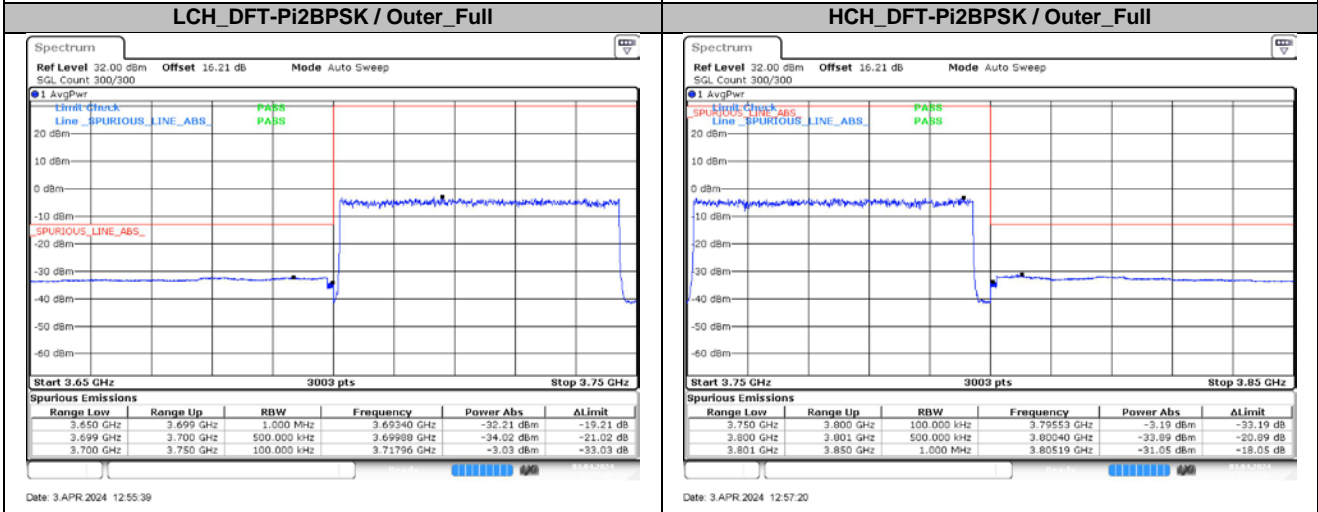
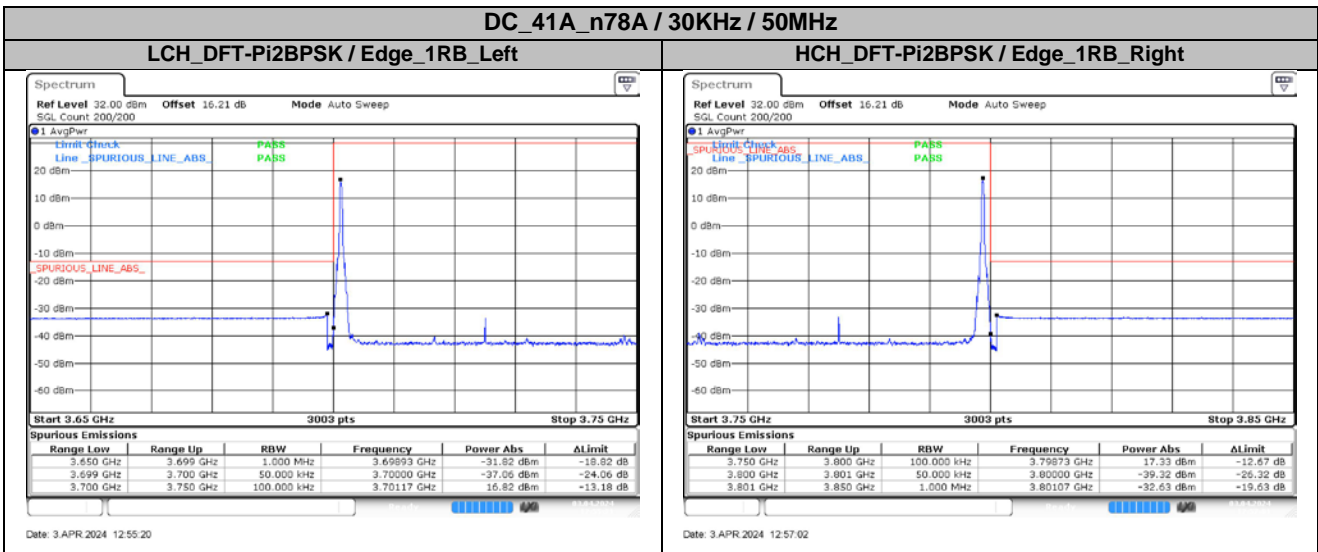
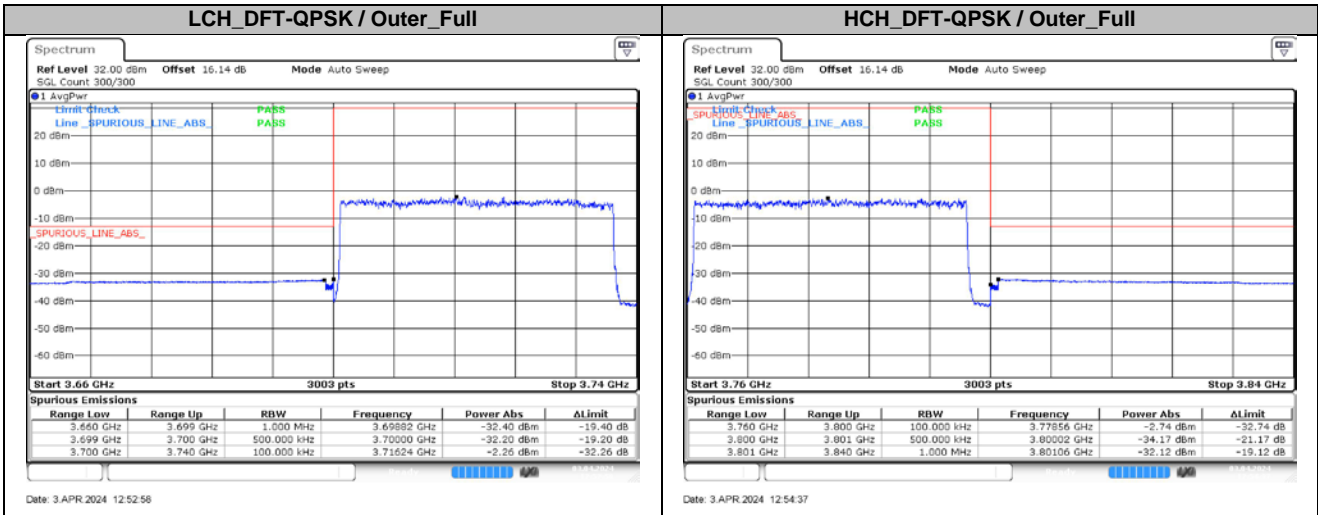


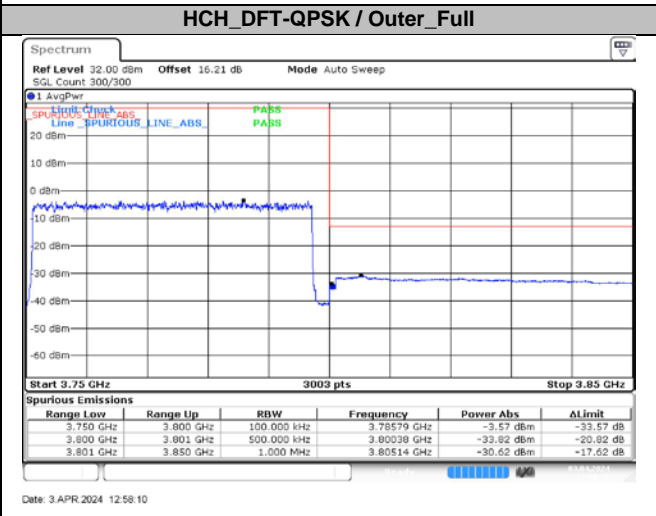
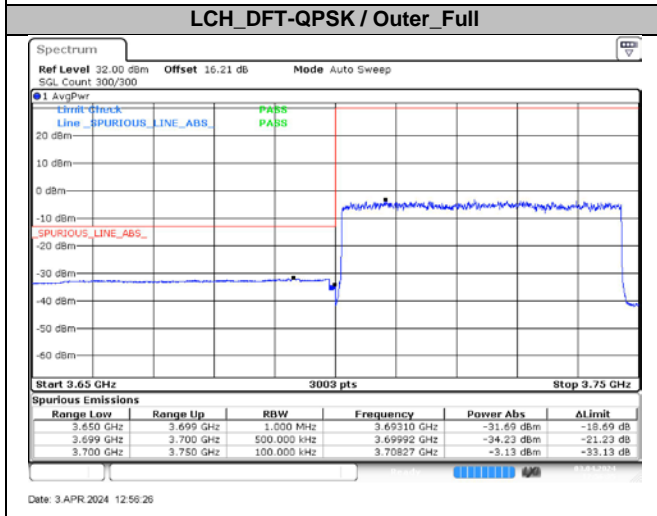
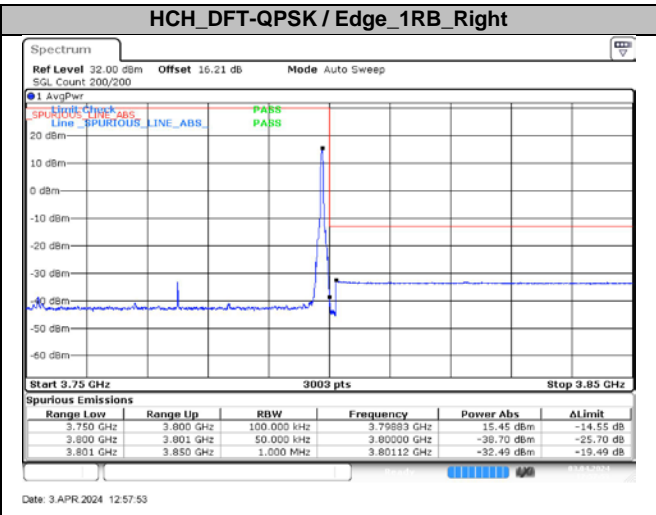
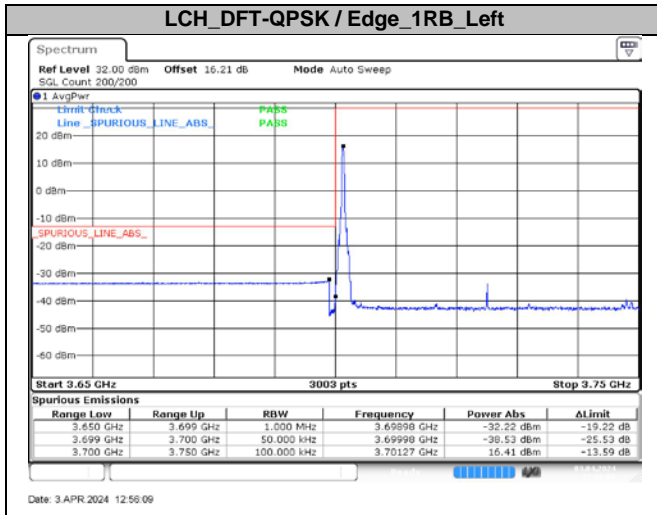
Date: 3 APR 2024 12:52:42

HCH_DFT-QPSK / Edge_1RB_Right

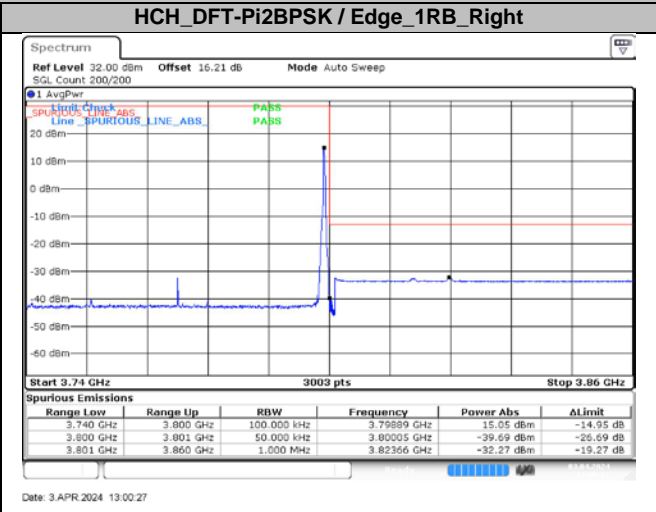
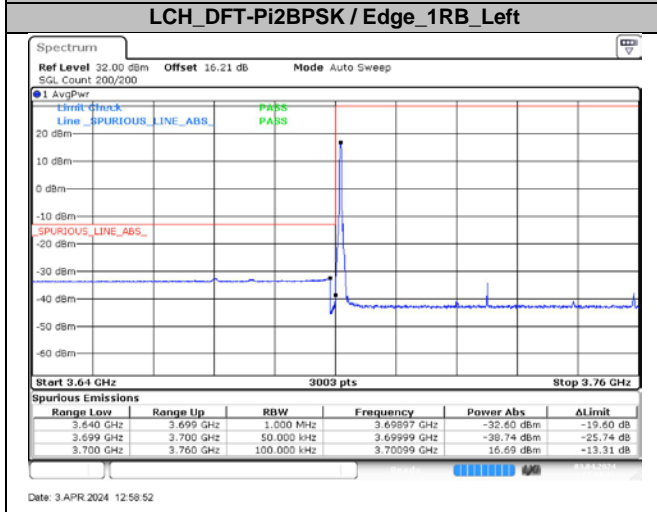


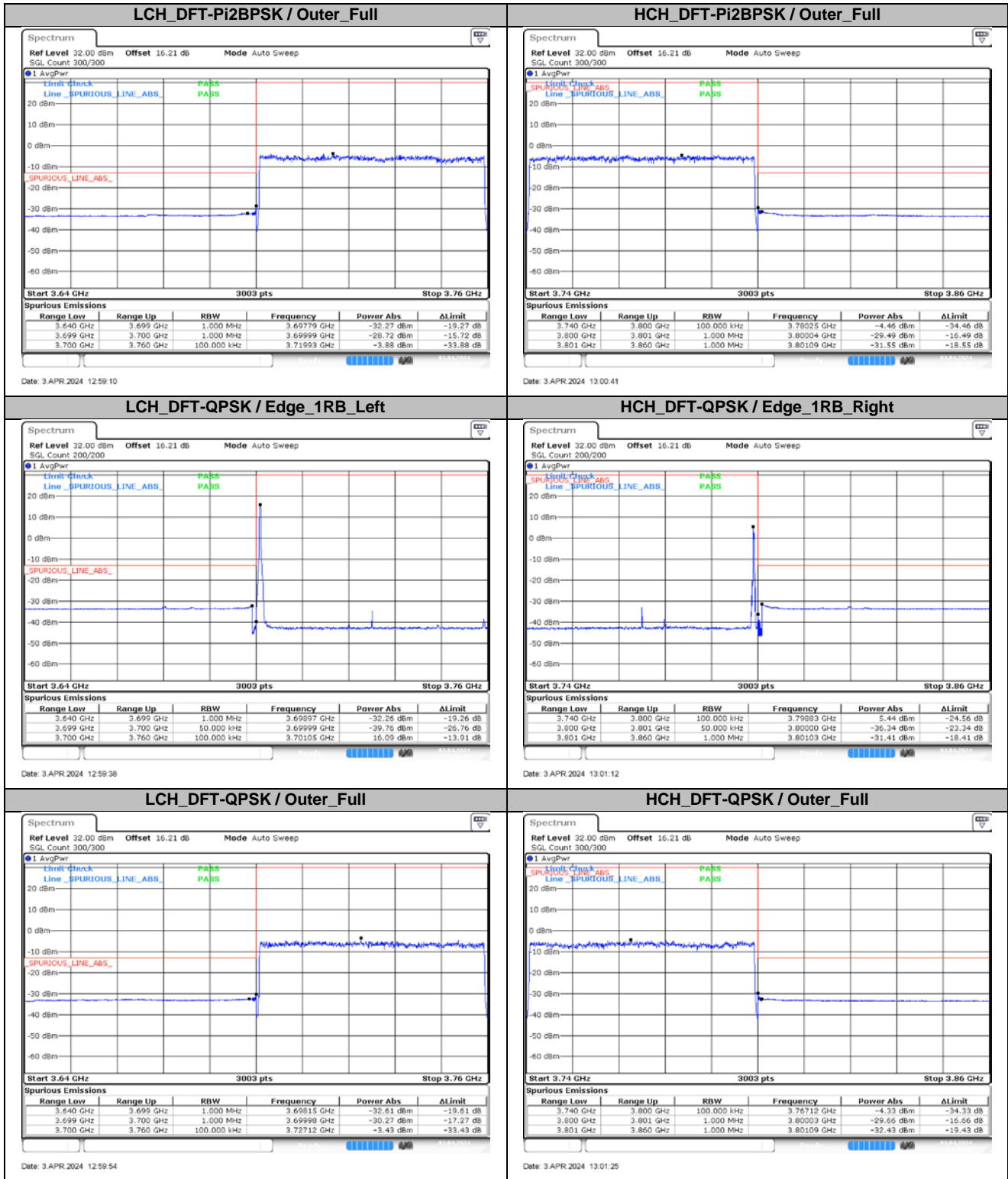
Date: 3 APR 2024 12:54:21





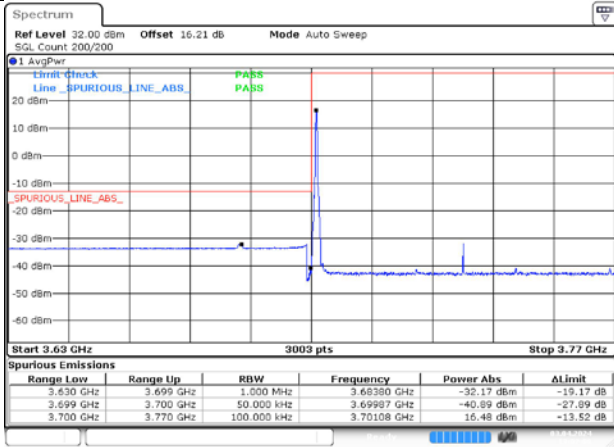
DC_41A_n78A / 30KHz / 60MHz





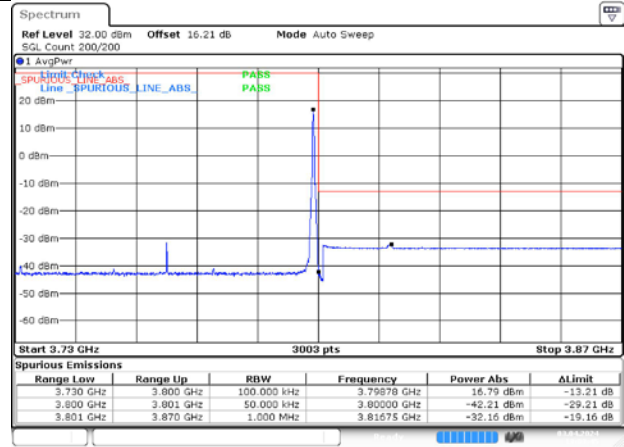
DC_41A_n78A / 30KHz / 70MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



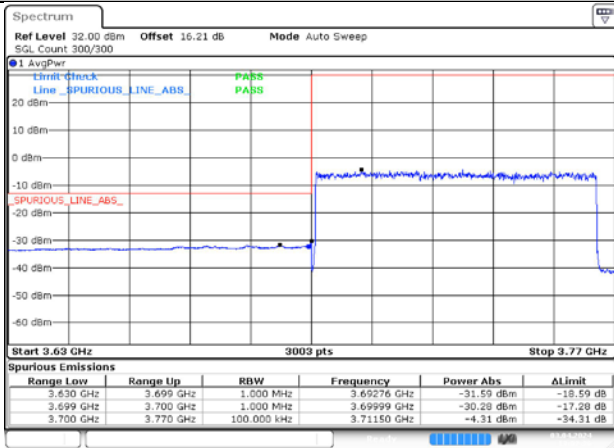
Date: 3 APR 2024 13:02:08

HCH_DFT-Pi2BPSK / Edge_1RB_Right



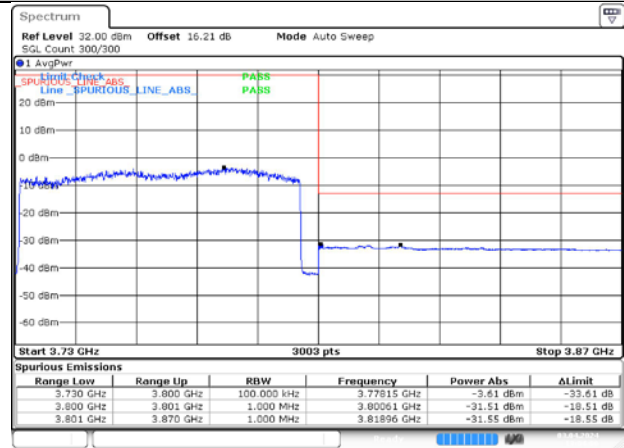
Date: 3 APR 2024 13:03:45

LCH_DFT-Pi2BPSK / Outer_Full



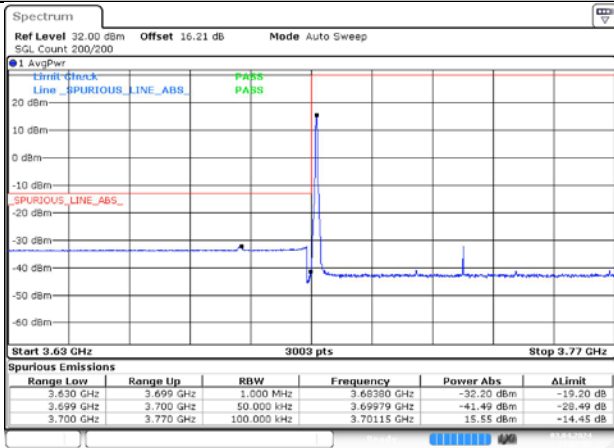
Date: 3 APR 2024 13:02:26

HCH_DFT-Pi2BPSK / Outer_Full



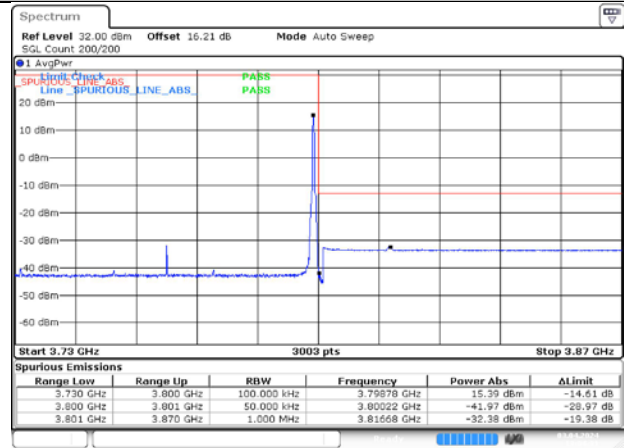
Date: 3 APR 2024 13:03:59

LCH_DFT-QPSK / Edge_1RB_Left



Date: 3 APR 2024 13:02:55

HCH_DFT-QPSK / Edge_1RB_Right



Date: 3 APR 2024 13:04:31

