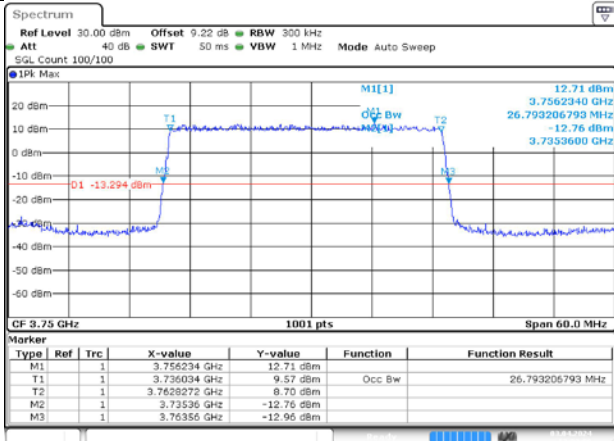


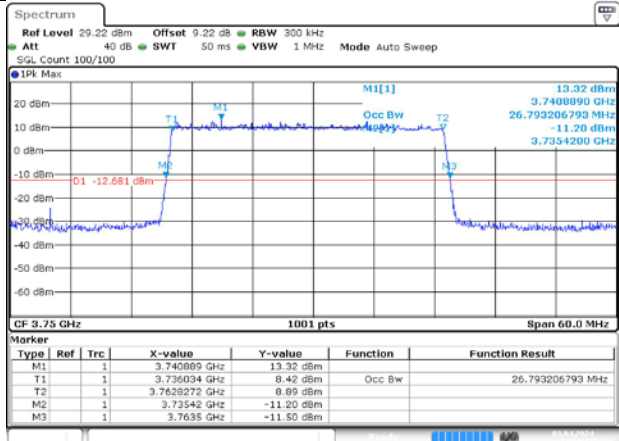
DC\_41A\_n78A / 30KHz / 30MHz

MCH\_DFT-Pi2BPSK / Outer\_Full



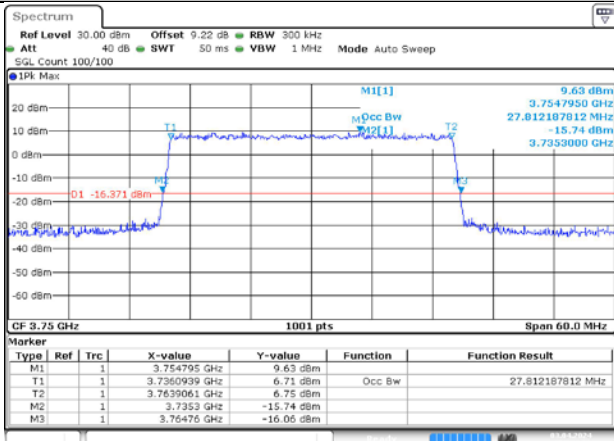
Date: 3 APR 2024 12:02:00

MCH\_DFT-QPSK / Outer\_Full



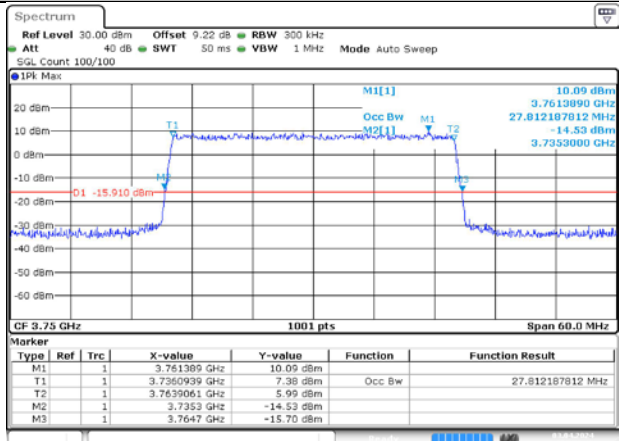
Date: 3 APR 2024 12:02:17

MCH\_CP-QPSK / Outer\_Full



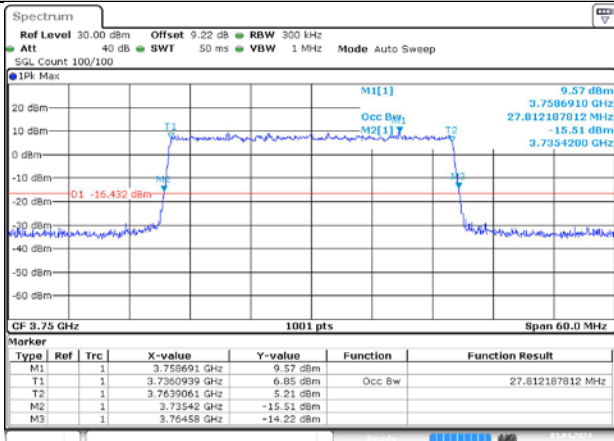
Date: 3 APR 2024 12:02:29

MCH\_CP-16QAM / Outer\_Full



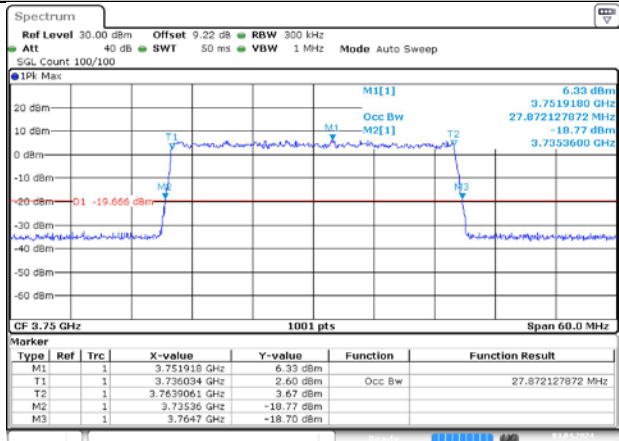
Date: 3 APR 2024 12:02:43

MCH\_CP-64QAM / Outer\_Full

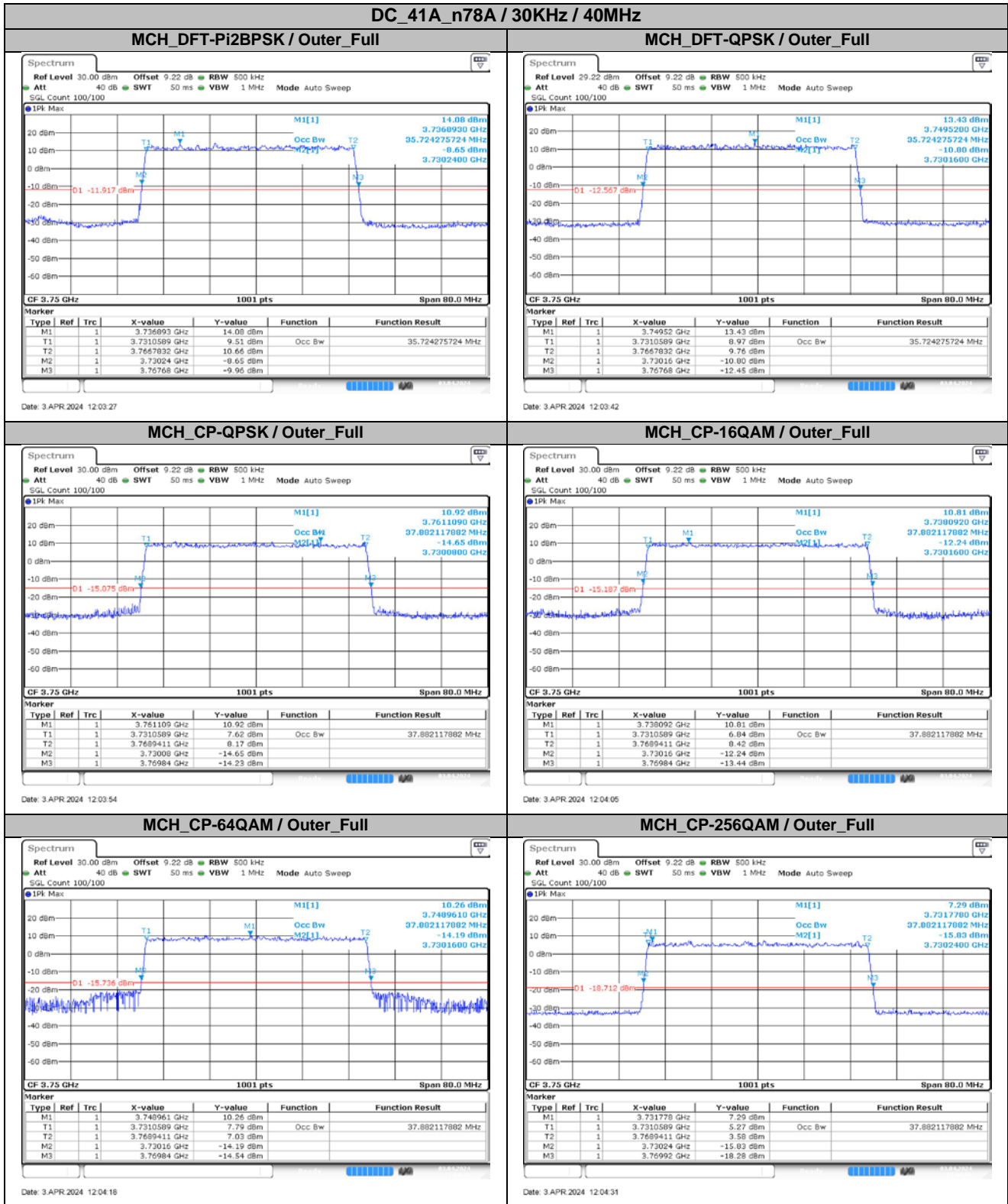


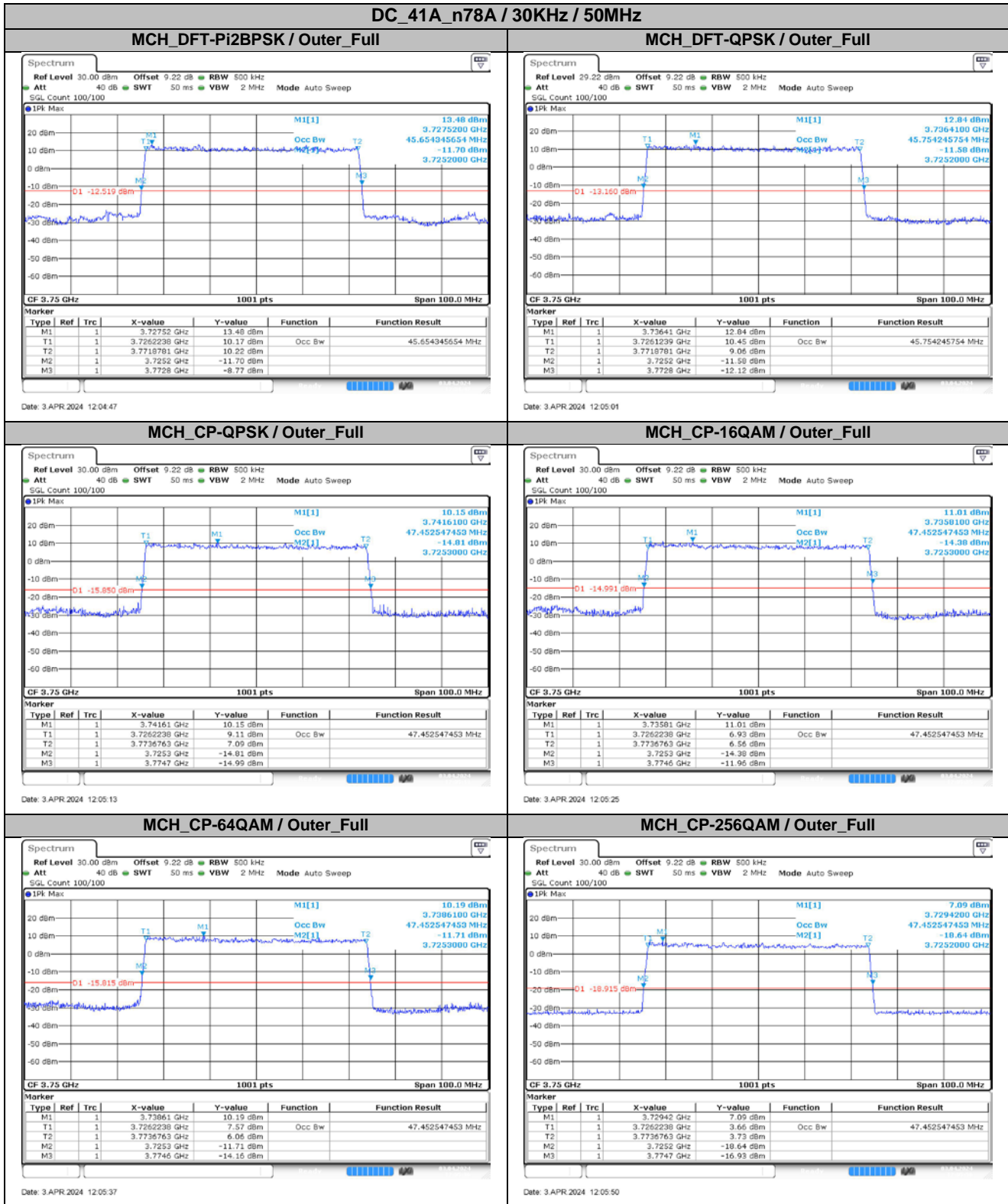
Date: 3 APR 2024 12:02:57

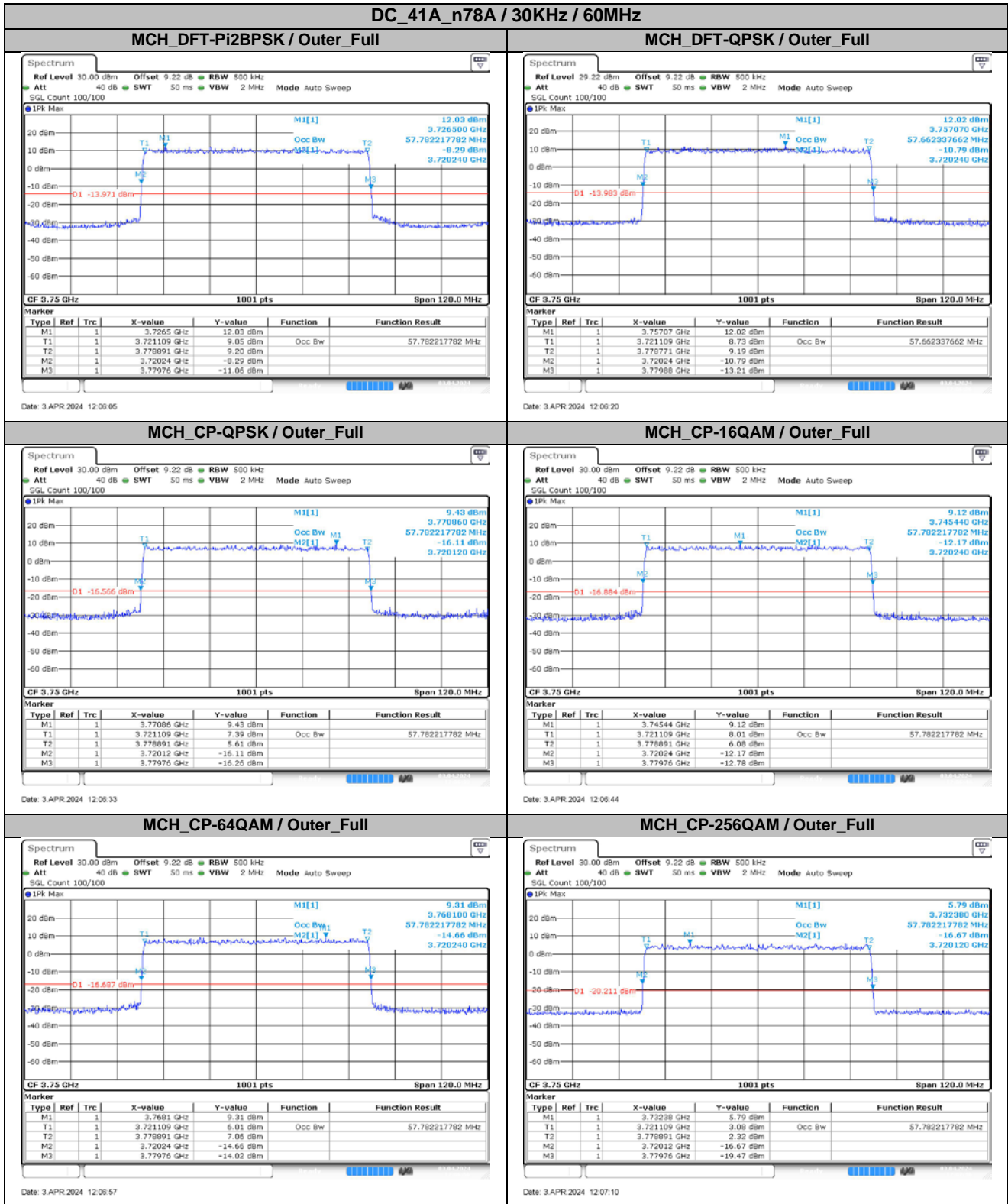
MCH\_CP-256QAM / Outer\_Full

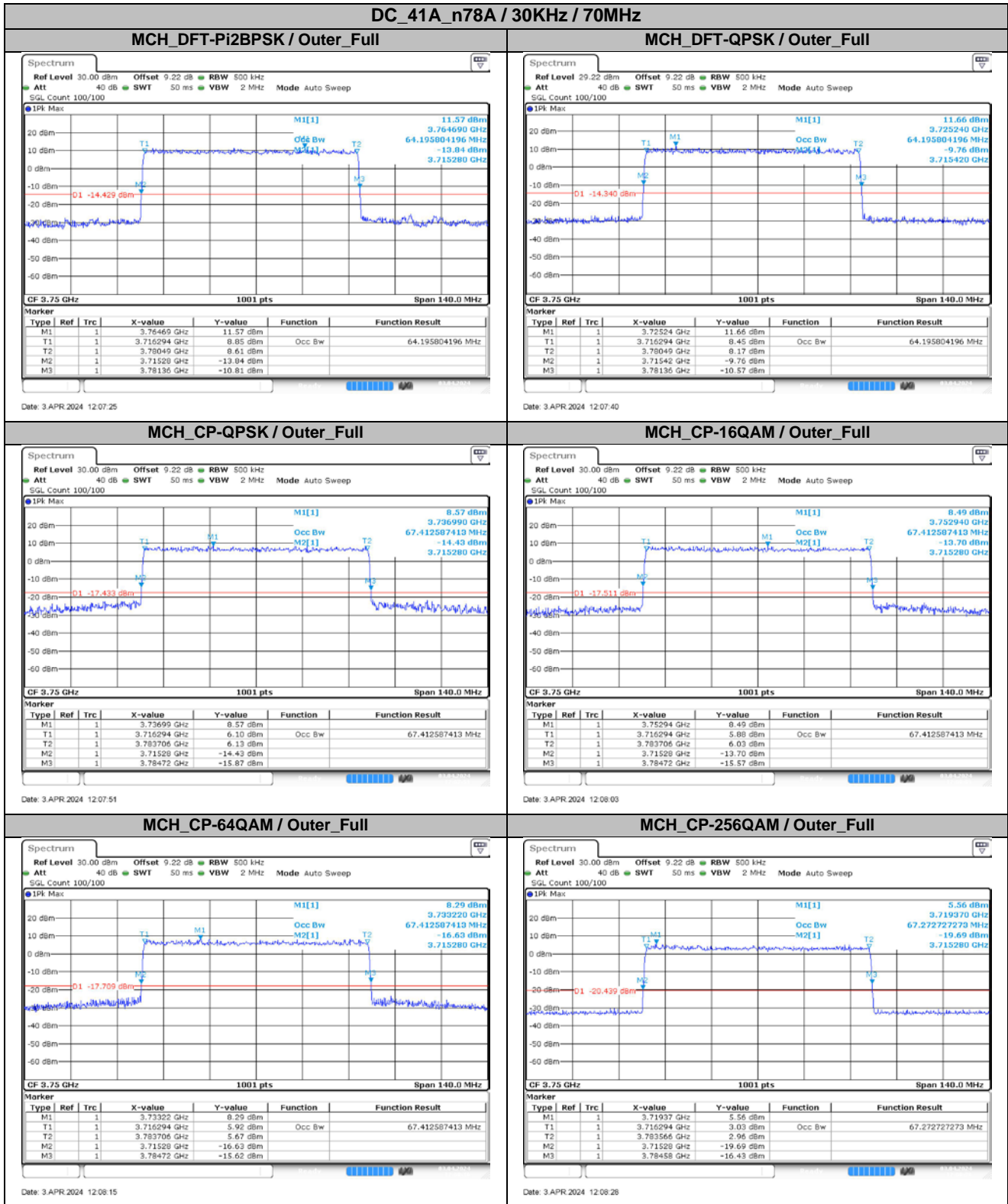


Date: 3 APR 2024 12:03:11



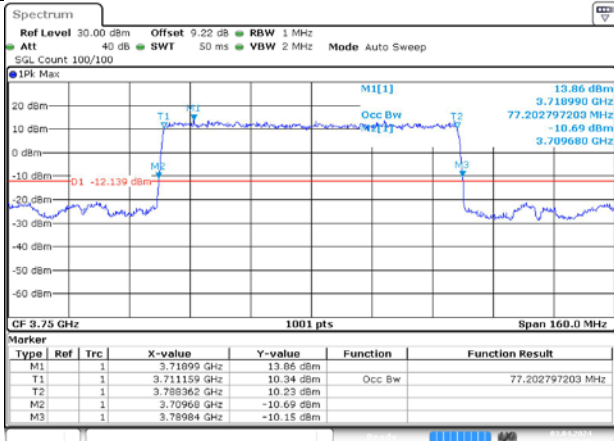






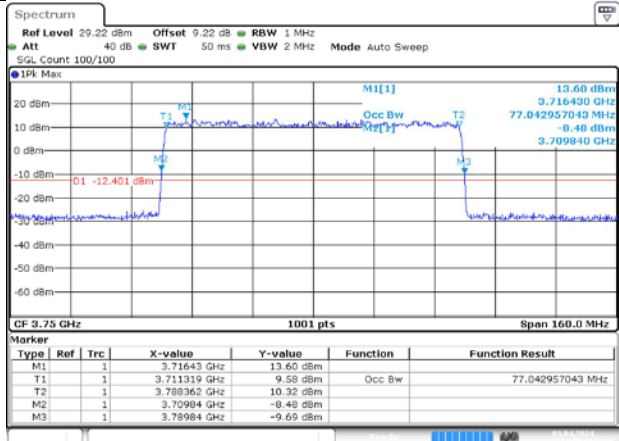
DC\_41A\_n78A / 30KHz / 80MHz

MCH\_DFT-Pi2BPSK / Outer\_Full



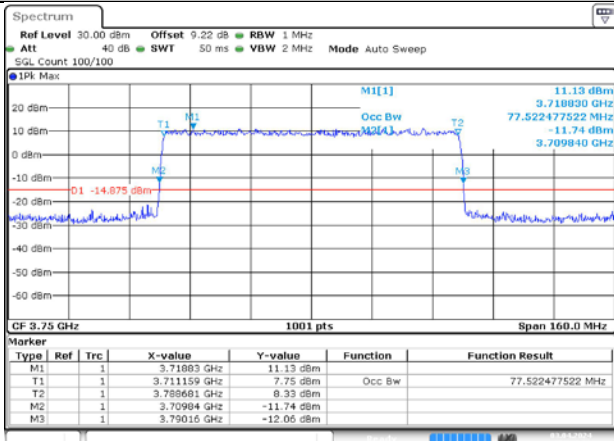
Date: 3 APR 2024 12:08:43

MCH\_DFT-QPSK / Outer\_Full



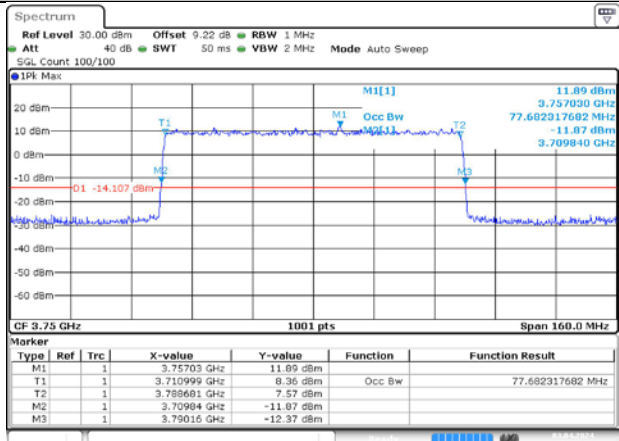
Date: 3 APR 2024 12:08:58

MCH\_CP-QPSK / Outer\_Full



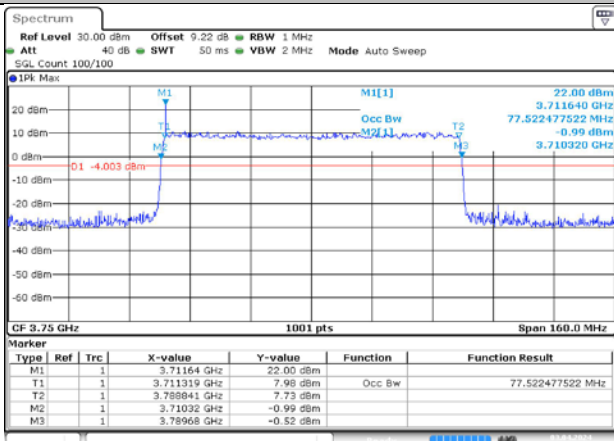
Date: 3 APR 2024 12:09:09

MCH\_CP-16QAM / Outer\_Full



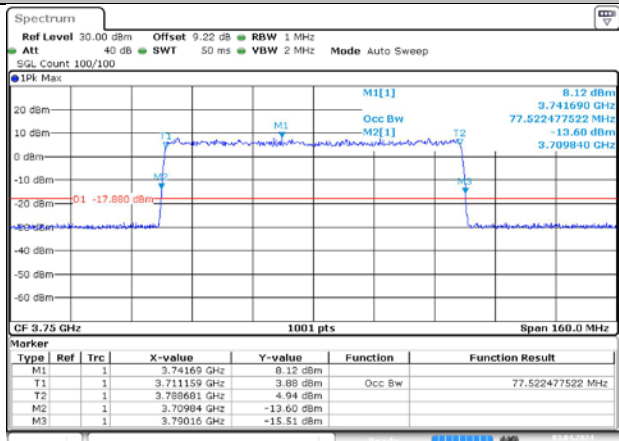
Date: 3 APR 2024 12:09:21

MCH\_CP-64QAM / Outer\_Full

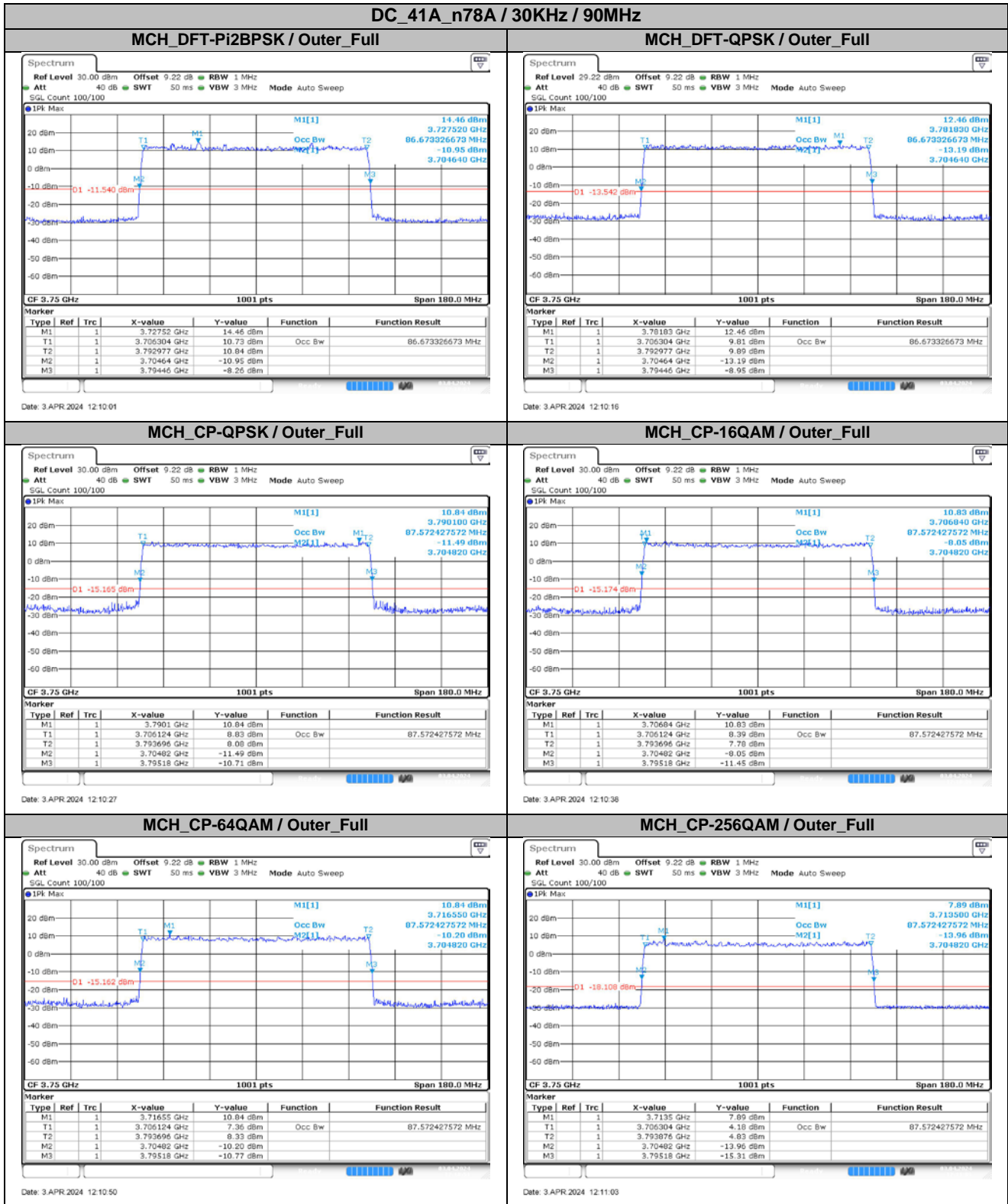


Date: 3 APR 2024 12:09:33

MCH\_CP-256QAM / Outer\_Full

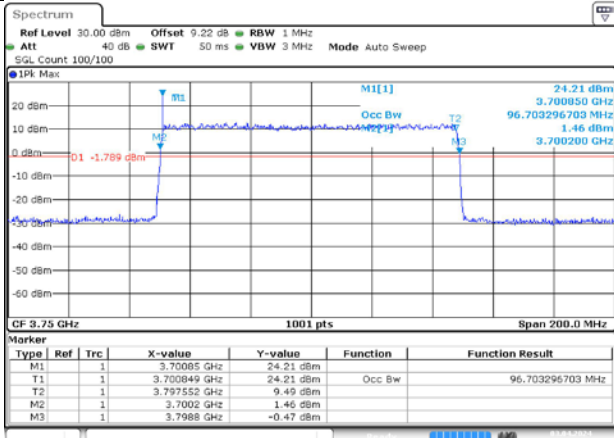


Date: 3 APR 2024 12:09:46



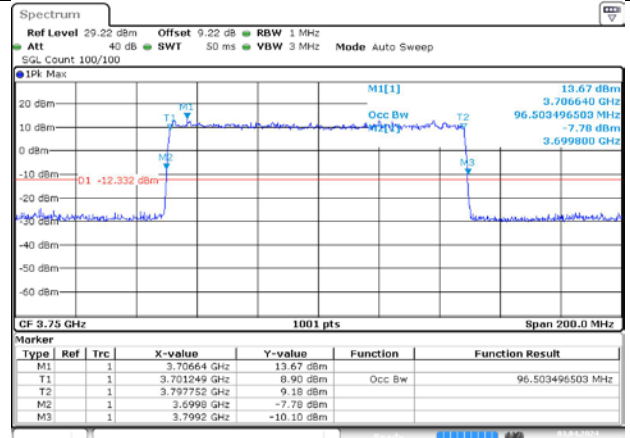
DC\_41A\_n78A / 30KHz / 100MHz

MCH\_DFT-Pi2BPSK / Outer\_Full



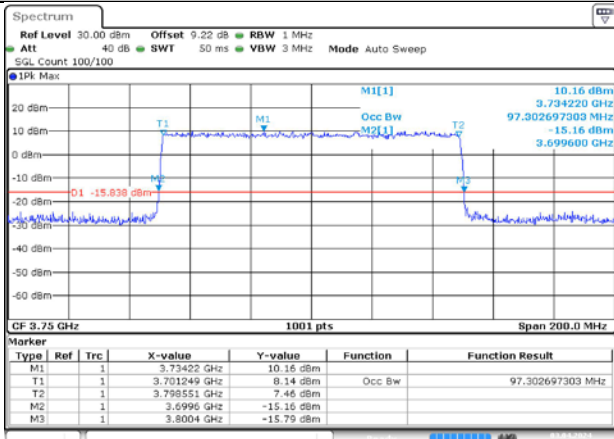
Date: 3 APR 2024 12:11:18

MCH\_DFT-QPSK / Outer\_Full



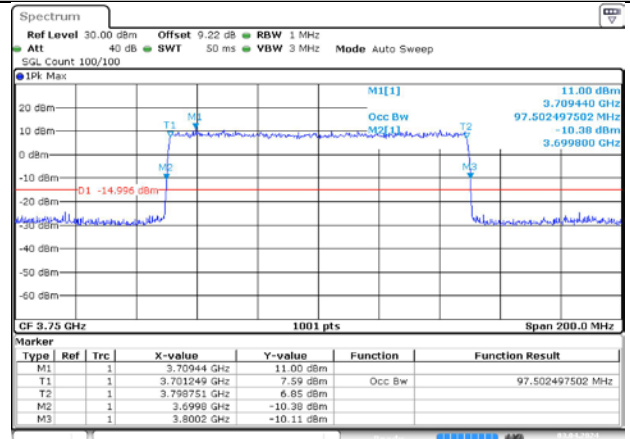
Date: 3 APR 2024 12:11:34

MCH\_CP-QPSK / Outer\_Full



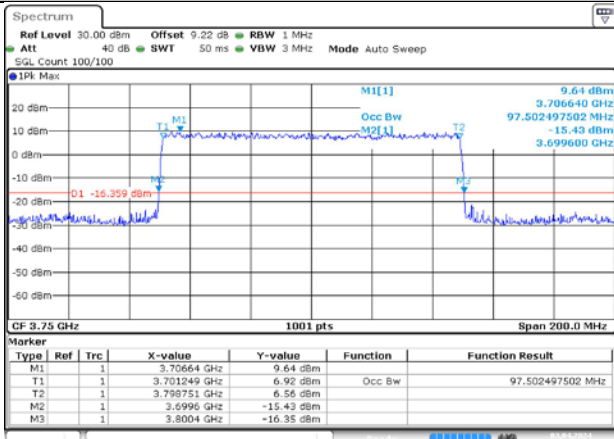
Date: 3 APR 2024 12:11:46

MCH\_CP-16QAM / Outer\_Full



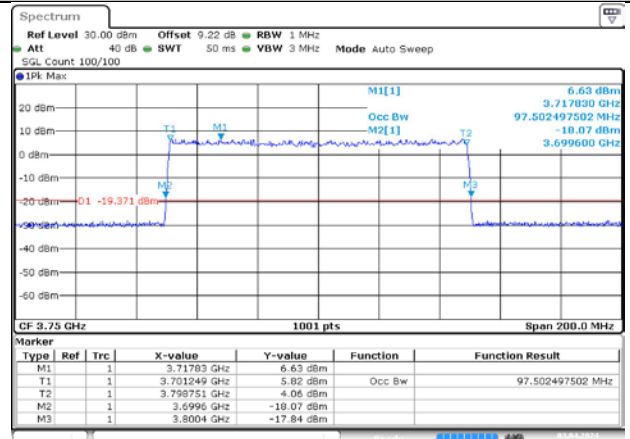
Date: 3 APR 2024 12:11:57

MCH\_CP-64QAM / Outer\_Full



Date: 3 APR 2024 12:12:10

MCH\_CP-256QAM / Outer\_Full

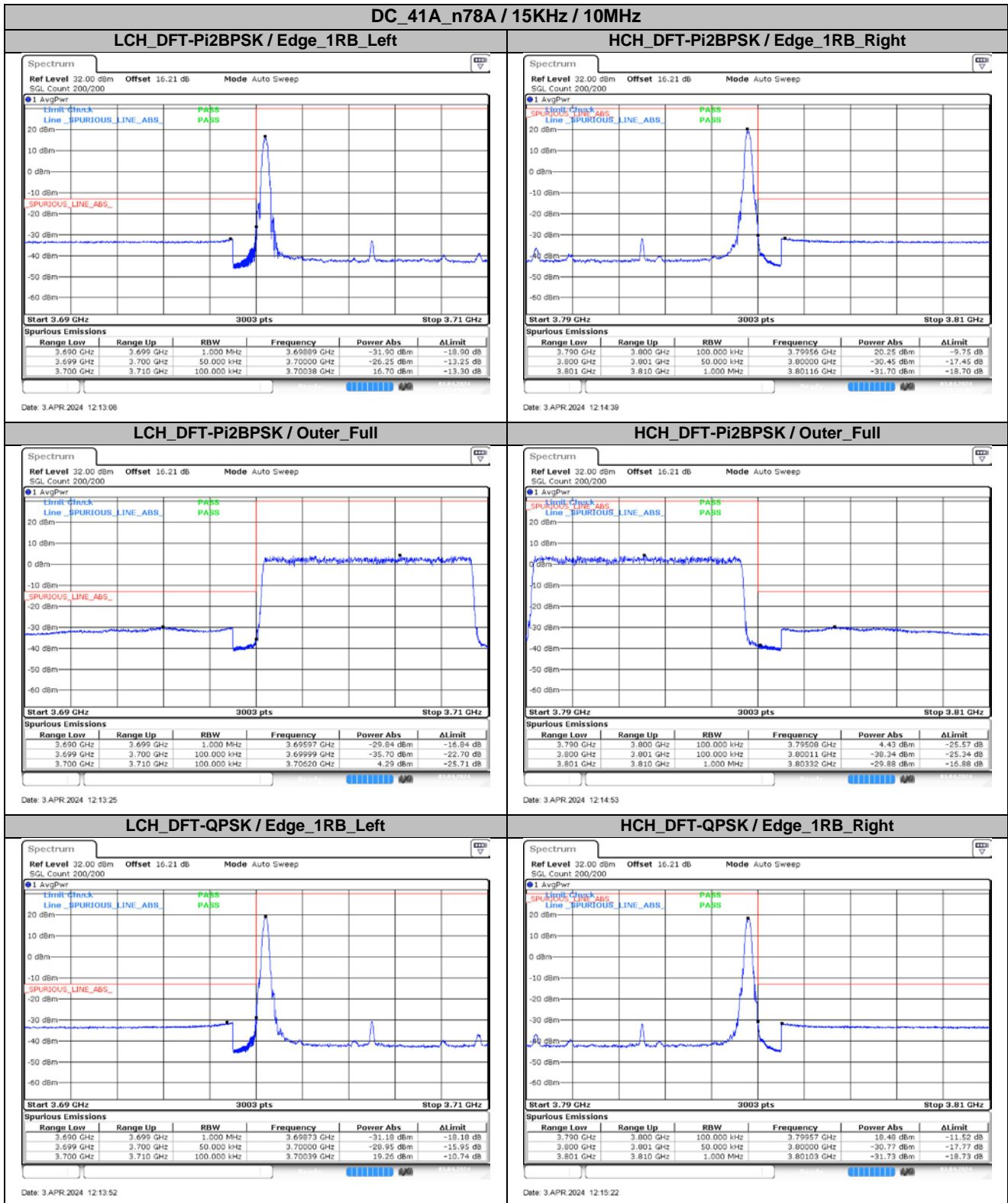


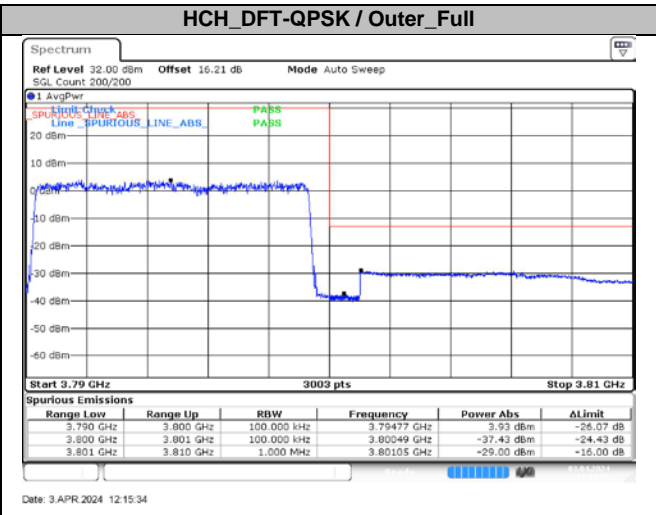
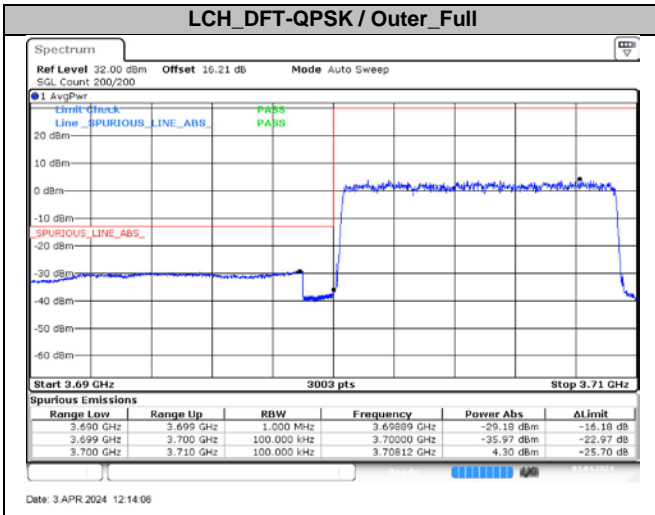
Date: 3 APR 2024 12:12:24



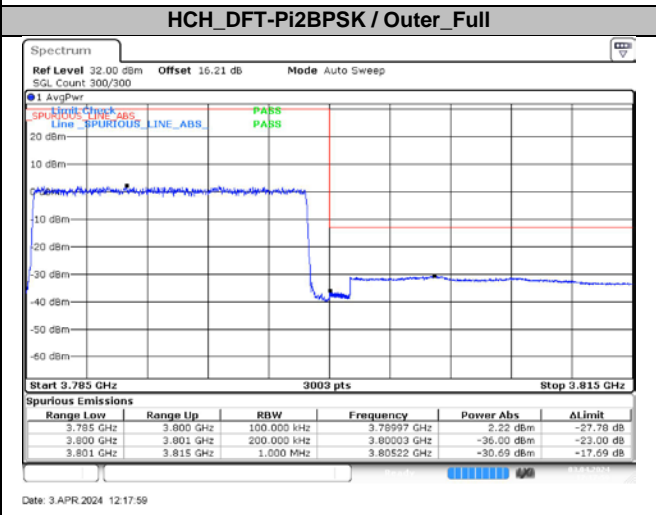
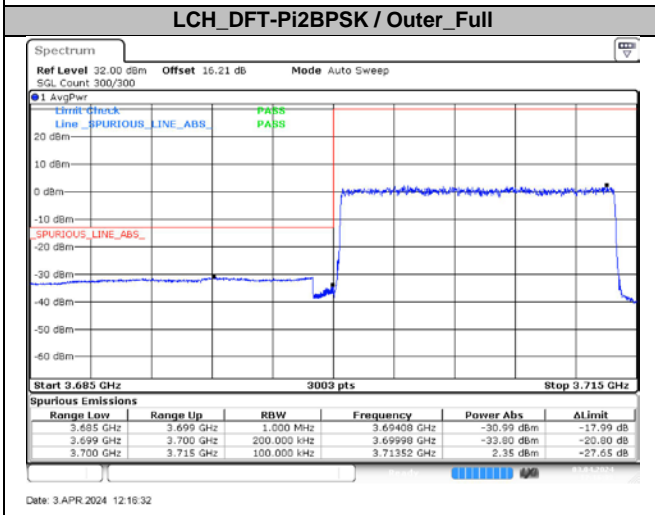
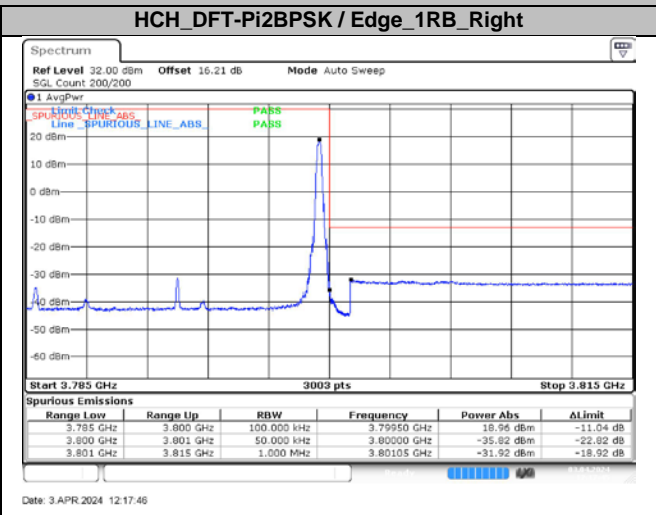
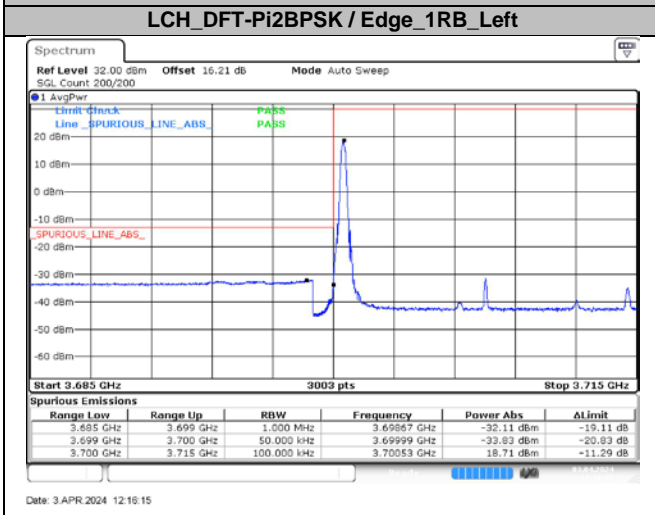
### 5. Conducted Band Edges

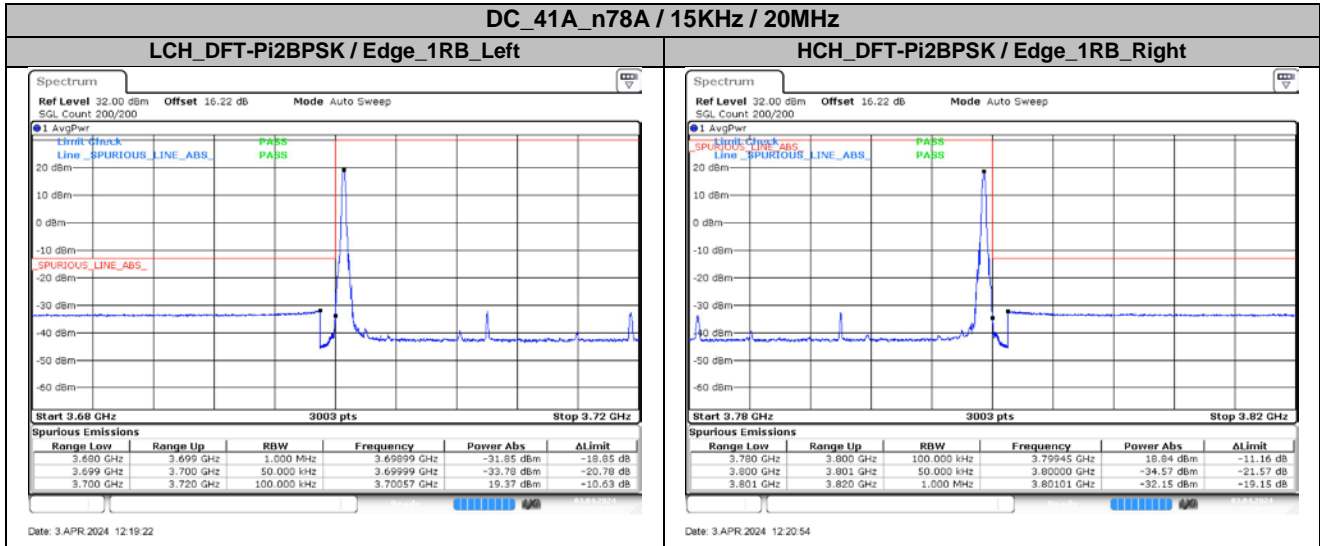
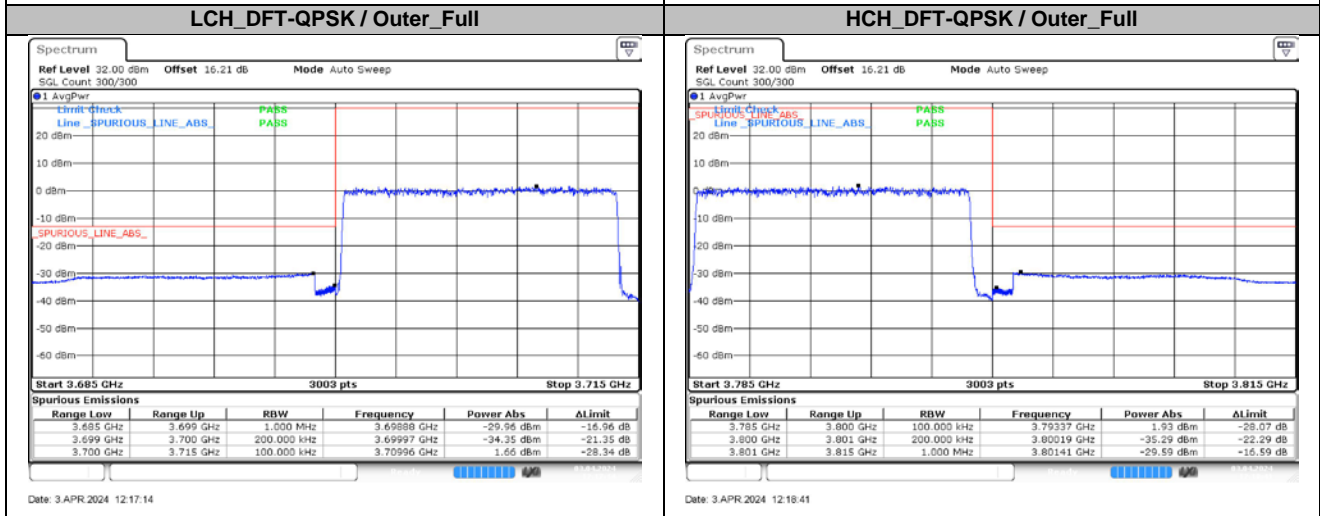
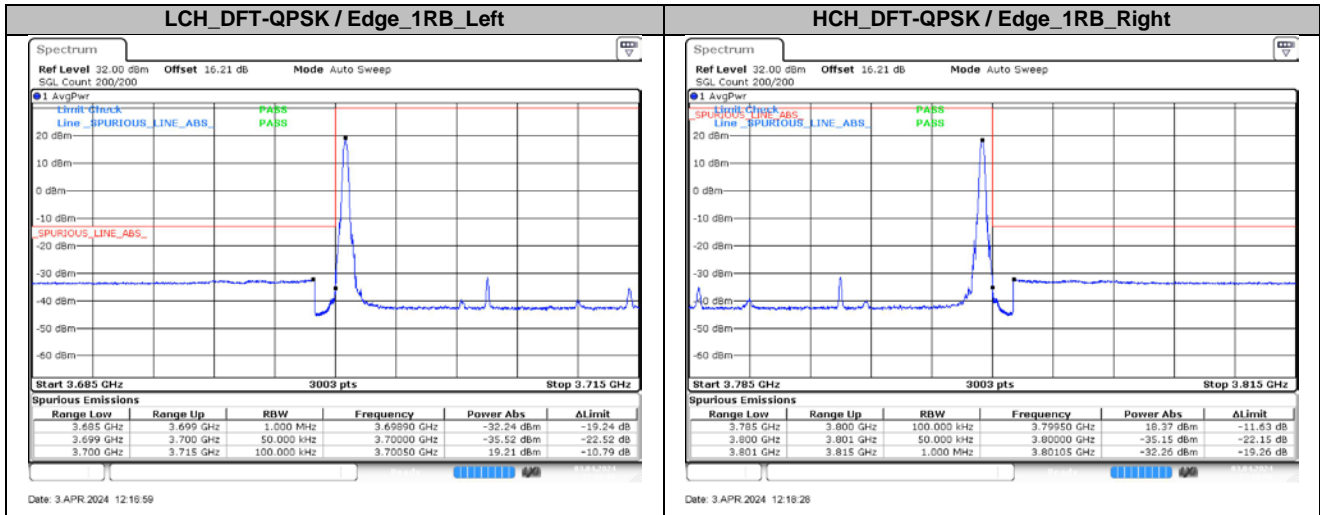
#### 5.1. Test Plots

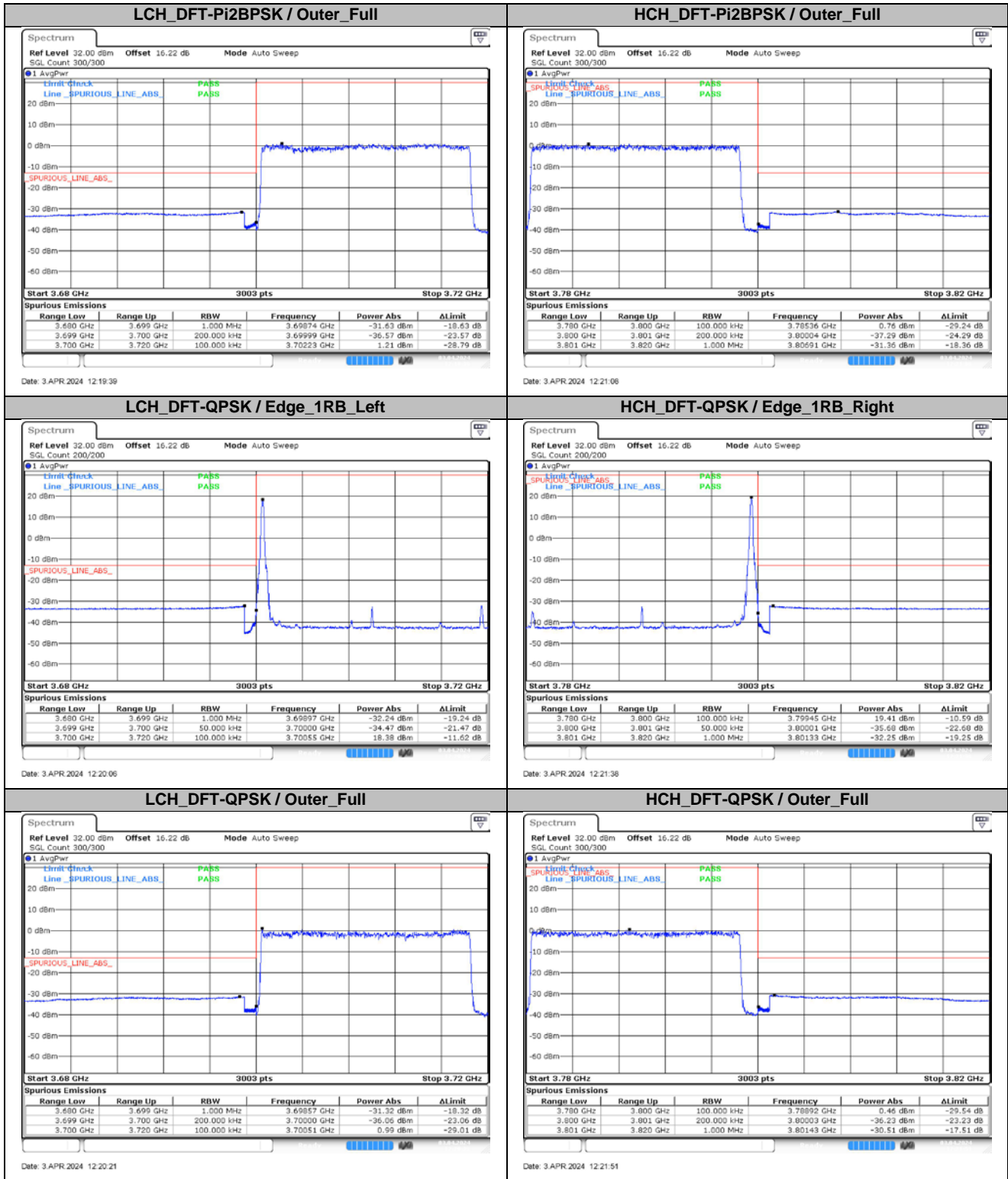




## DC\_41A\_n78A / 15KHz / 15MHz

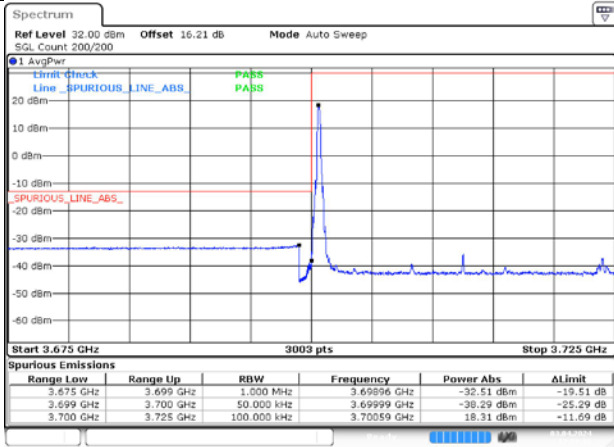






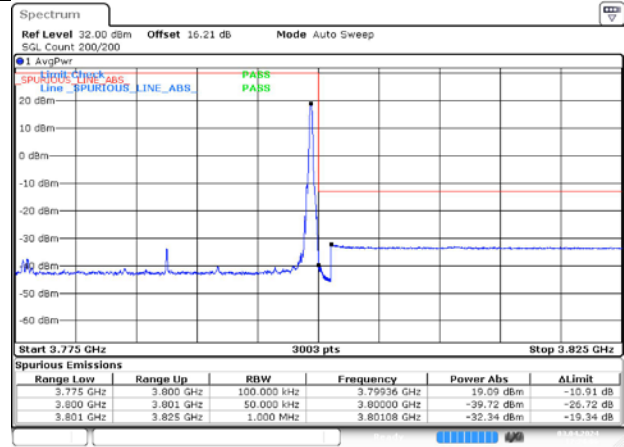
DC\_41A\_n78A / 15KHz / 25MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



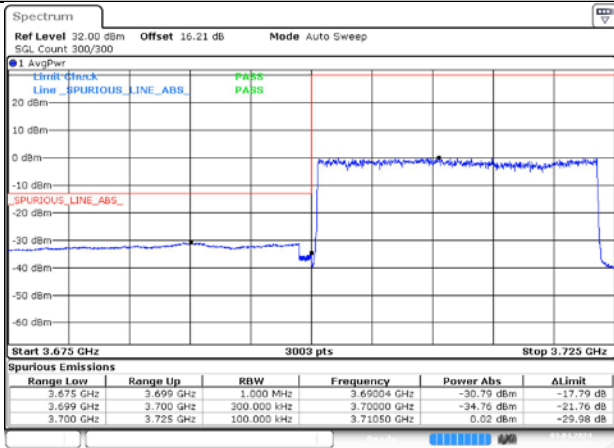
Date: 3 APR 2024 12:22:33

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Right



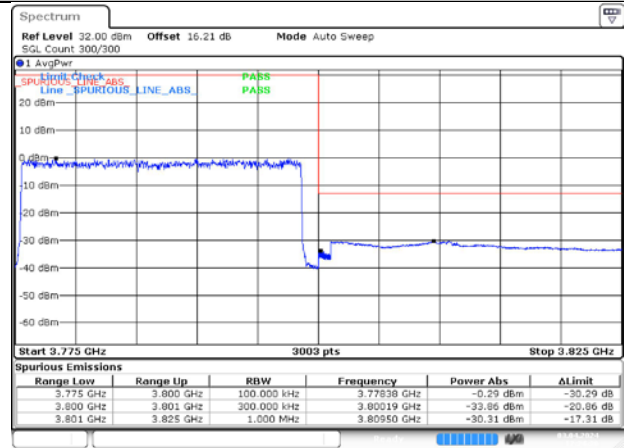
Date: 3 APR 2024 12:24:06

LCH\_DFT-Pi2BPSK / Outer\_Full



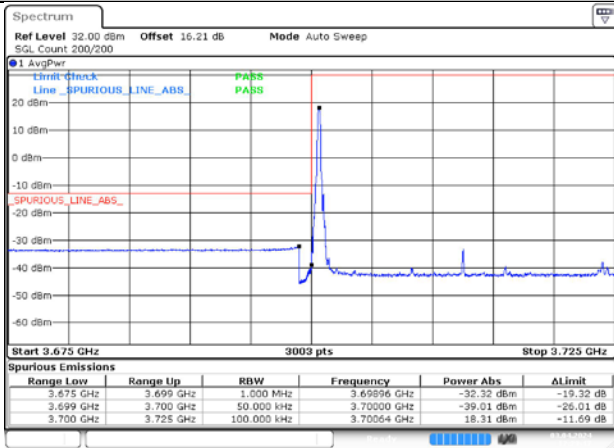
Date: 3 APR 2024 12:22:50

HCH\_DFT-Pi2BPSK / Outer\_Full



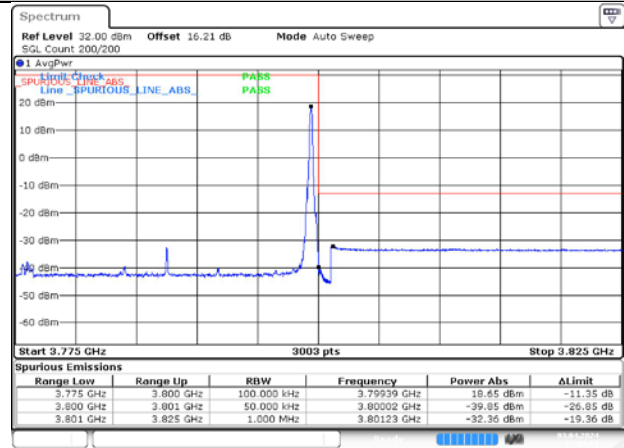
Date: 3 APR 2024 12:24:20

LCH\_DFT-QPSK / Edge\_1RB\_Left

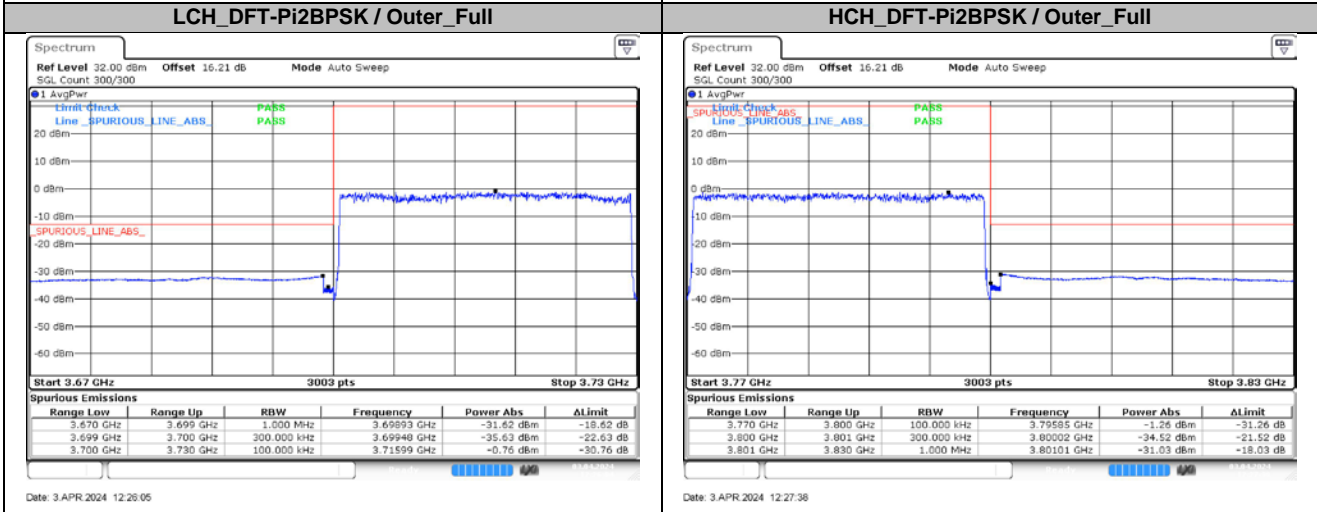
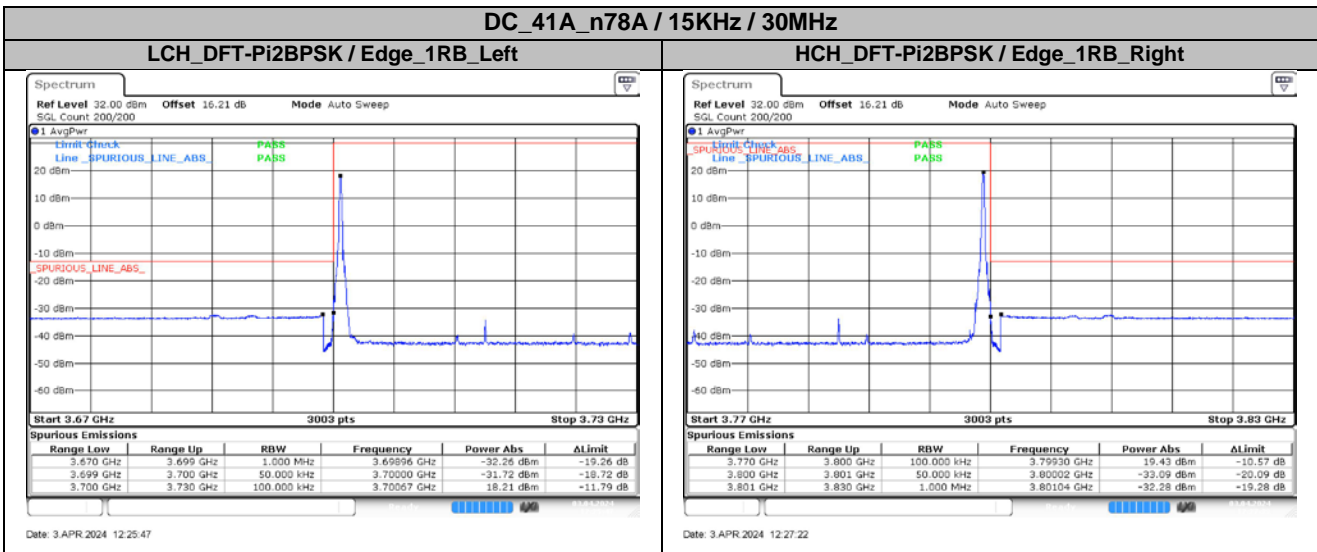
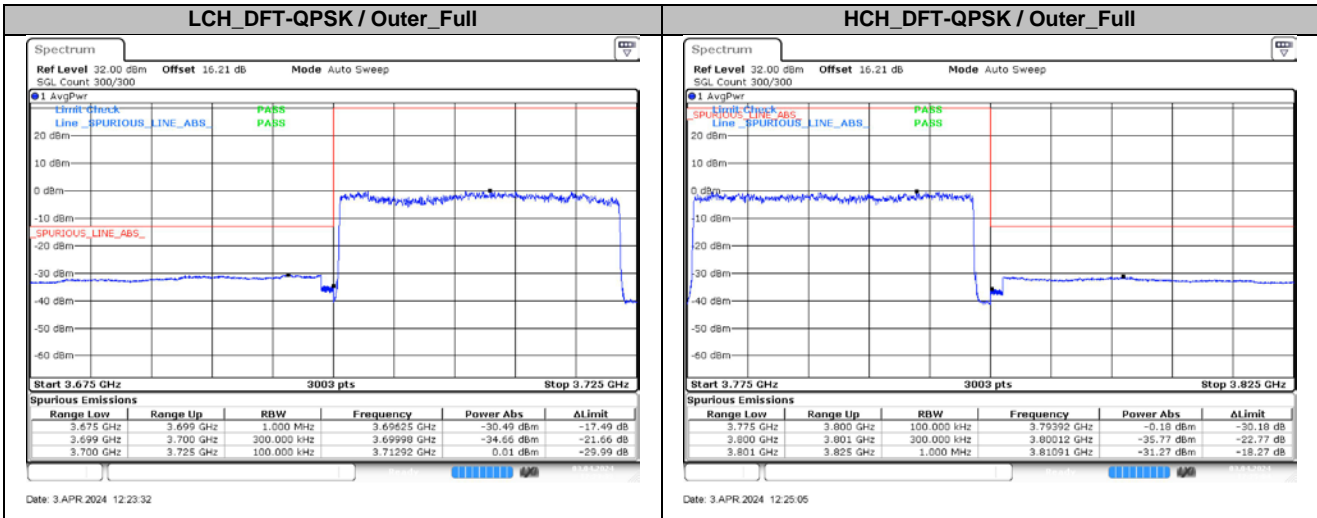


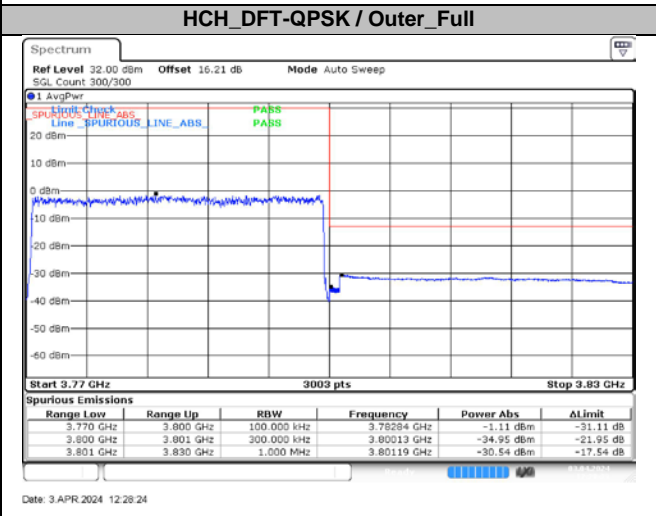
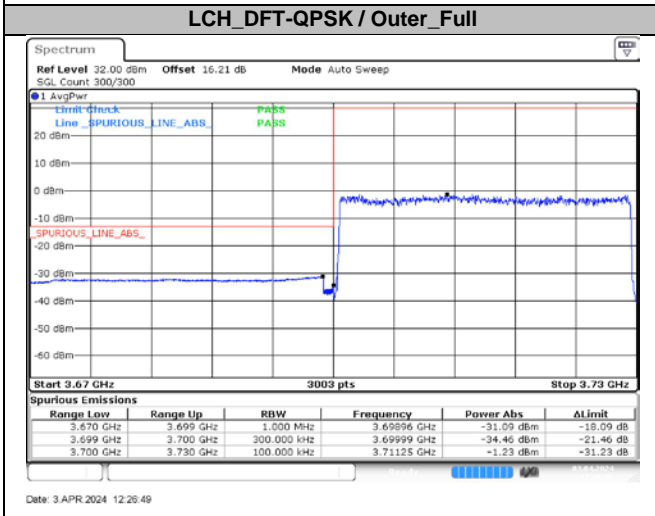
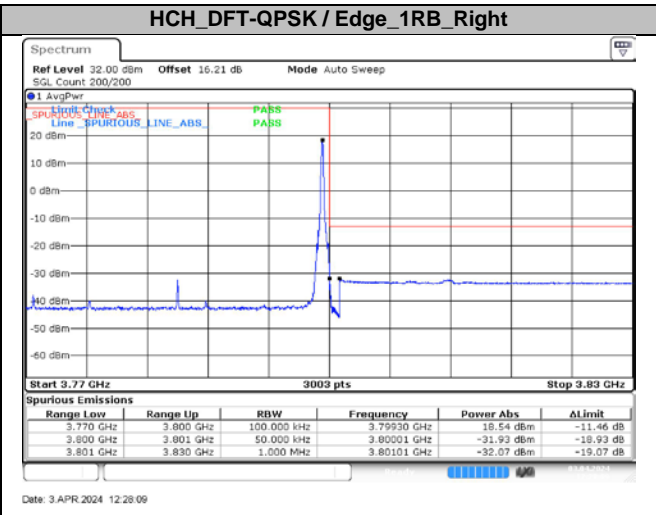
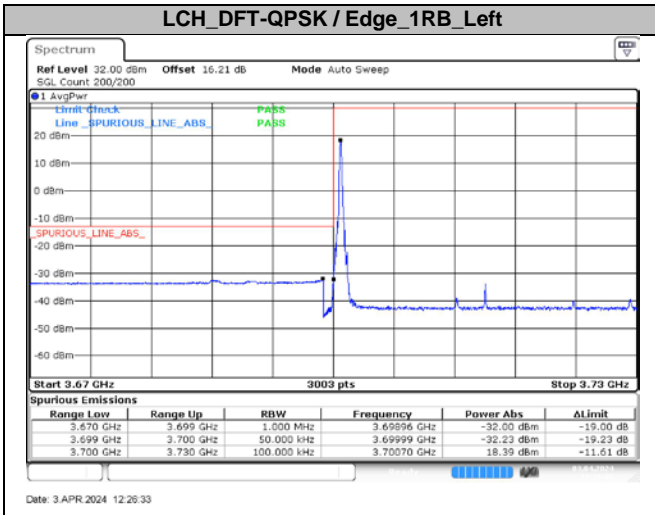
Date: 3 APR 2024 12:23:17

HCH\_DFT-QPSK / Edge\_1RB\_Right



Date: 3 APR 2024 12:24:51





## DC\_41A\_n78A / 15KHz / 40MHz

