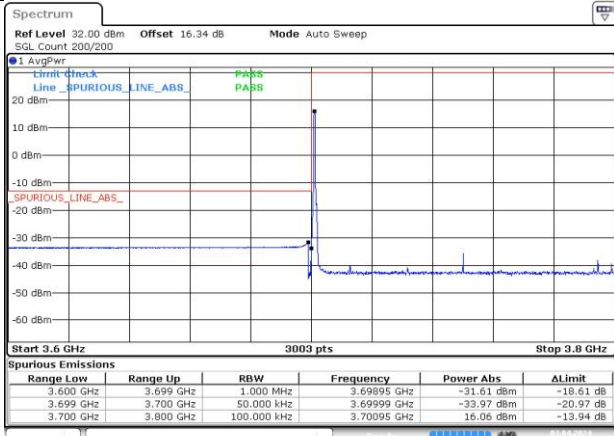


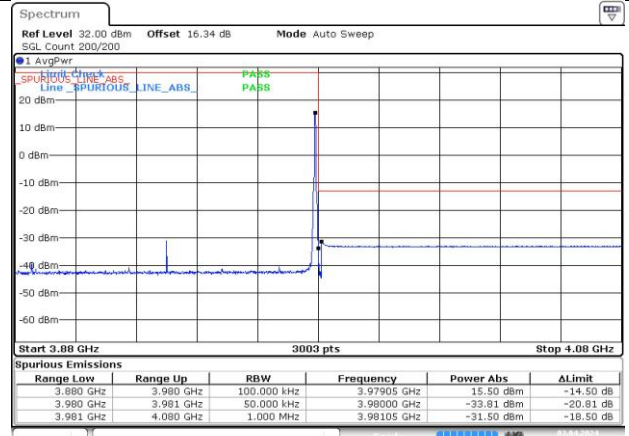
DC_41A_n77A / 30KHz / 100MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



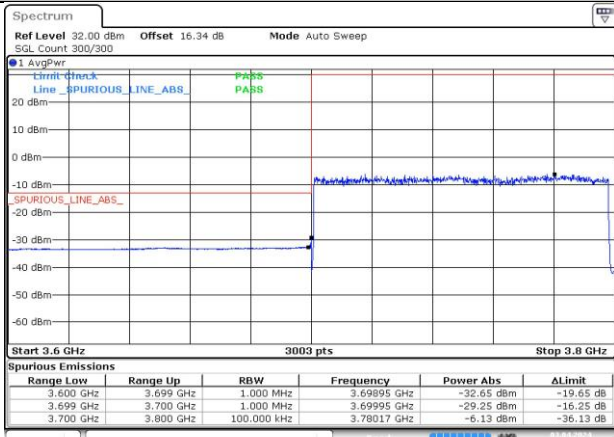
Date: 3 APR 2024 16:47:43

HCH / DFT-Pi2BPSK / Edge_1RB_Right



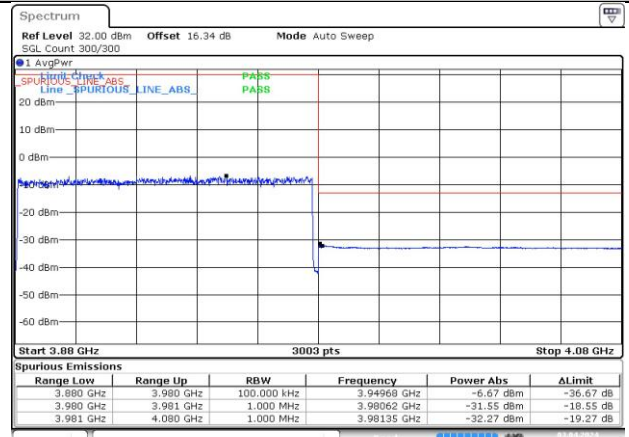
Date: 3 APR 2024 16:49:38

LCH / DFT-Pi2BPSK / Outer_Full



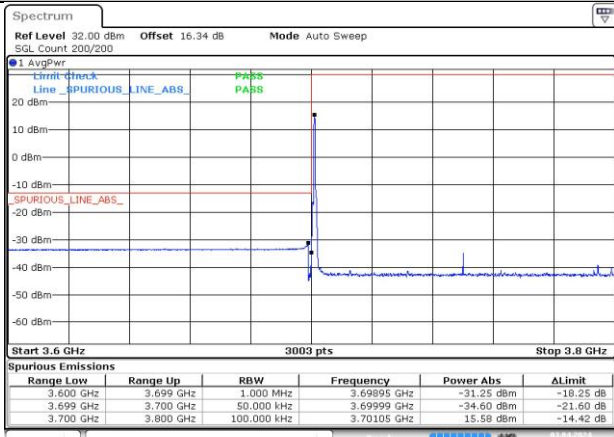
Date: 3 APR 2024 16:48:03

HCH / DFT-Pi2BPSK / Outer_Full



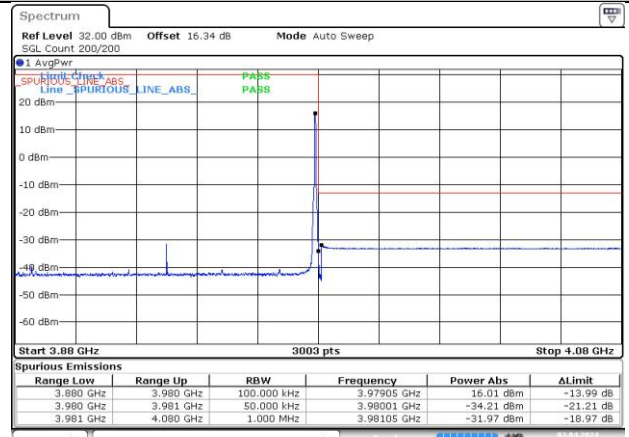
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LCH / DFT-QPSK / Edge_1RB_Left

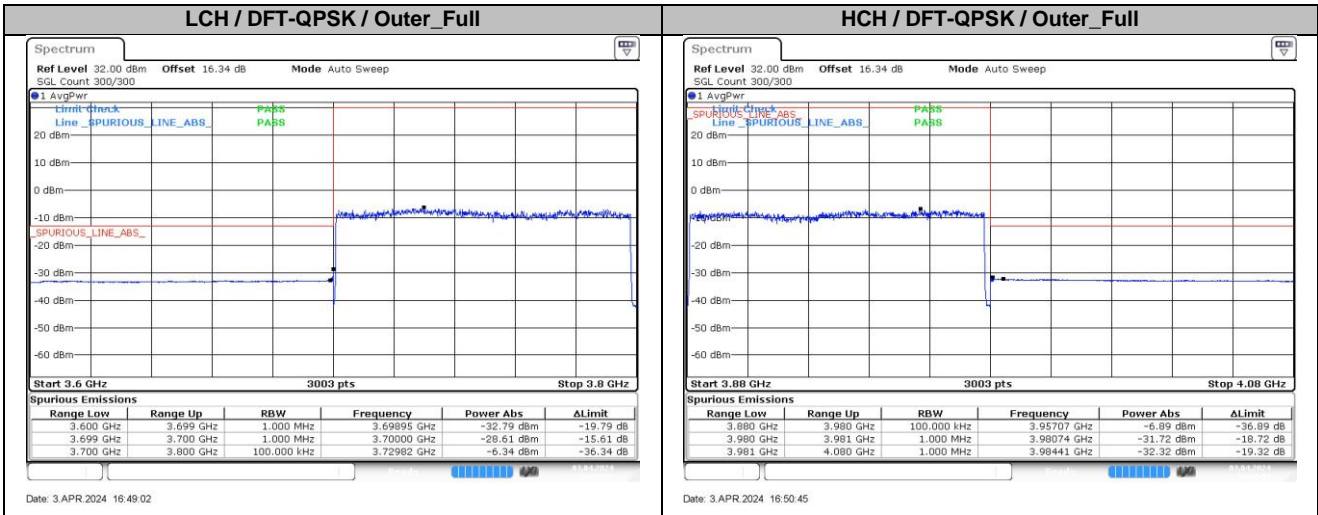


Date: 3 APR 2024 16:48:44

HCH / DFT-QPSK / Edge_1RB_Right

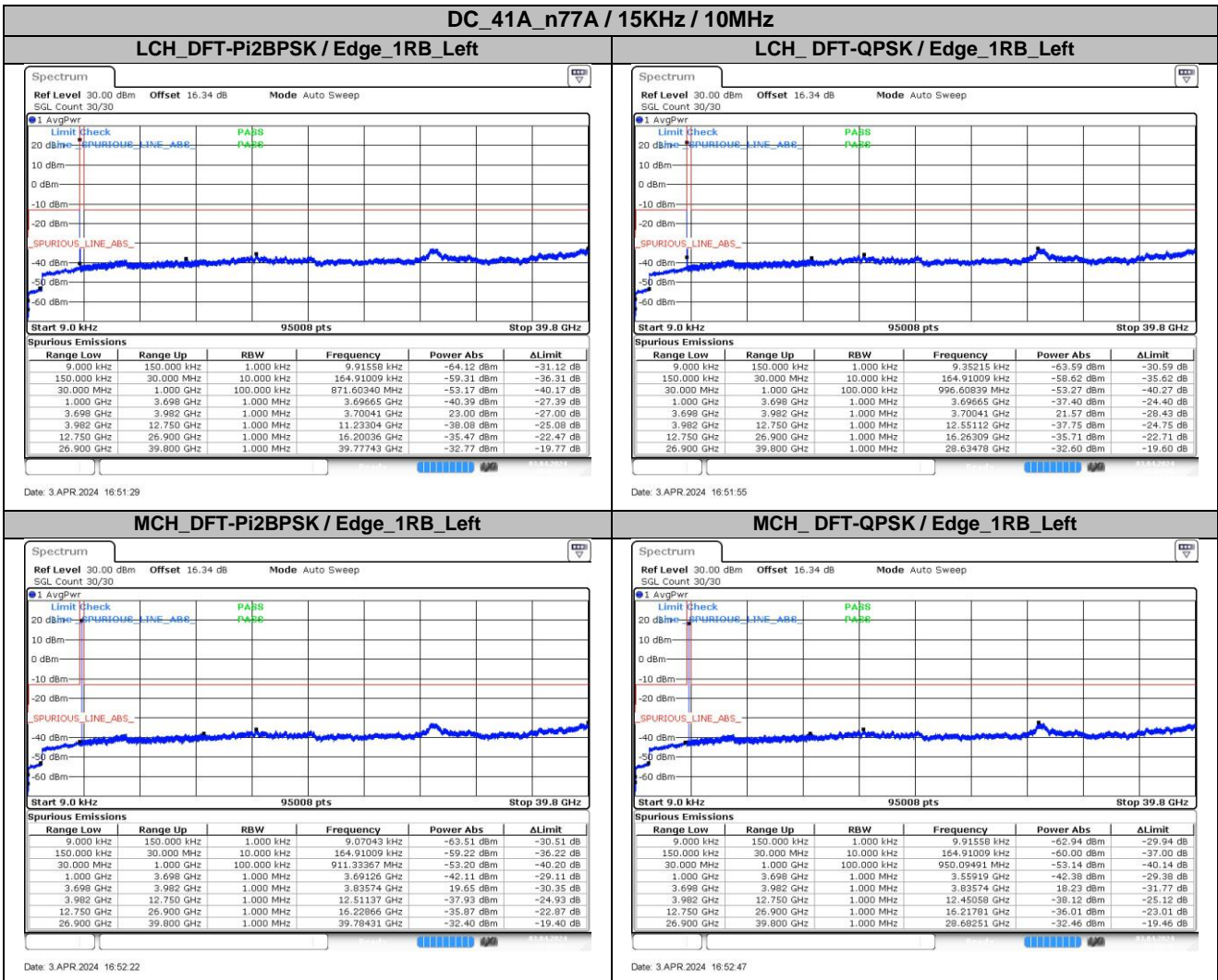


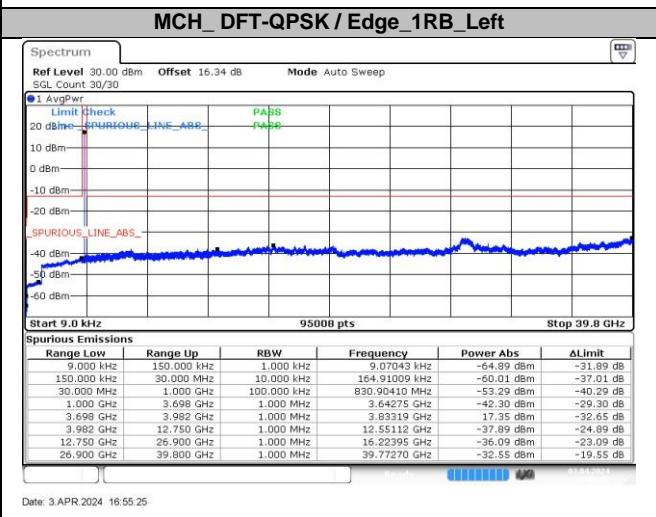
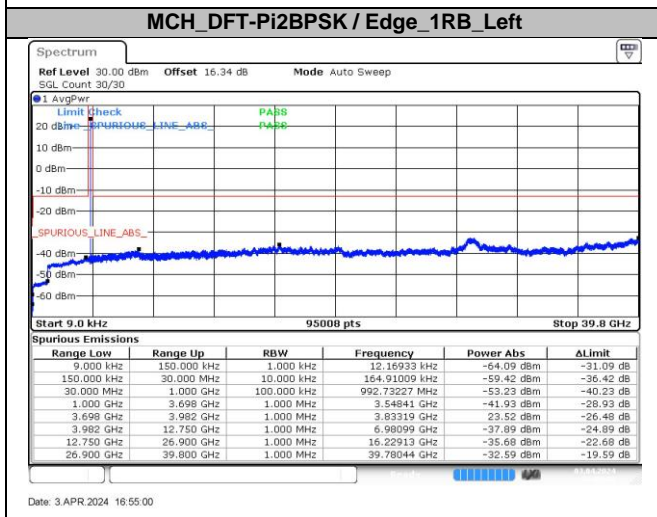
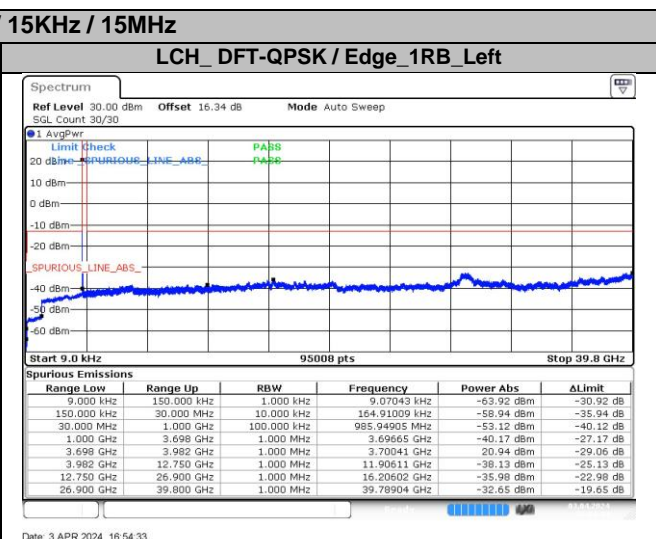
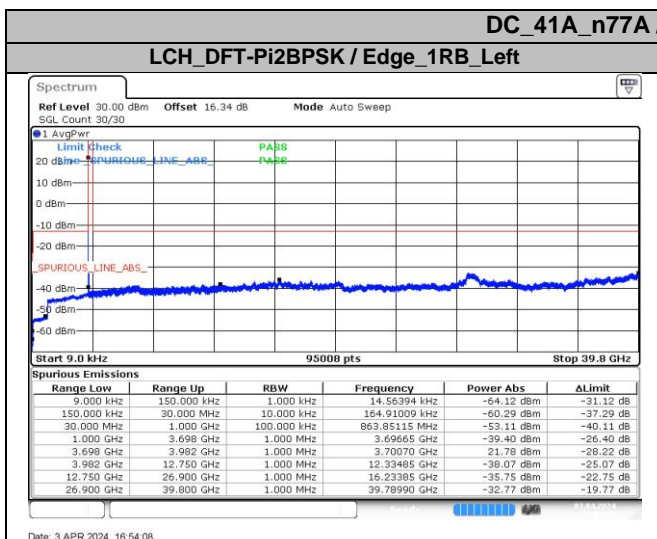
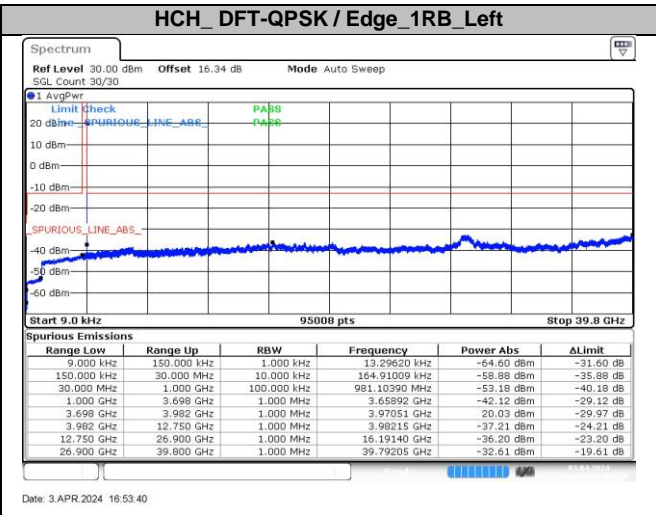
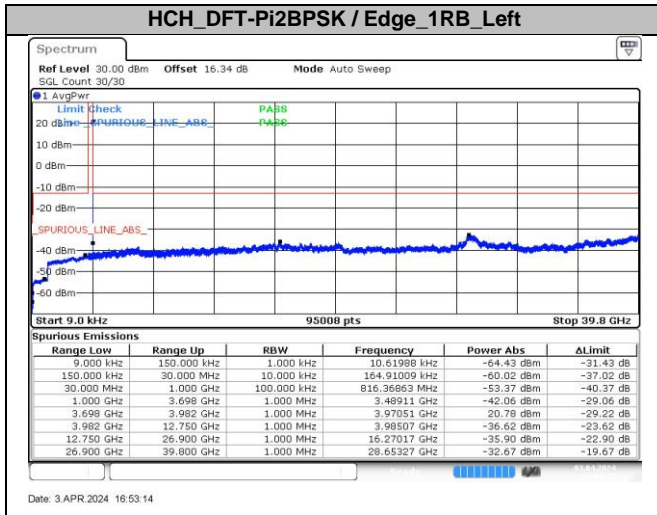
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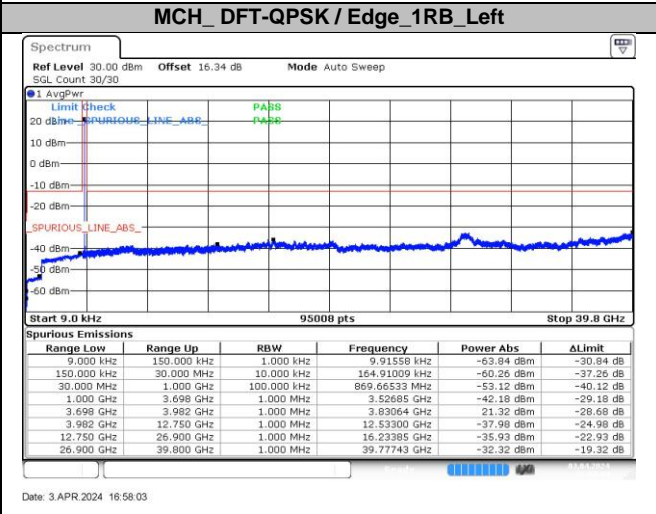
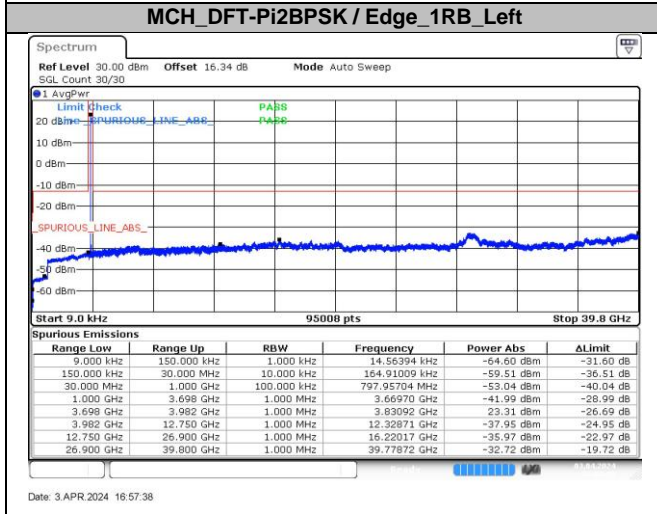
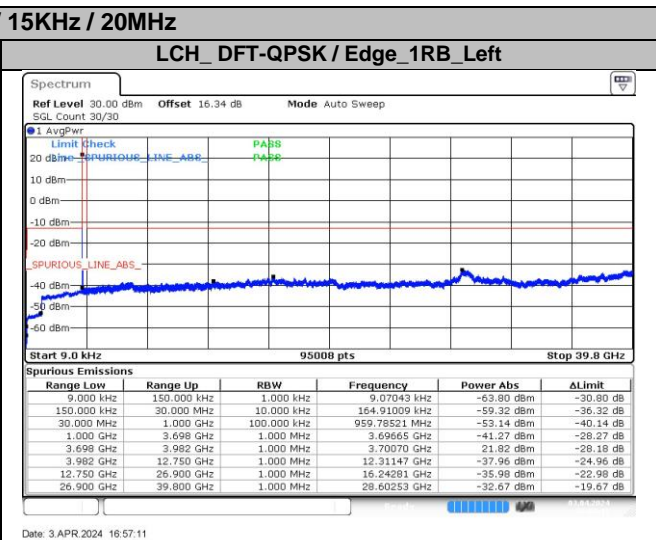
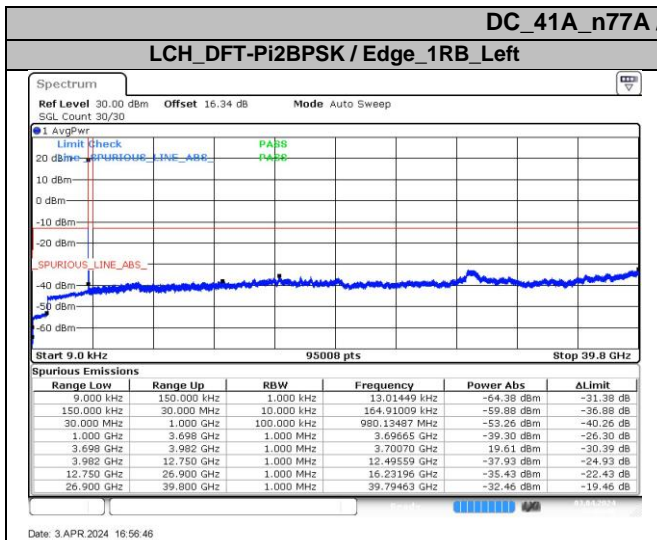
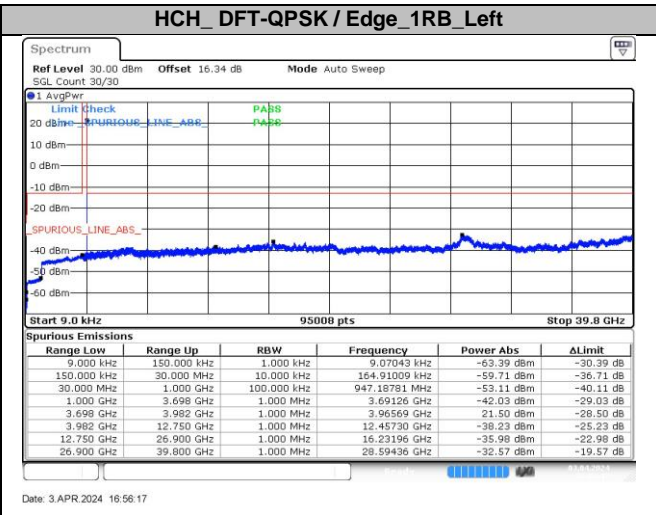
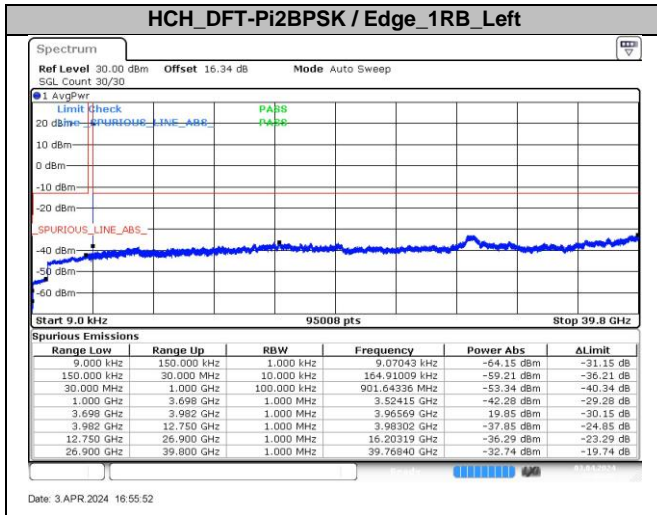


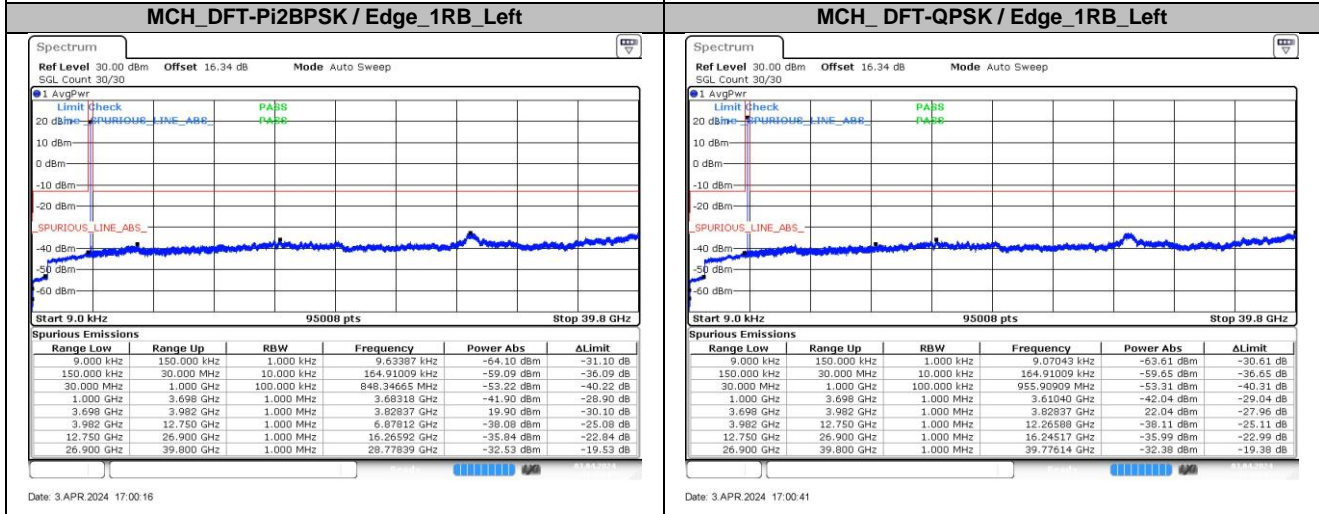
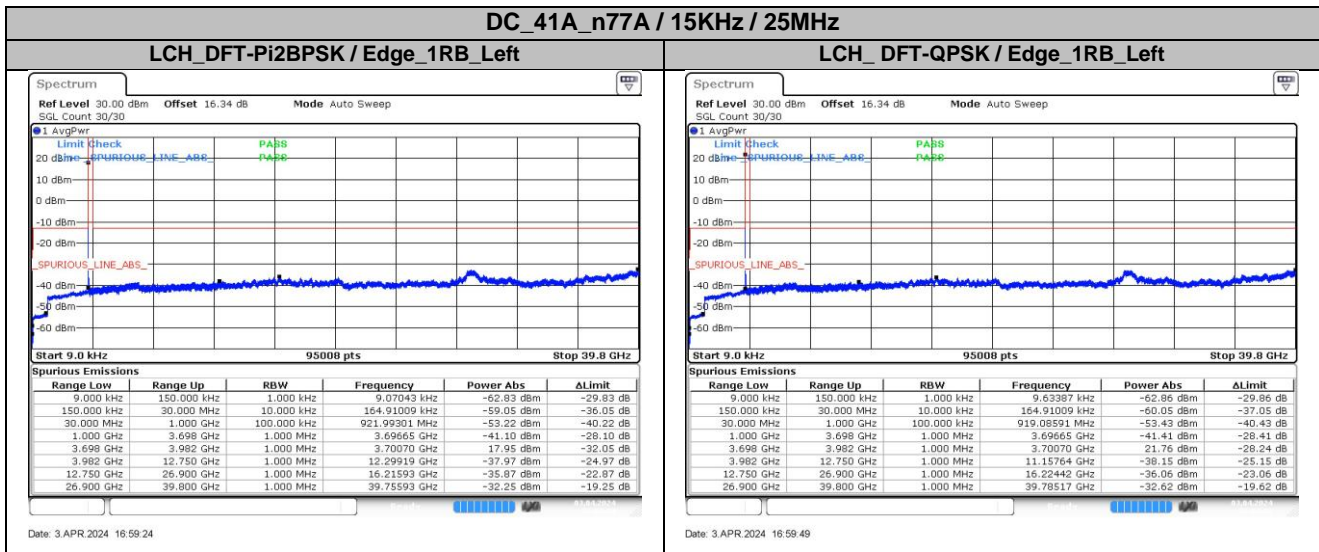
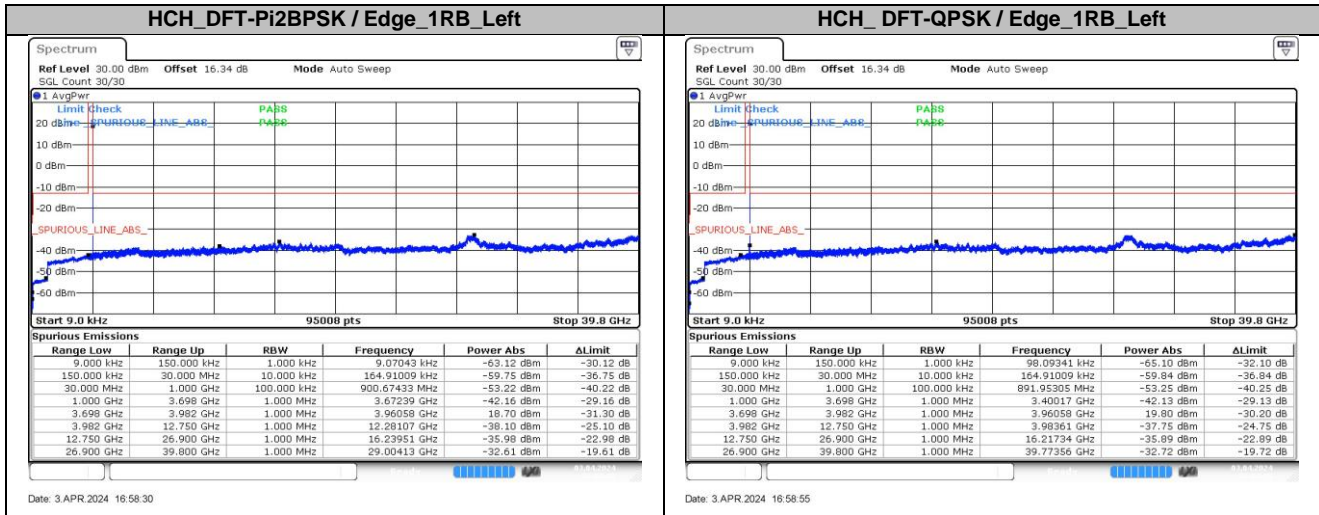
6. Conducted Spurious Emission

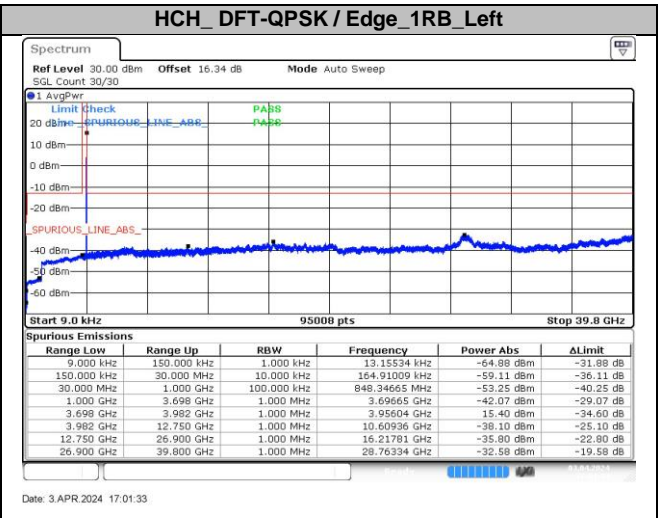
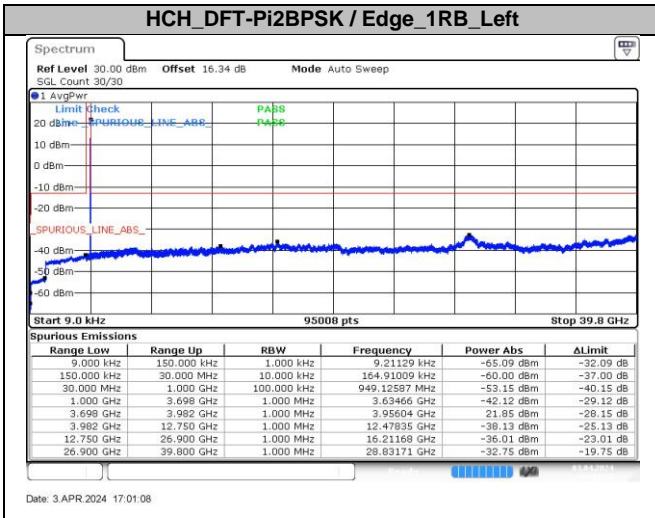
6.1. Test Plots



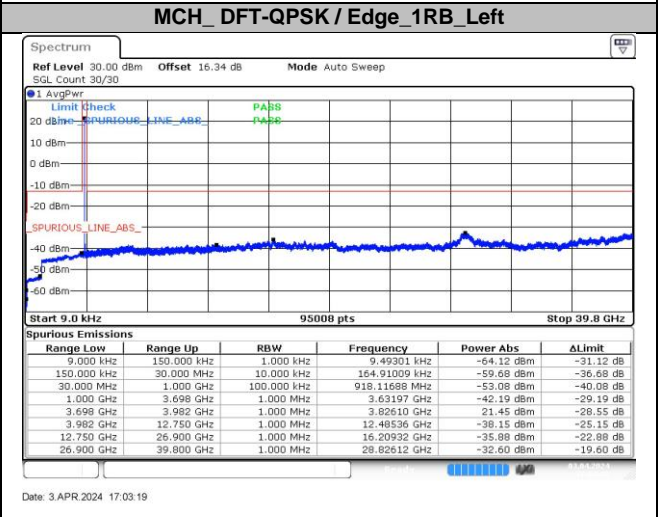
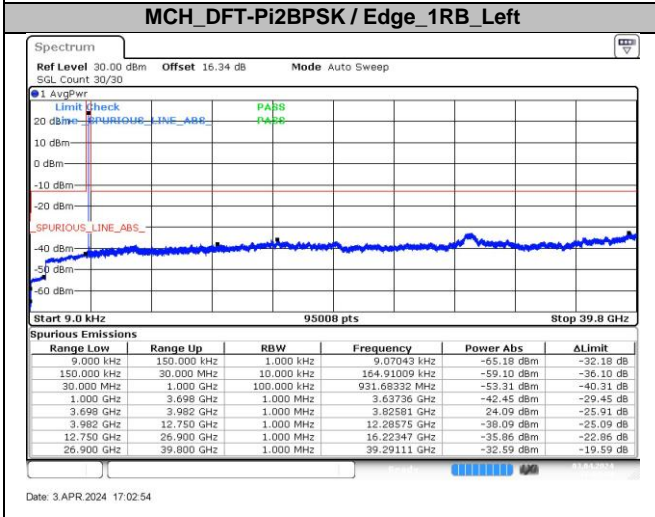
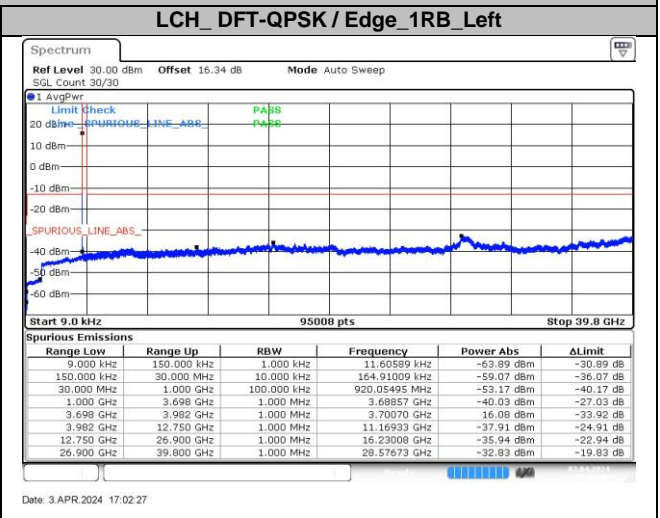
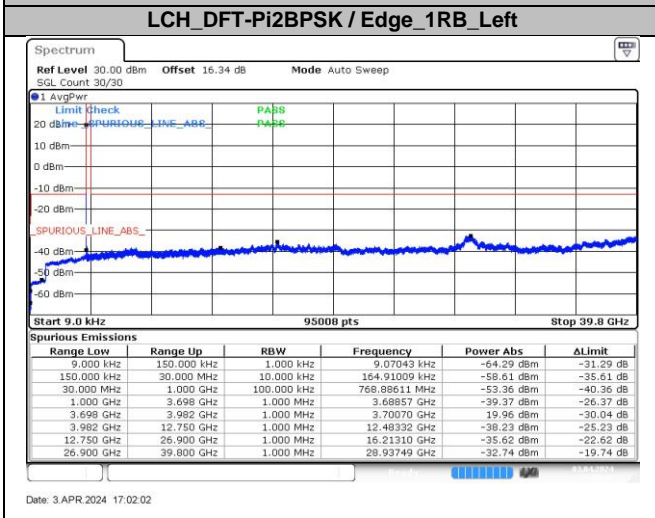


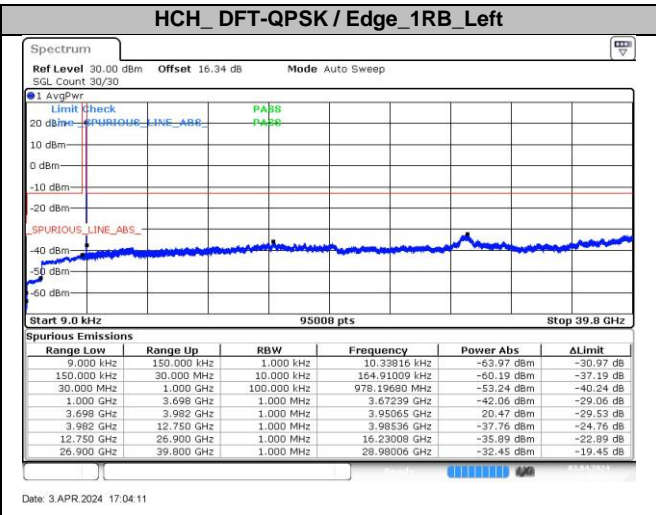
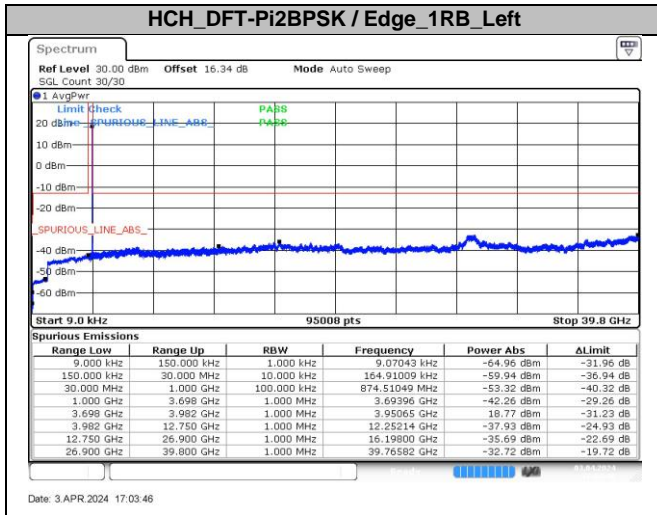






DC_41A_n77A / 15KHz / 30MHz





DC_41A_n77A / 15KHz / 40MHz

