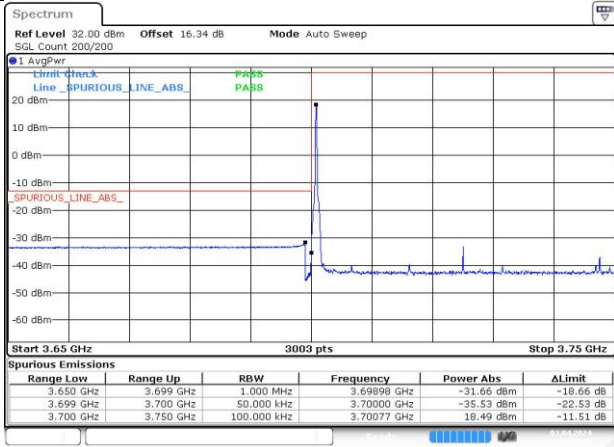


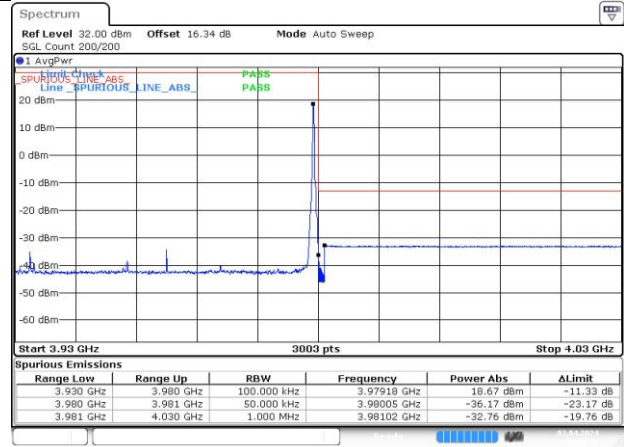
DC\_41A\_n77A / 15KHz / 50MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



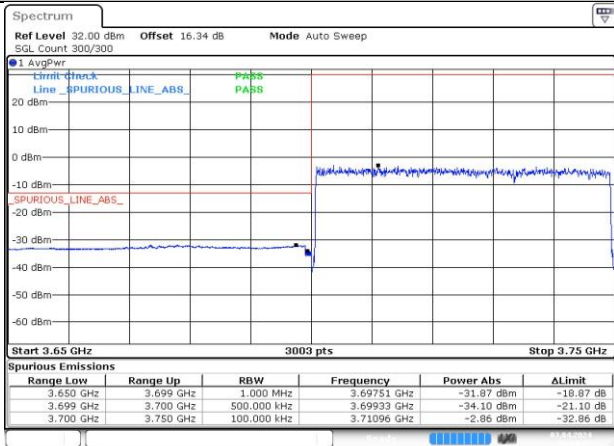
Date: 3 APR 2024 16:10:08

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Right



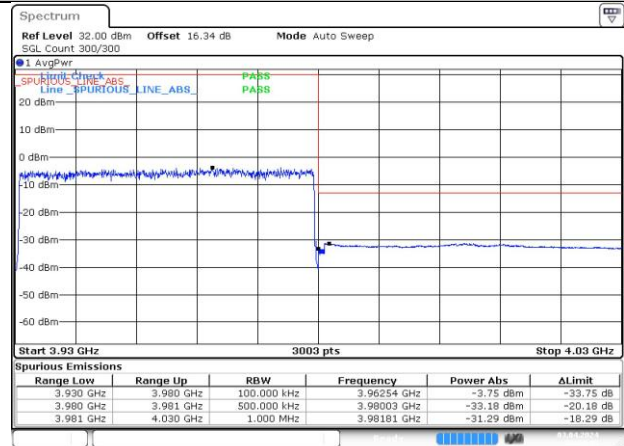
Date: 3 APR 2024 16:11:49

LCH\_DFT-Pi2BPSK / Outer\_Full



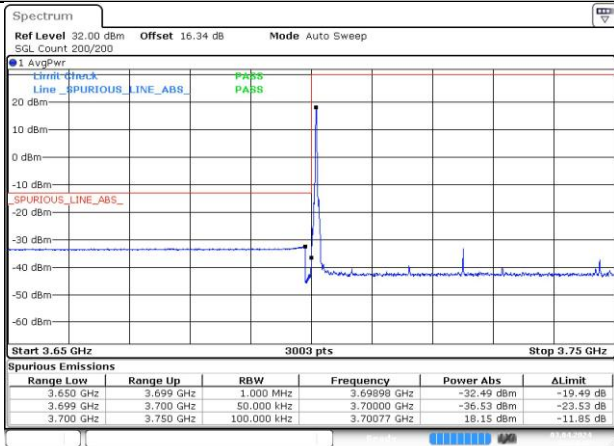
Date: 3 APR 2024 16:10:28

HCH\_DFT-Pi2BPSK / Outer\_Full



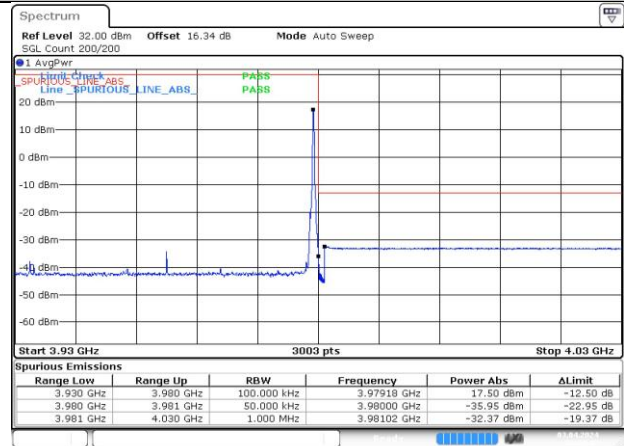
Date: 3 APR 2024 16:12:05

LCH\_DFT-QPSK / Edge\_1RB\_Left

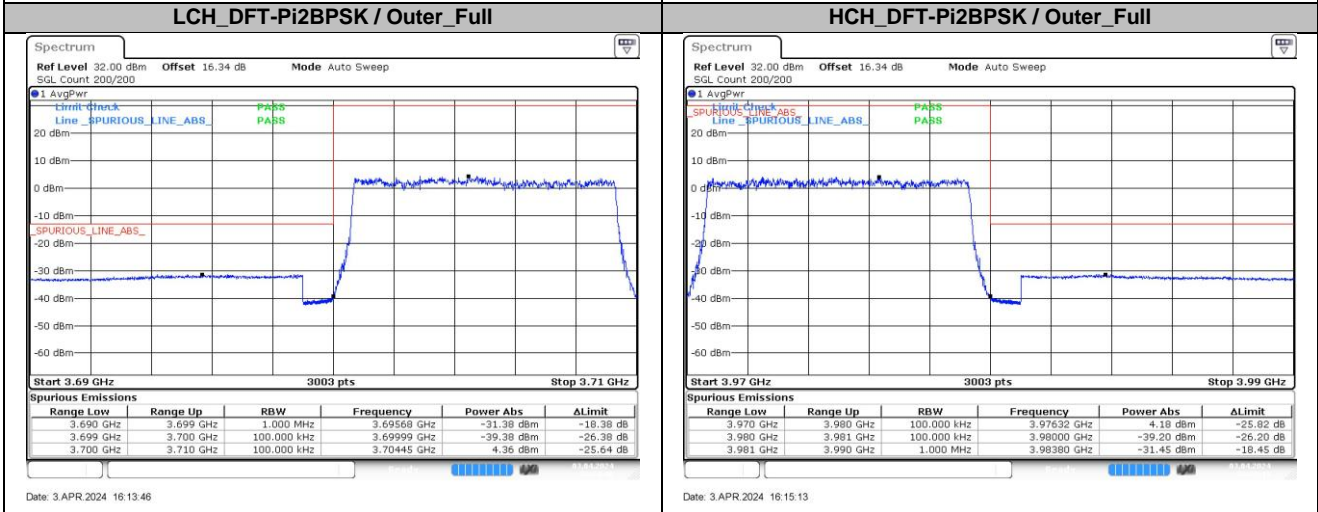
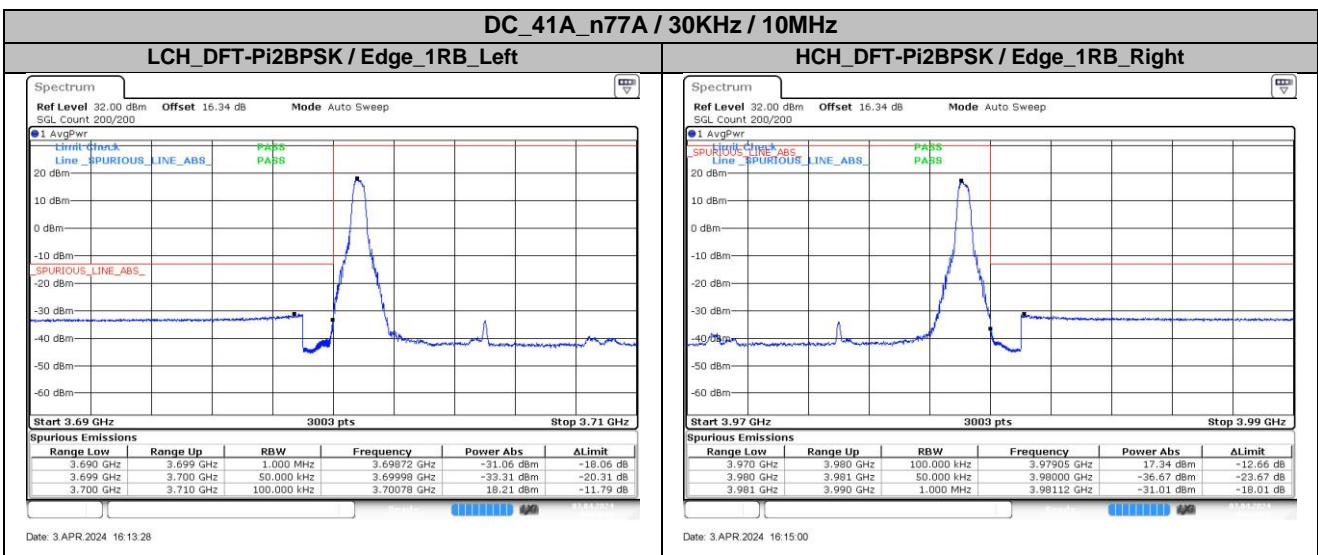
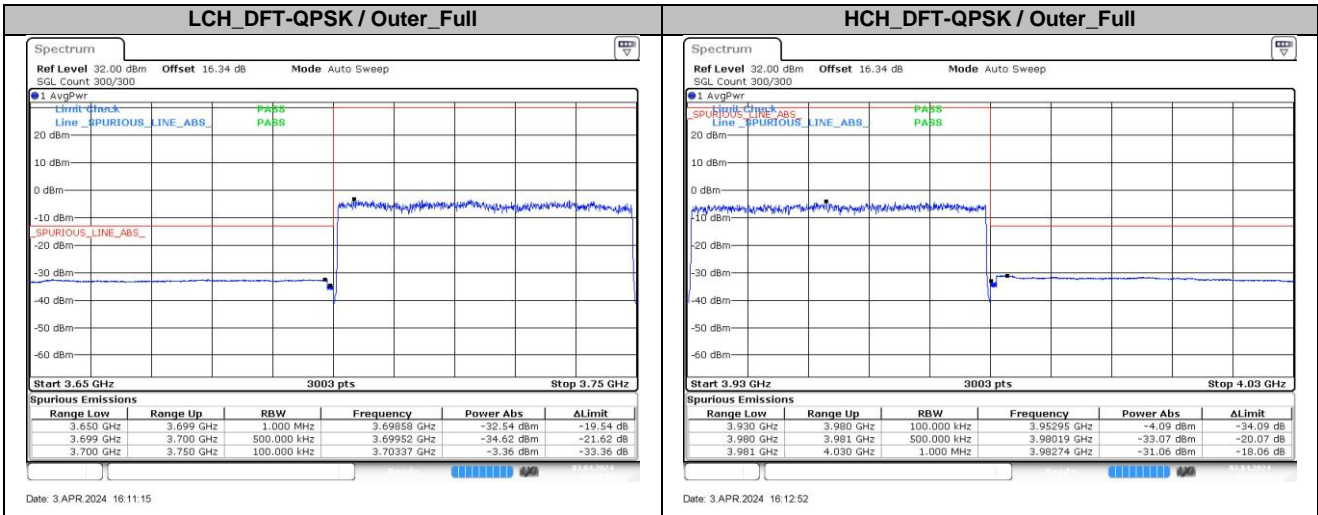


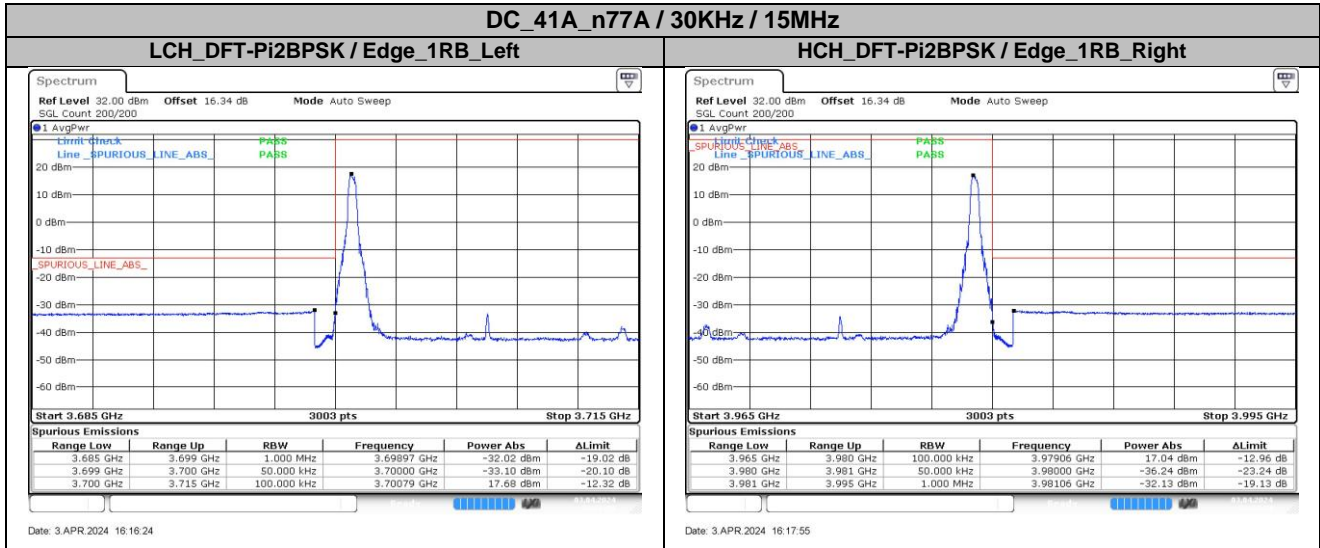
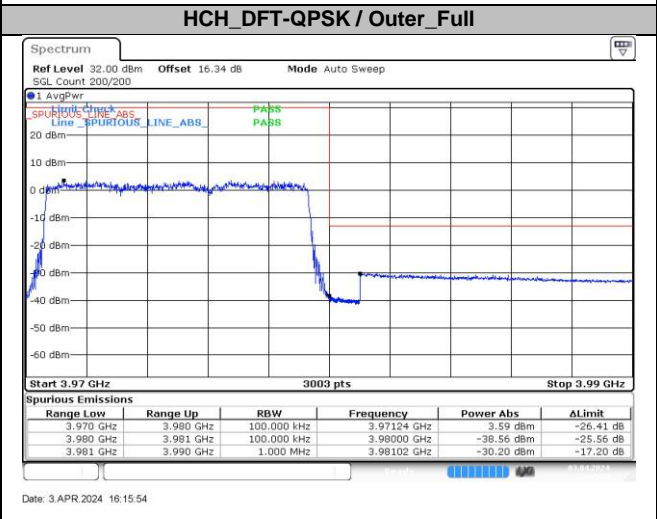
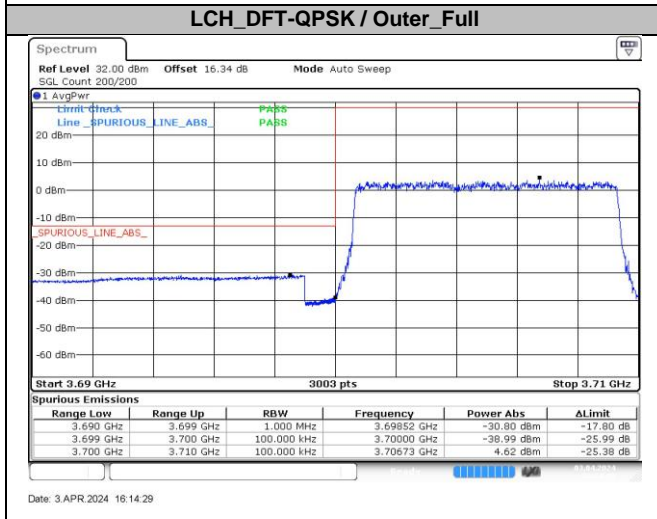
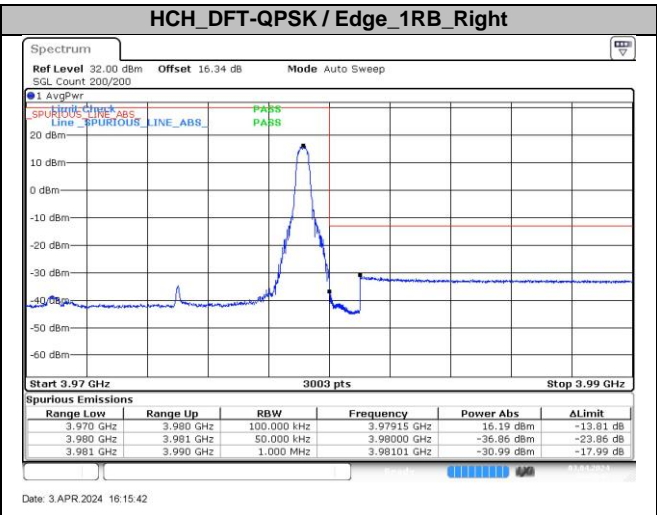
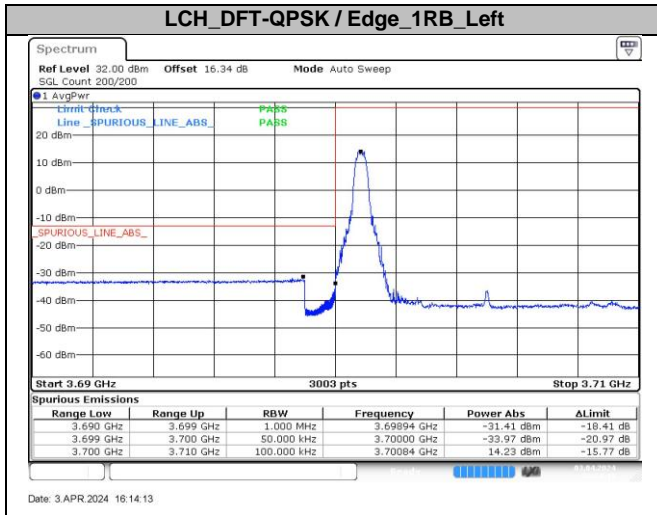
Date: 3 APR 2024 16:10:57

HCH\_DFT-QPSK / Edge\_1RB\_Right

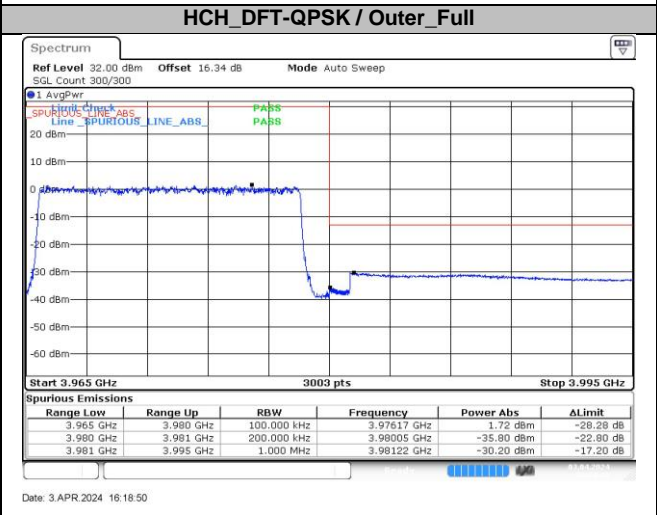
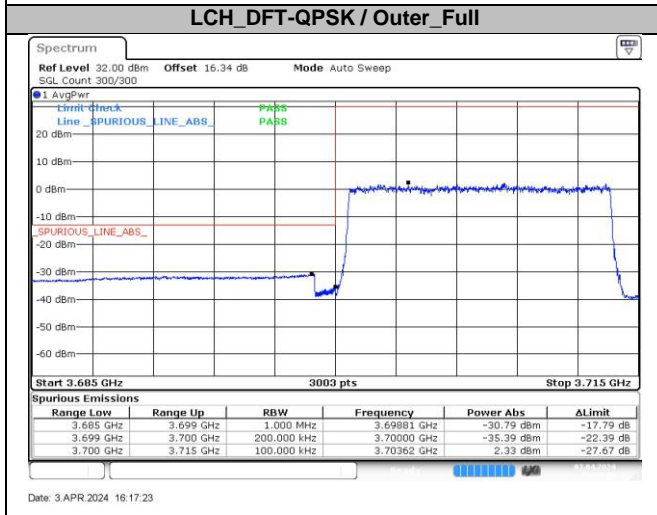
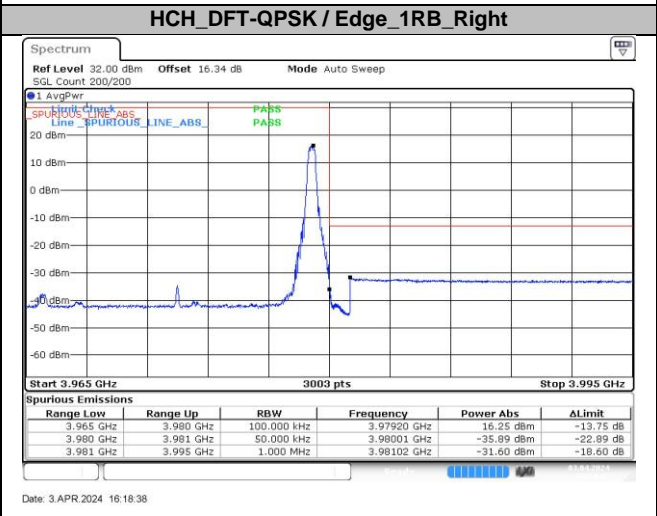
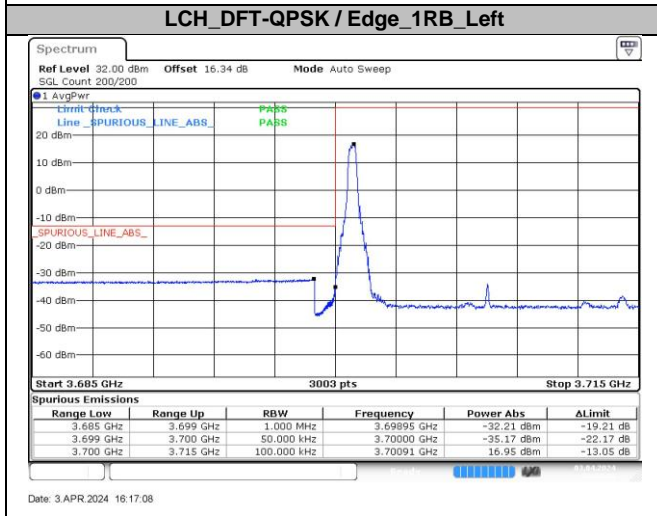
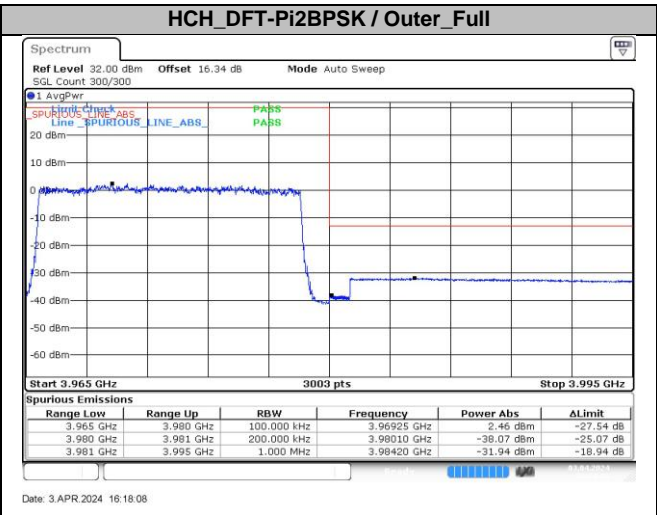
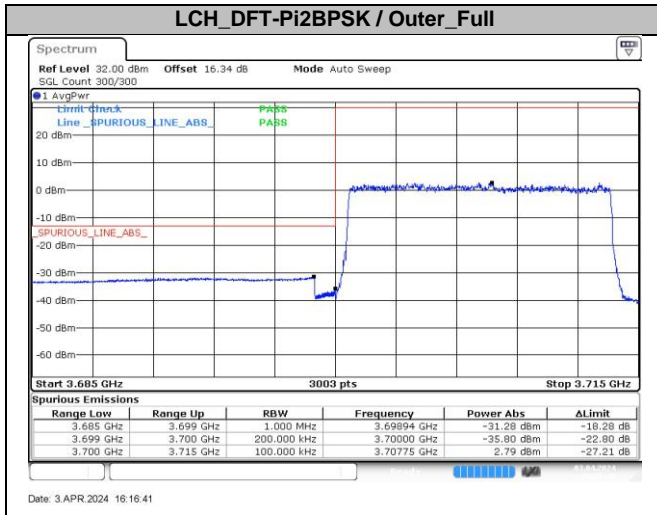


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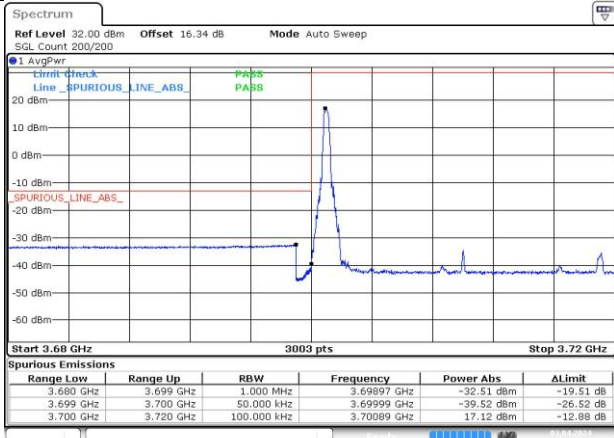






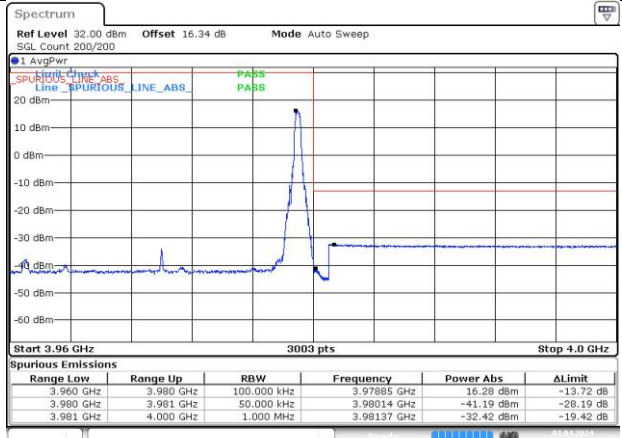
DC\_41A\_n77A / 30KHz / 20MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 3 APR 2024 16:19:20

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Right



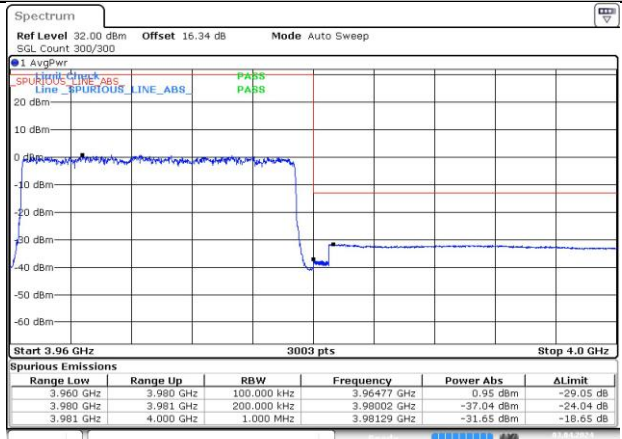
Date: 3 APR 2024 16:20:52

LCH\_DFT-Pi2BPSK / Outer\_Full



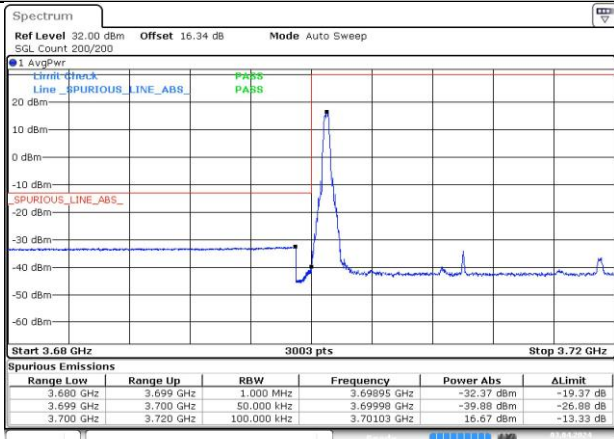
Date: 3 APR 2024 16:19:37

HCH\_DFT-Pi2BPSK / Outer\_Full



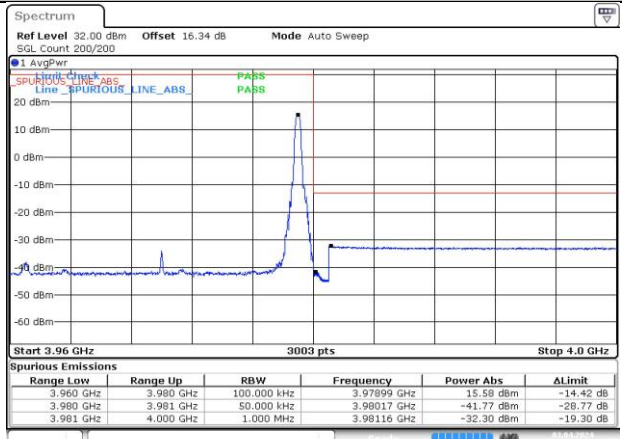
Date: 3 APR 2024 16:21:05

LCH\_DFT-QPSK / Edge\_1RB\_Left

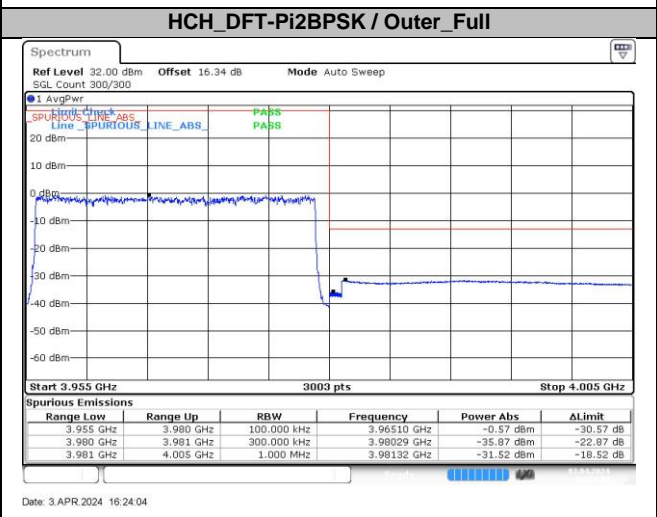
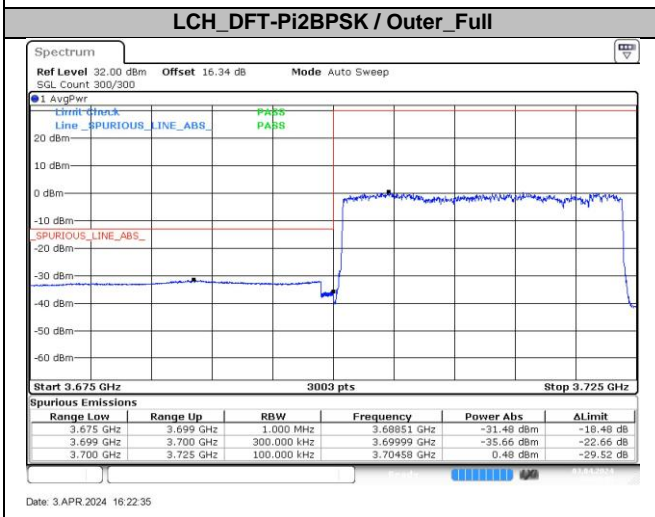
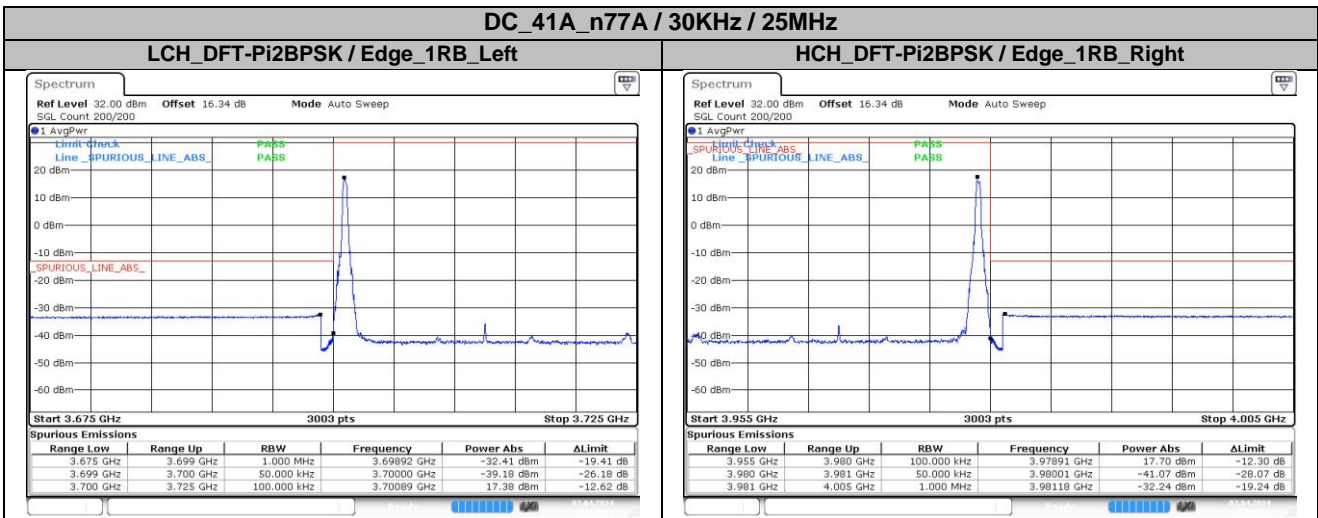
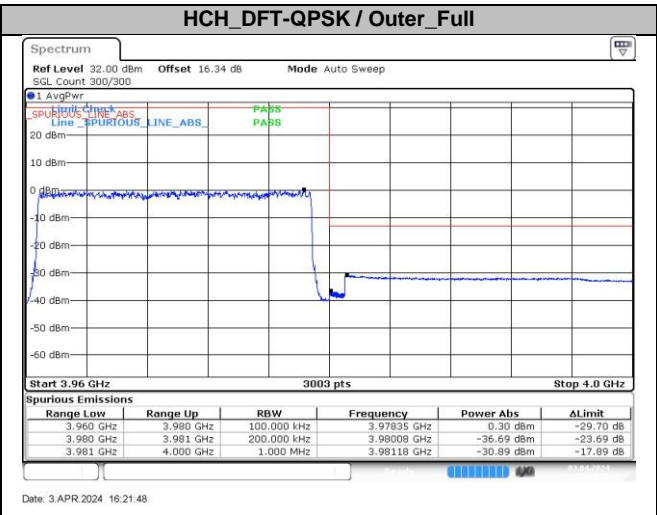
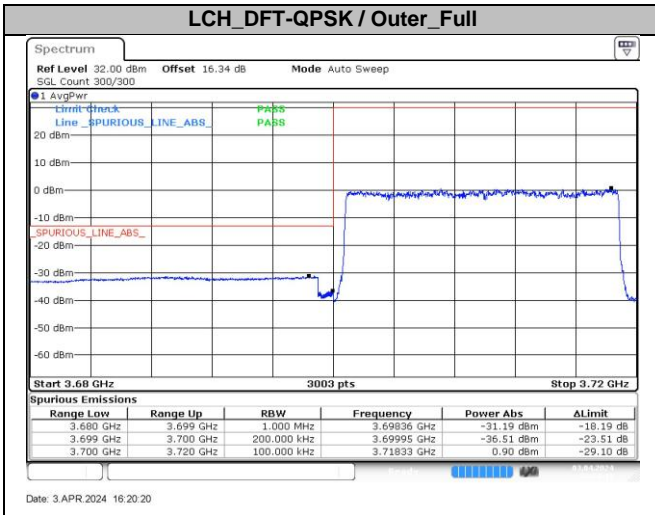


Date: 3 APR 2024 16:20:04

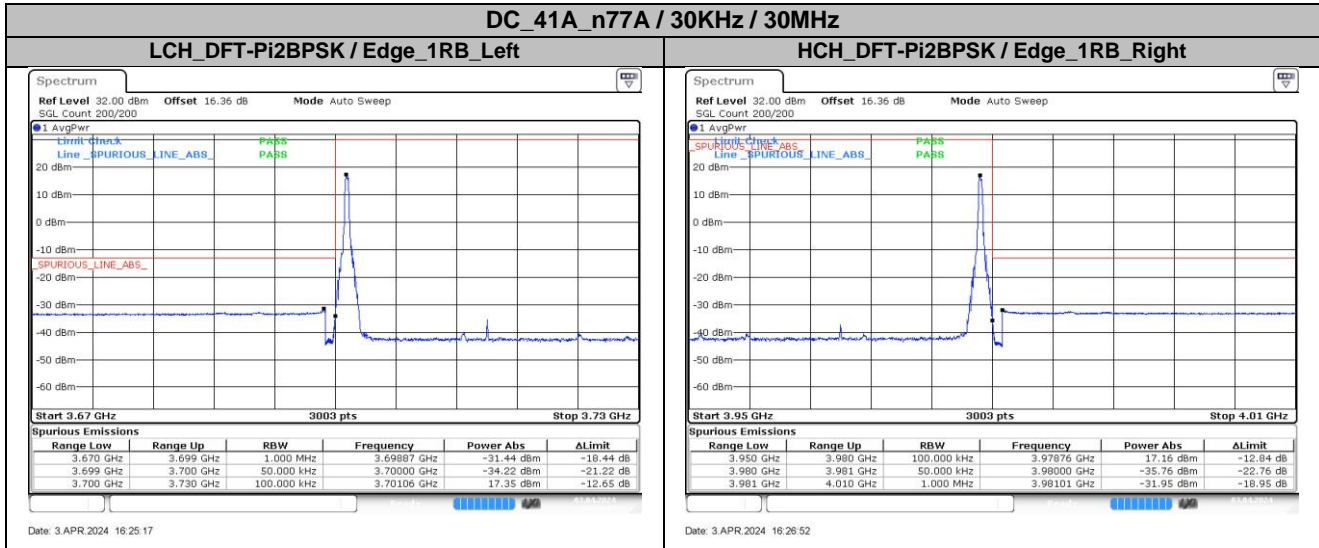
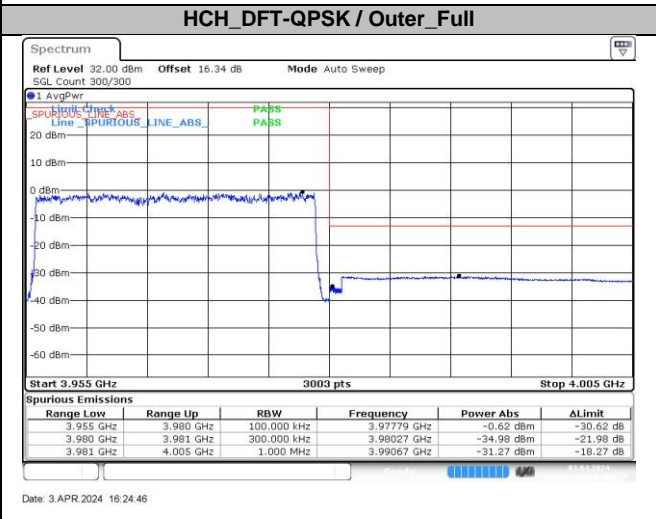
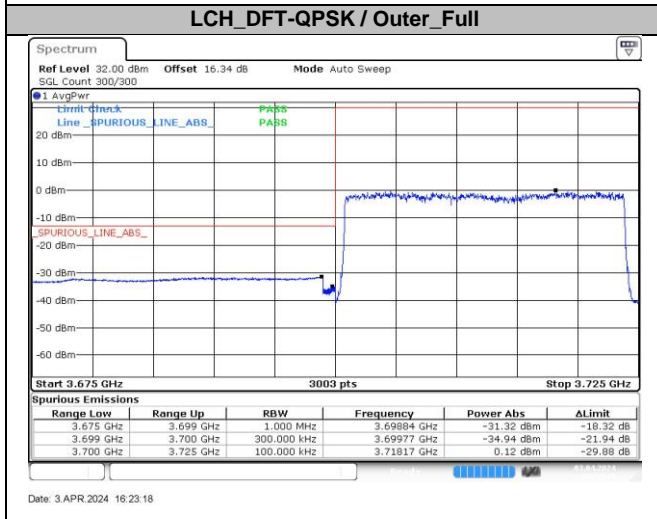
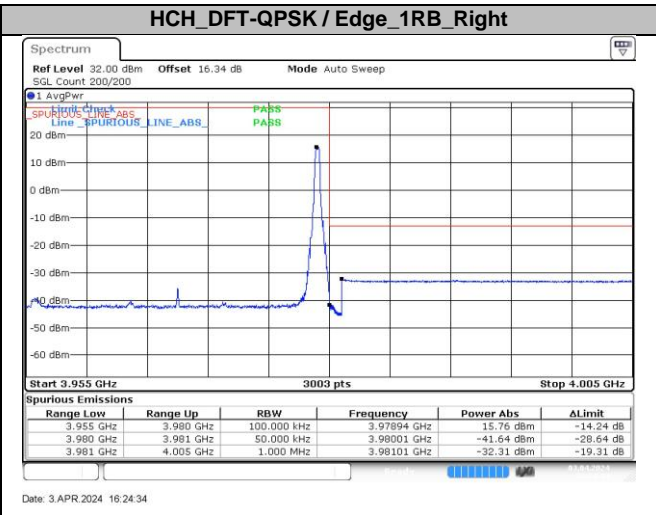
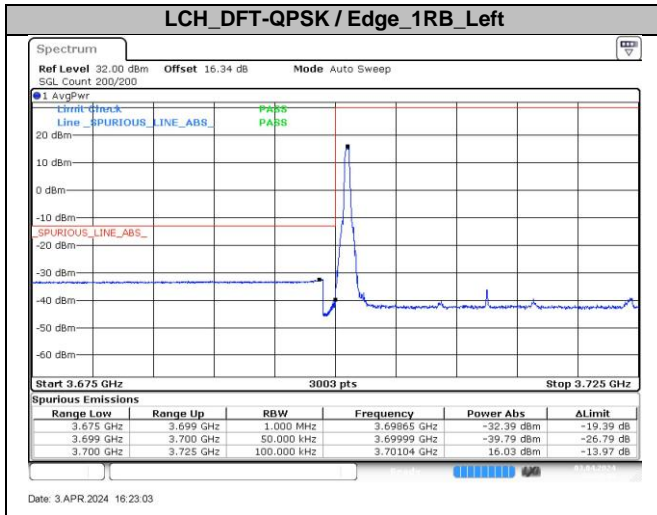
HCH\_DFT-QPSK / Edge\_1RB\_Right

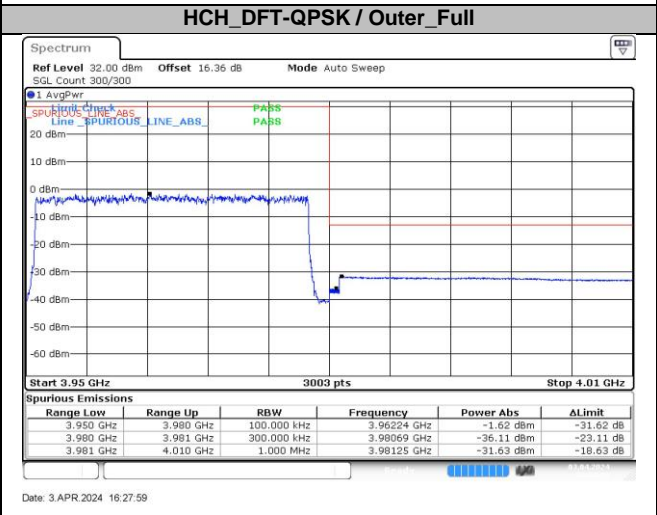
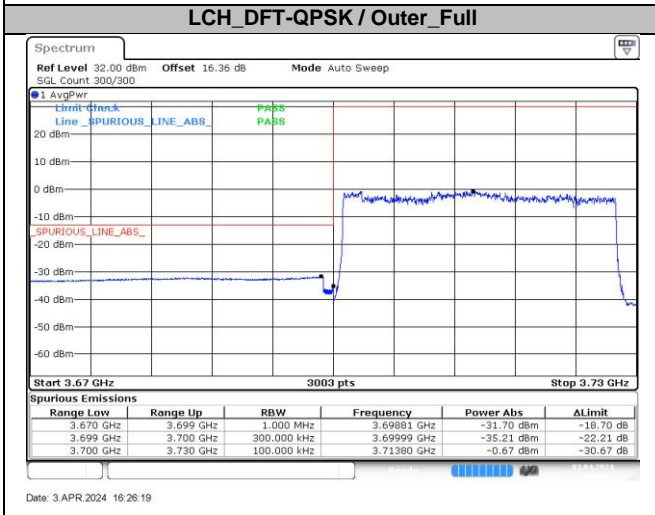
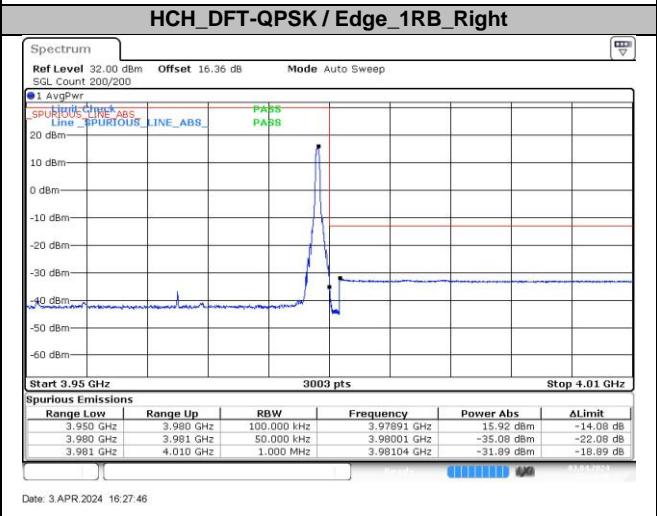
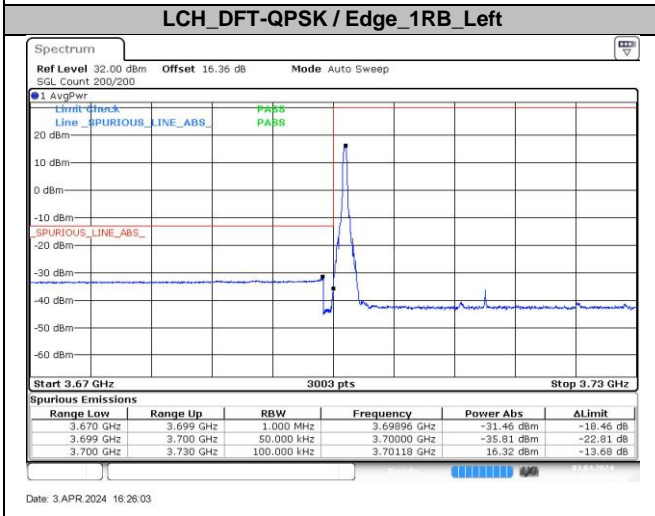
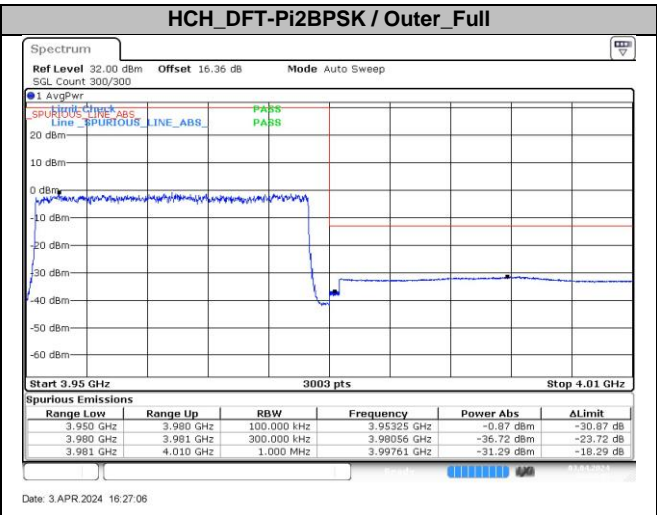
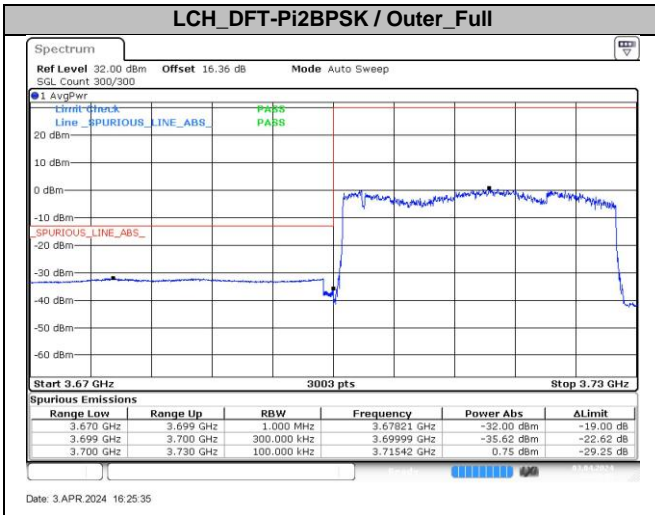


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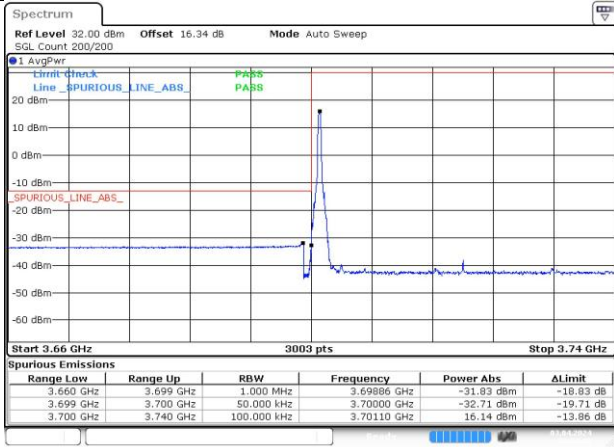






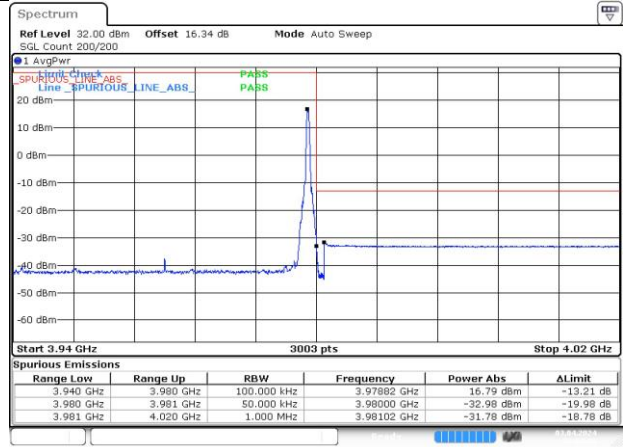
DC\_41A\_n77A / 30KHz / 40MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



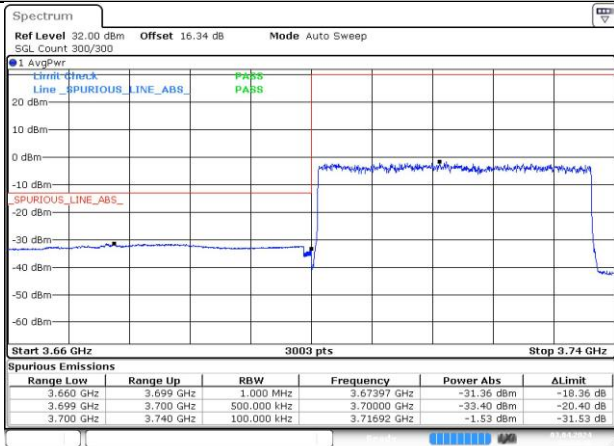
Date: 3 APR 2024 16:28:30

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Right



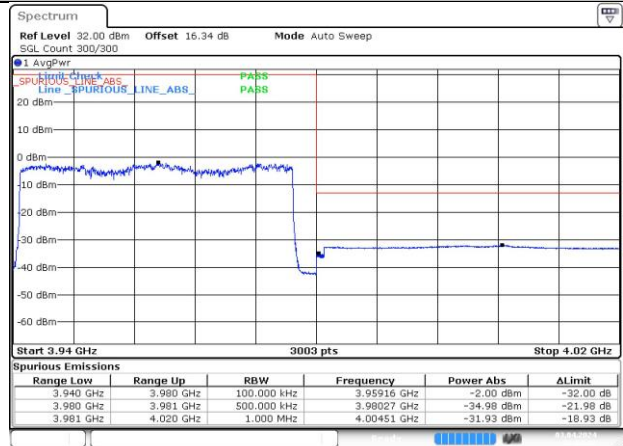
Date: 3 APR 2024 16:30:08

LCH\_DFT-Pi2BPSK / Outer\_Full



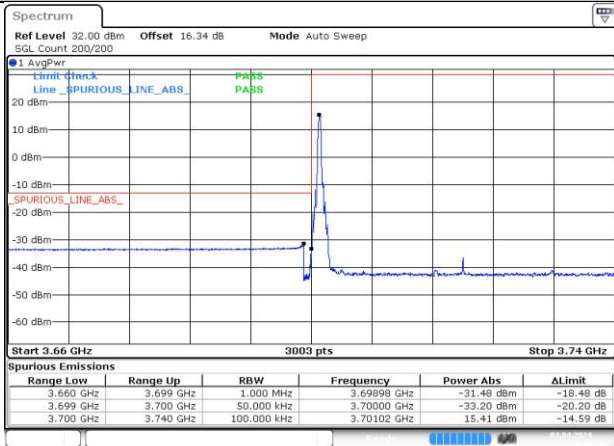
Date: 3 APR 2024 16:28:49

HCH\_DFT-Pi2BPSK / Outer\_Full



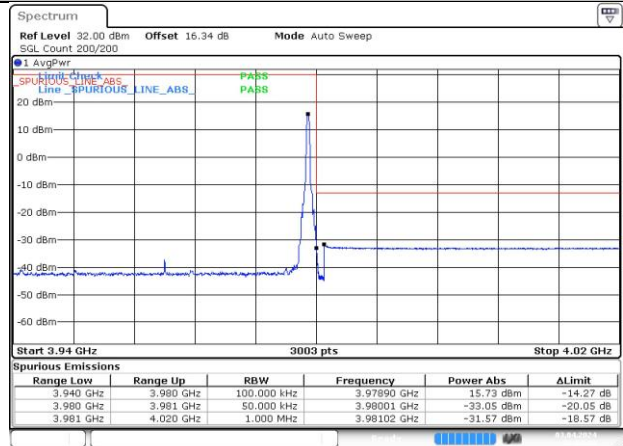
Date: 3 APR 2024 16:30:22

LCH\_DFT-QPSK / Edge\_1RB\_Left

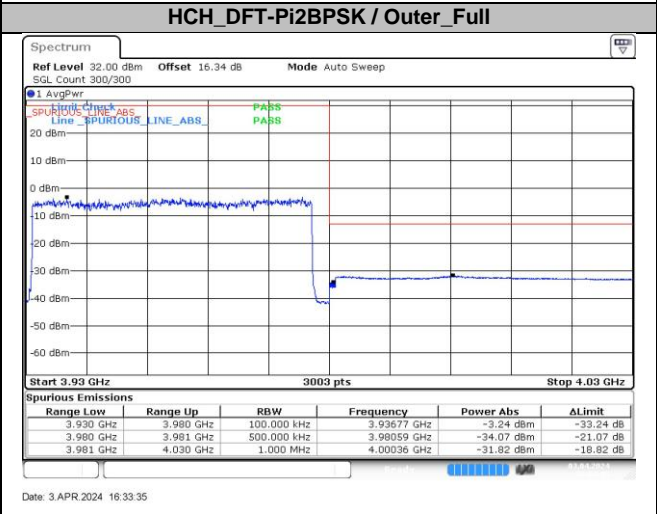
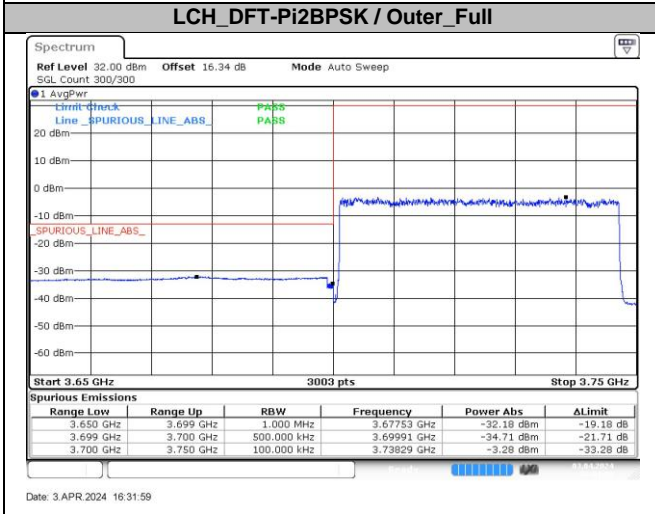
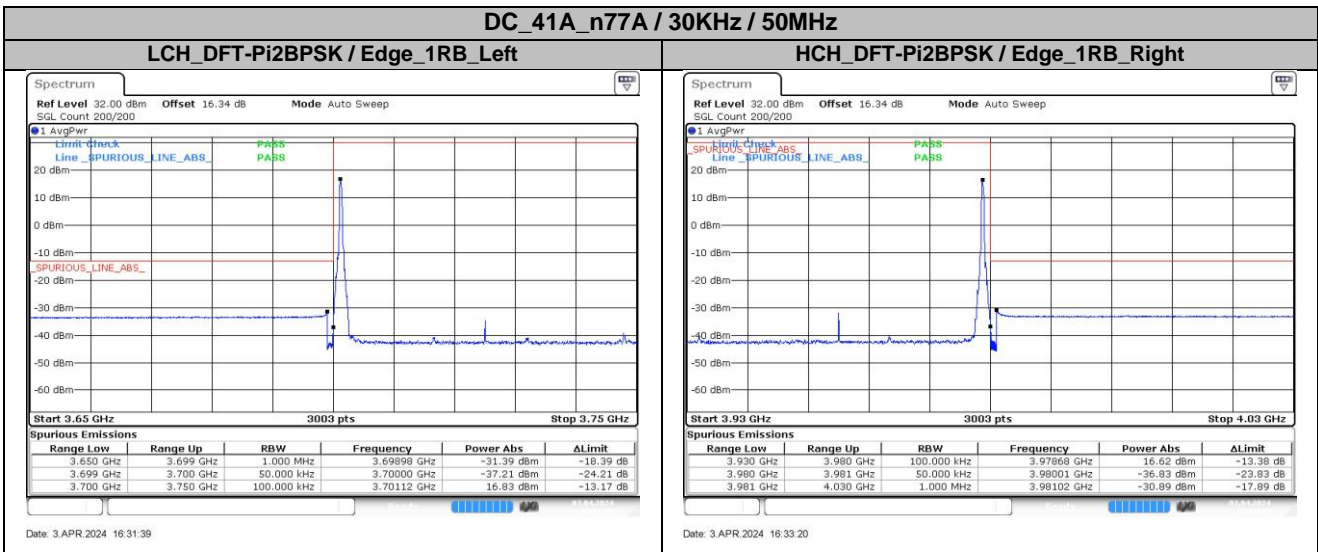
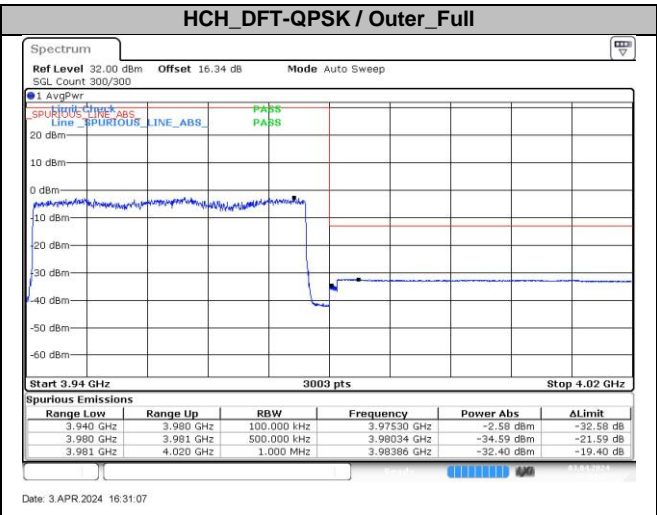
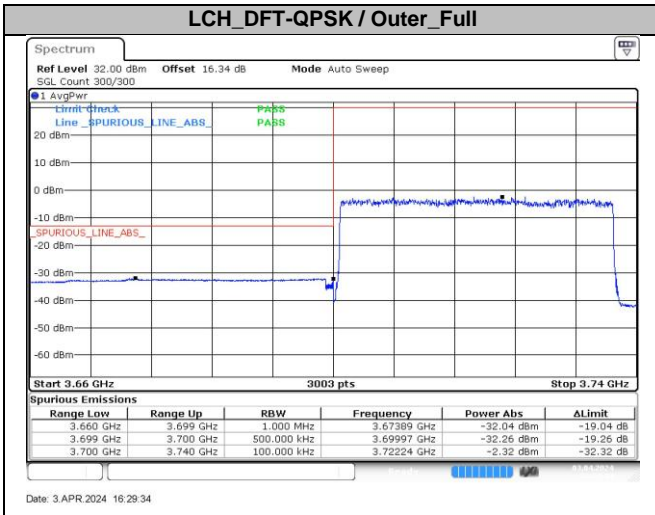


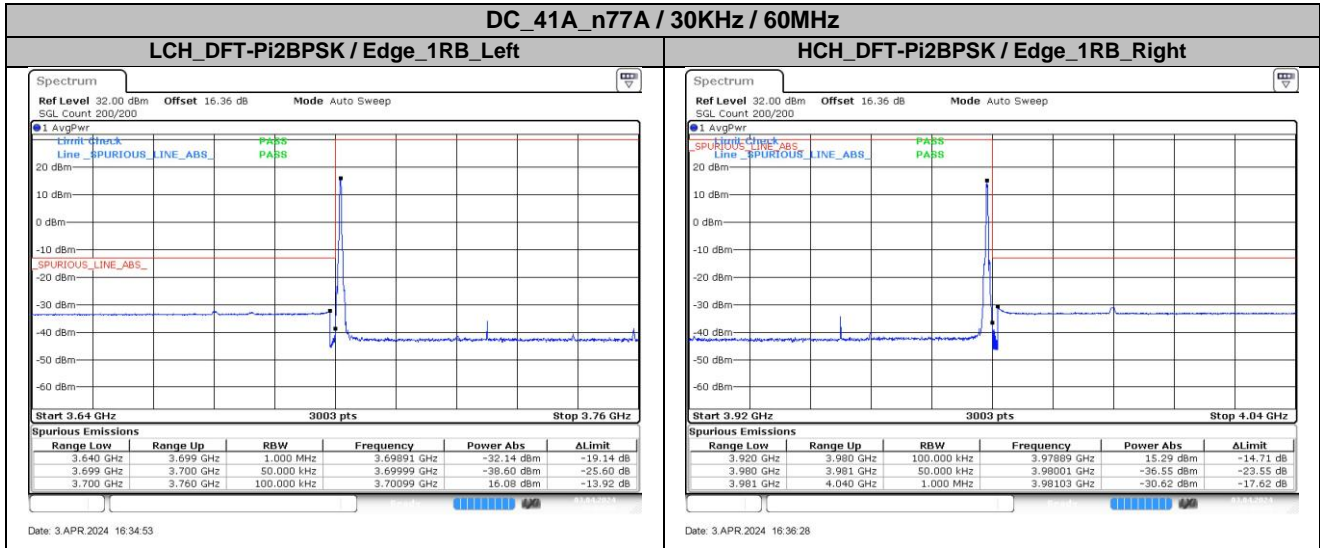
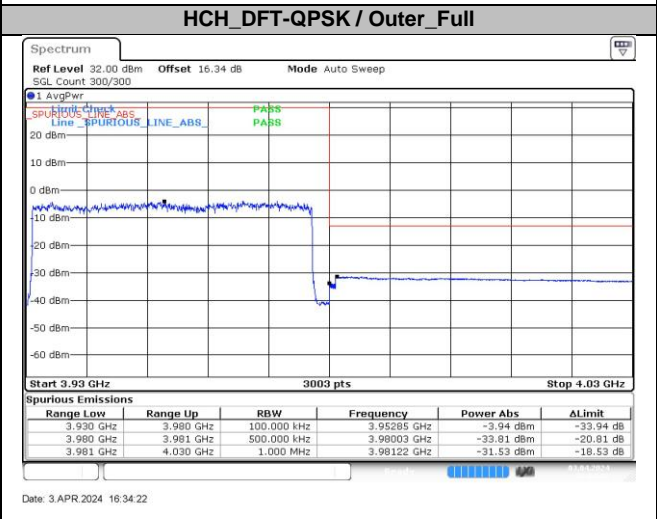
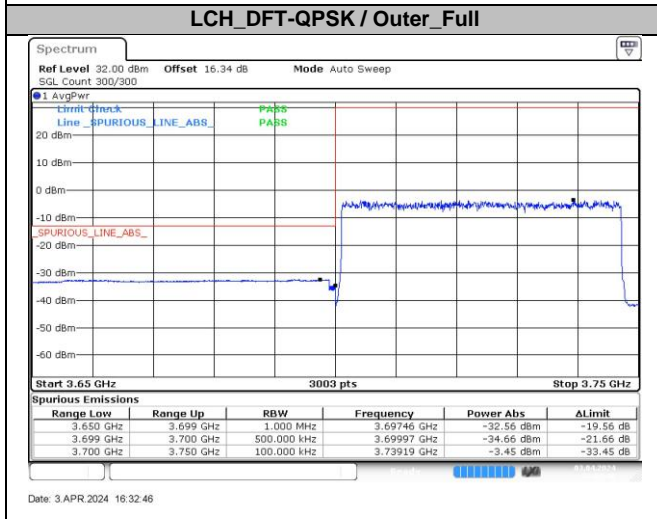
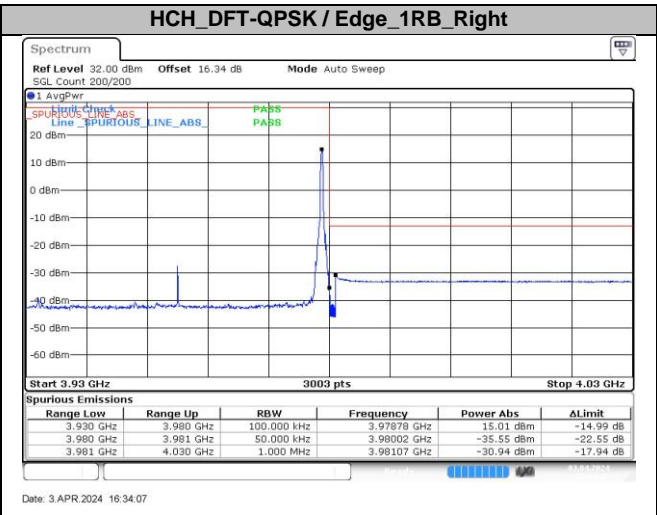
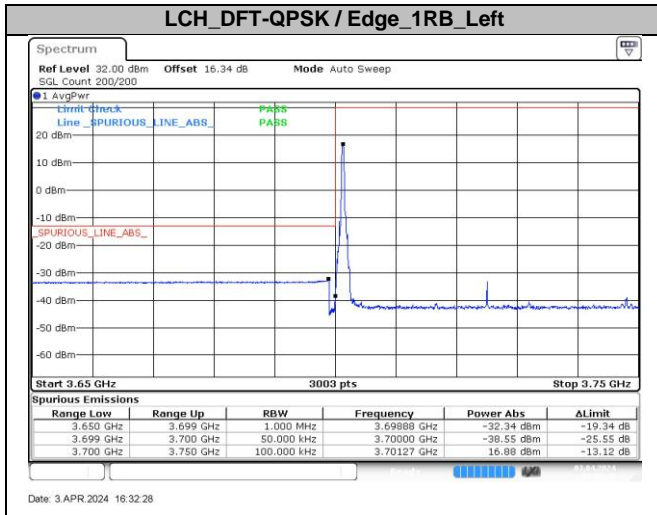
Date: 3 APR 2024 16:29:18

HCH\_DFT-QPSK / Edge\_1RB\_Right

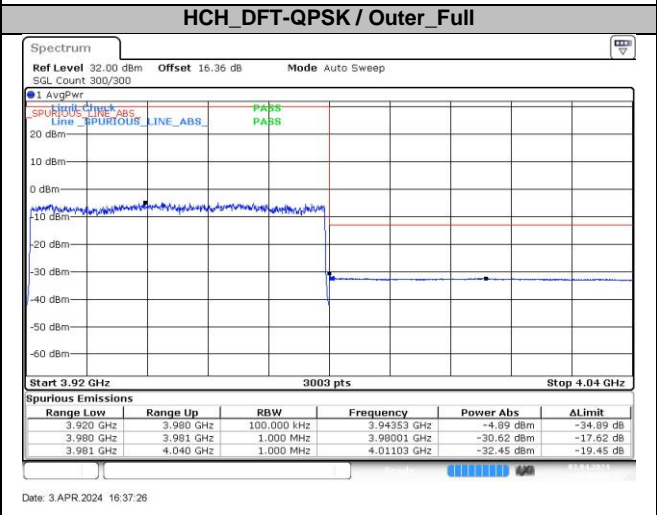
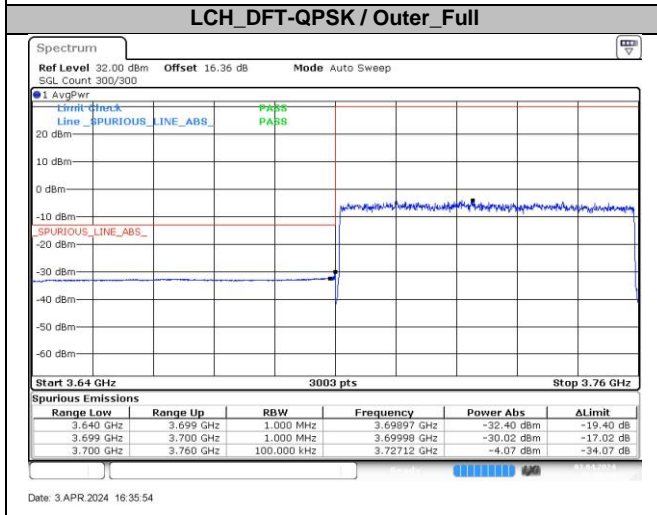
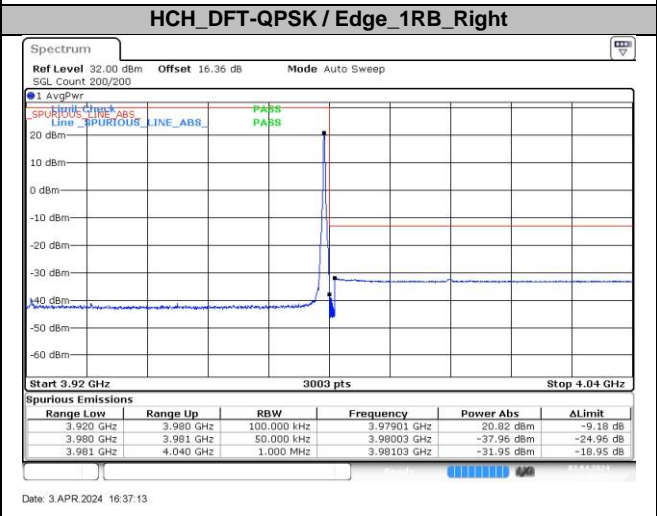
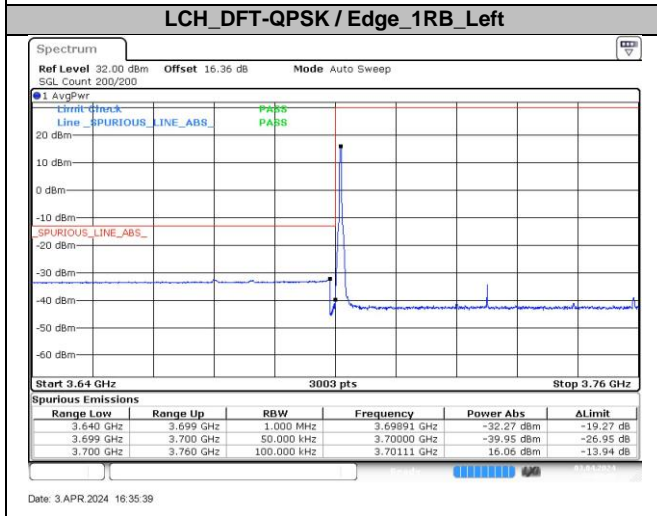
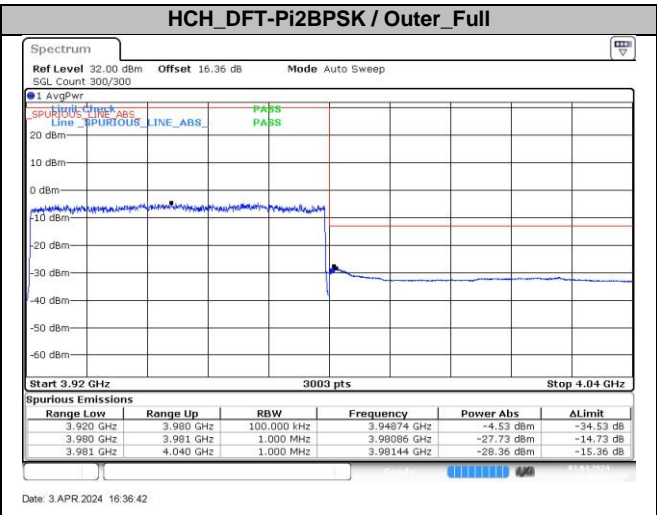
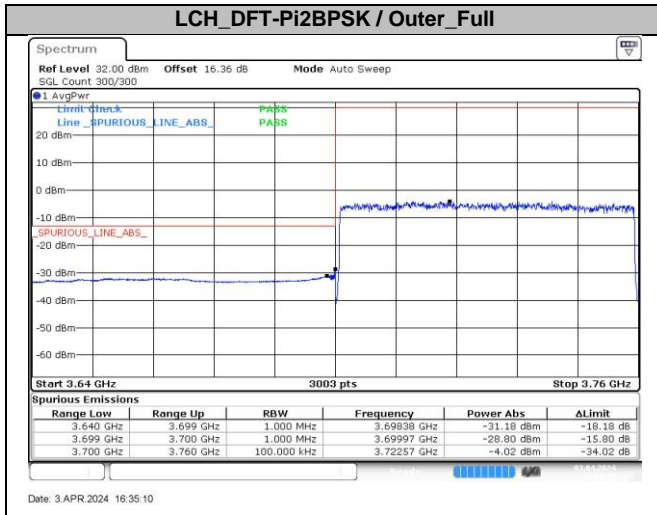


Date: 3 APR 2024 16:30:54



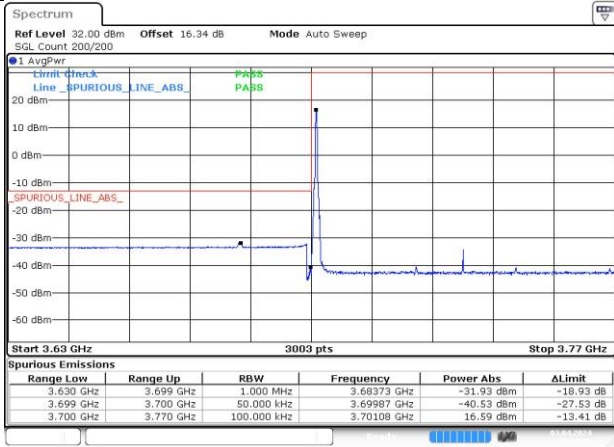






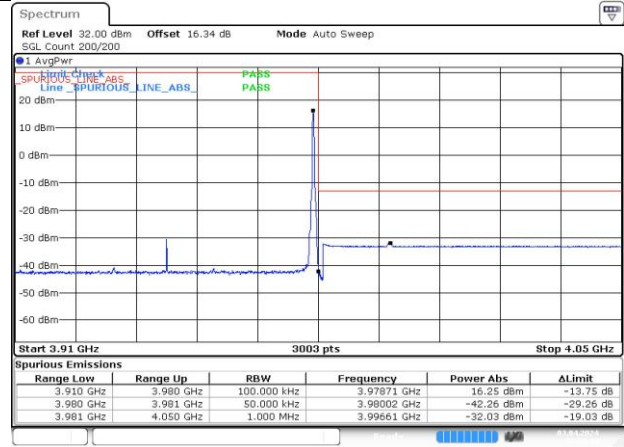
DC\_41A\_n77A / 30KHz / 70MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



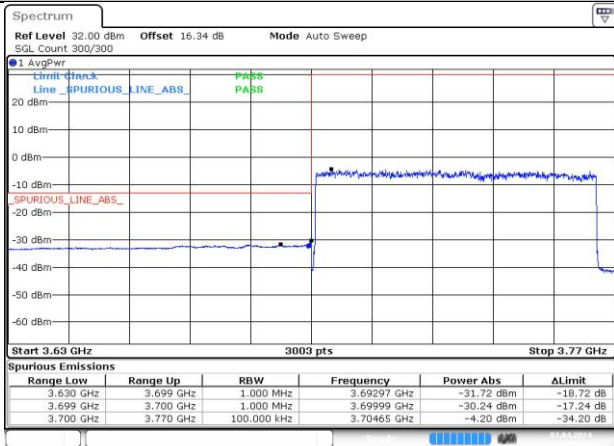
Date: 3 APR 2024 16:37:58

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Right



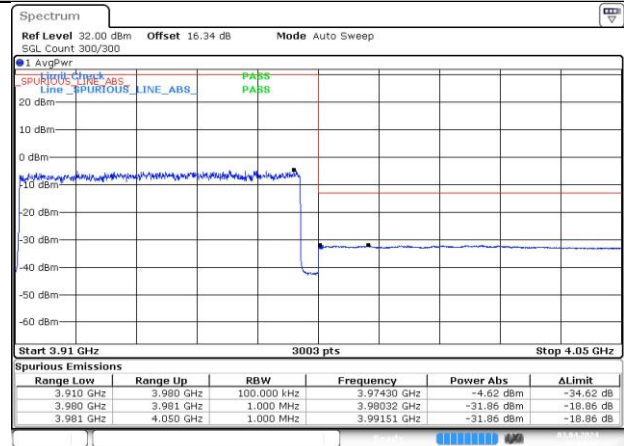
Date: 3 APR 2024 16:38:35

LCH\_DFT-Pi2BPSK / Outer\_Full



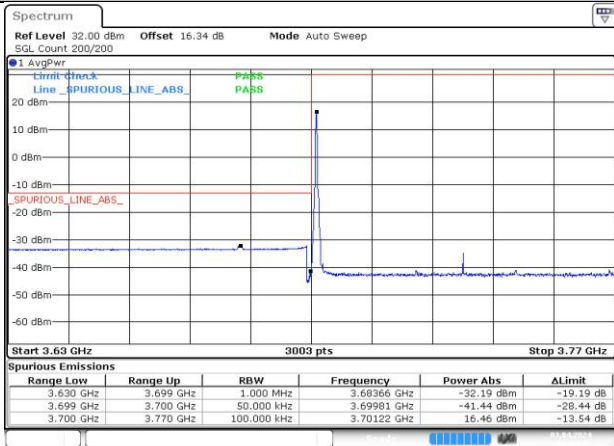
Date: 3 APR 2024 16:38:16

HCH\_DFT-Pi2BPSK / Outer\_Full



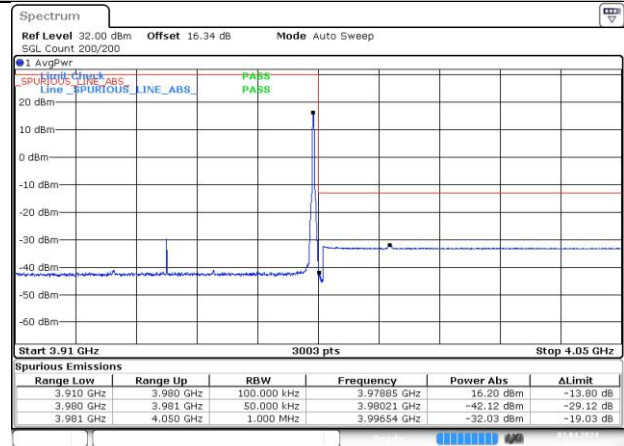
Date: 3 APR 2024 16:38:50

LCH\_DFT-QPSK / Edge\_1RB\_Left



Date: 3 APR 2024 16:38:45

HCH\_DFT-QPSK / Edge\_1RB\_Right



Date: 3 APR 2024 16:40:21

