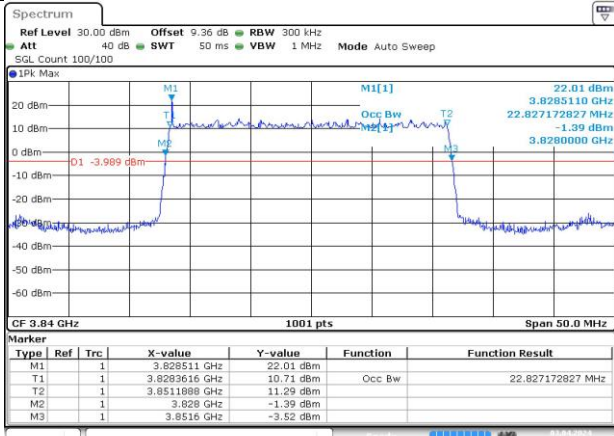


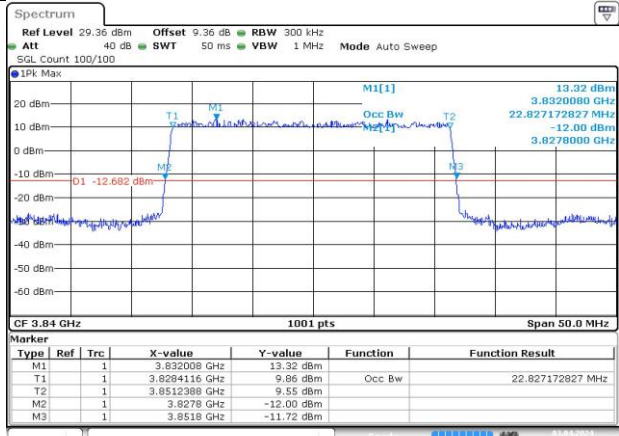
DC_41A_n77A / 30KHz / 25MHz

MCH_DFT-Pi2BPSK / Outer_Full



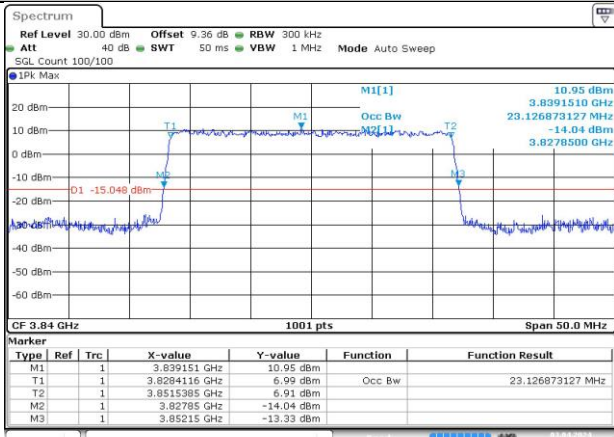
Date: 3 APR 2024 15:39:45

MCH_DFT-QPSK / Outer_Full



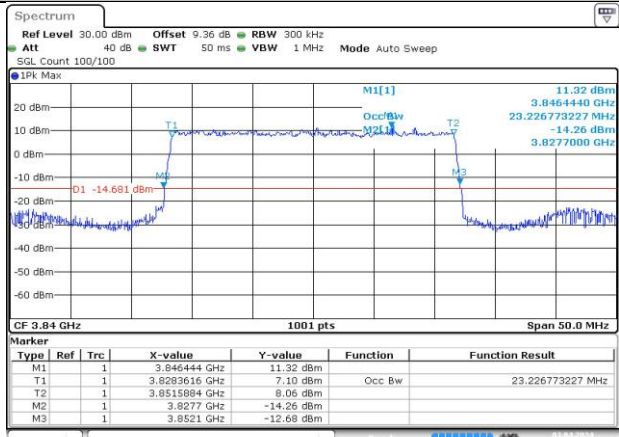
Date: 3 APR 2024 15:40:00

MCH_CP-QPSK / Outer_Full



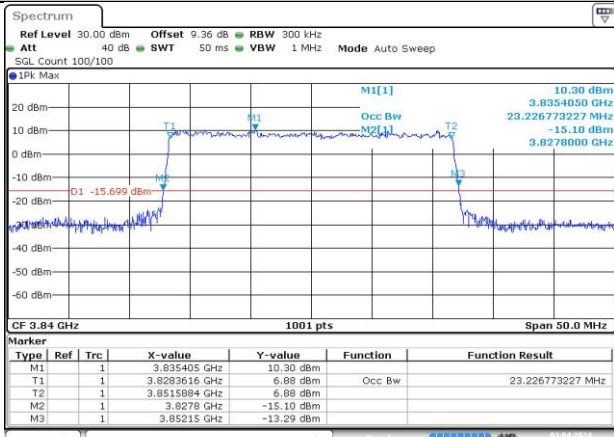
Date: 3 APR 2024 15:40:12

MCH_CP-16QAM / Outer_Full



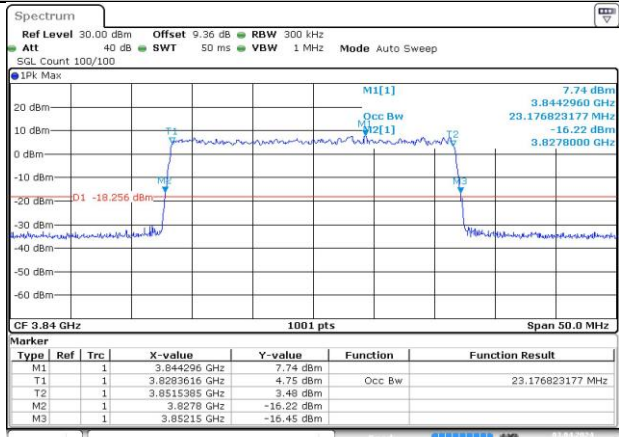
Date: 3 APR 2024 15:40:24

MCH_CP-64QAM / Outer_Full



Date: 3 APR 2024 15:40:36

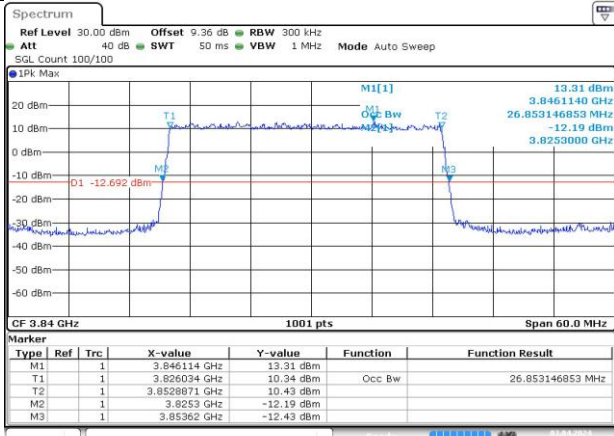
MCH_CP-256QAM / Outer_Full



Date: 3 APR 2024 15:40:49

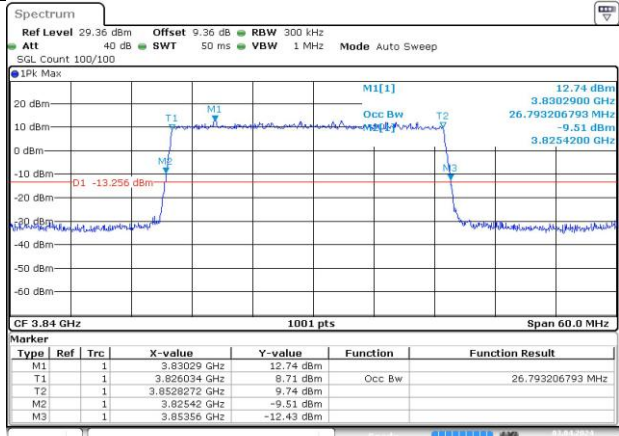
DC_41A_n77A / 30KHz / 30MHz

MCH_DFT-Pi2BPSK / Outer_Full



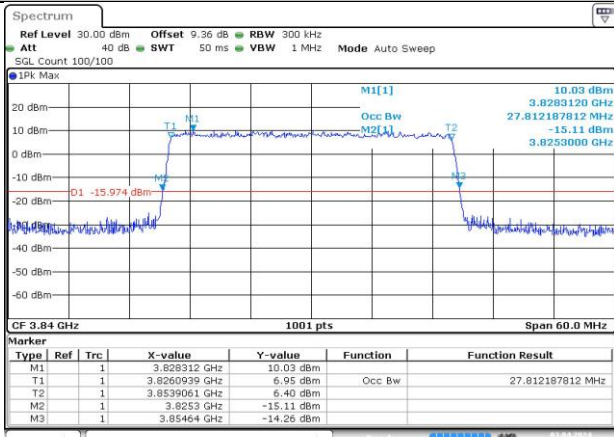
Date: 3 APR 2024 15:41:04

MCH_DFT-QPSK / Outer_Full



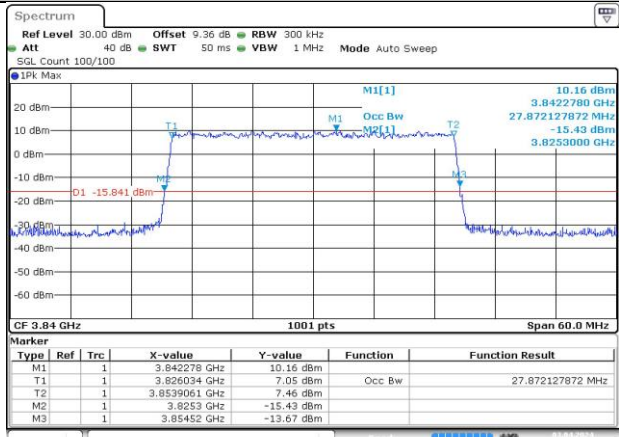
Date: 3 APR 2024 15:41:19

MCH_CP-QPSK / Outer_Full



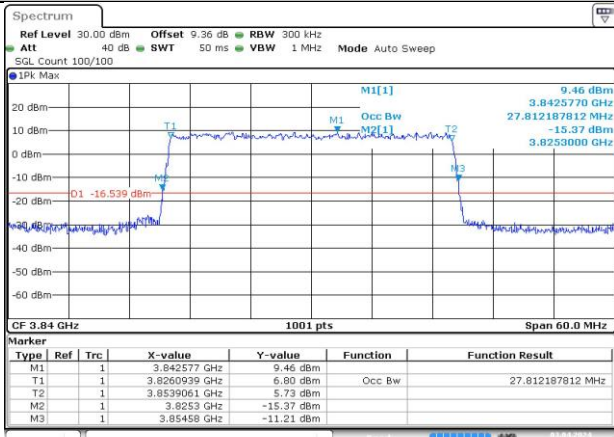
Date: 3 APR 2024 15:41:30

MCH_CP-16QAM / Outer_Full



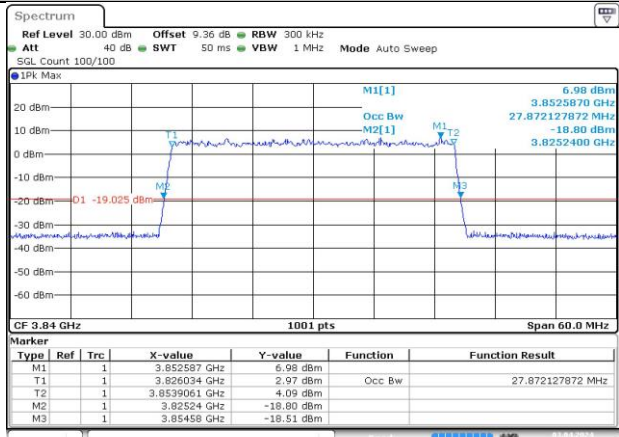
Date: 3 APR 2024 15:41:42

MCH_CP-64QAM / Outer_Full



Date: 3 APR 2024 15:41:54

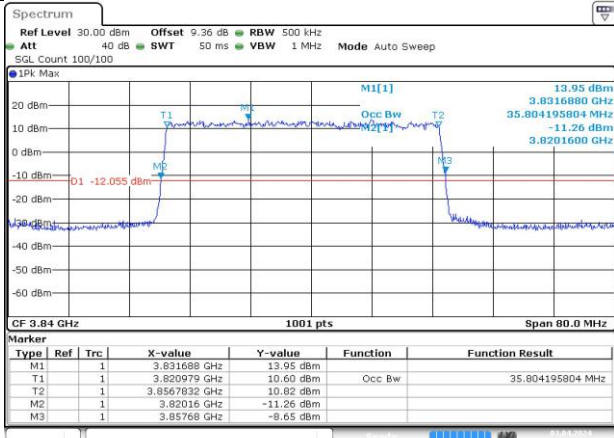
MCH_CP-256QAM / Outer_Full



Date: 3 APR 2024 15:42:07

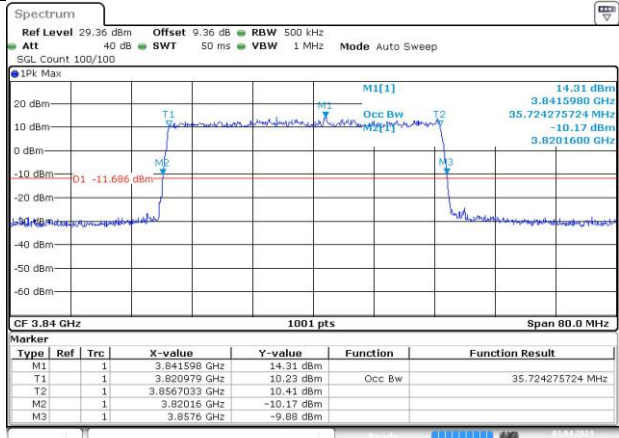
DC_41A_n77A / 30KHz / 40MHz

MCH_DFT-Pi2BPSK / Outer_Full



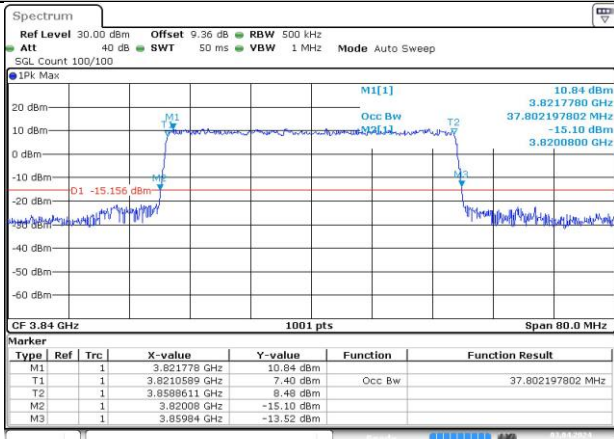
Date: 3 APR 2024 15:42:22

MCH_DFT-QPSK / Outer_Full



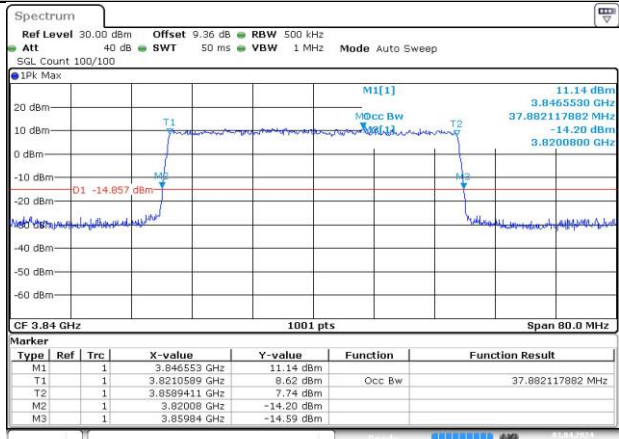
Date: 3 APR 2024 15:42:37

MCH_CP-QPSK / Outer_Full



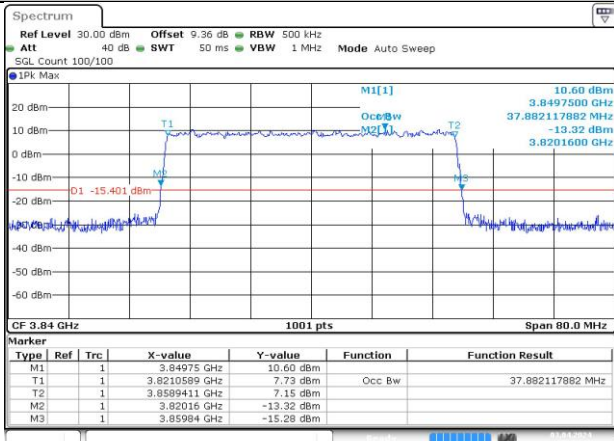
Date: 3 APR 2024 15:42:48

MCH_CP-16QAM / Outer_Full



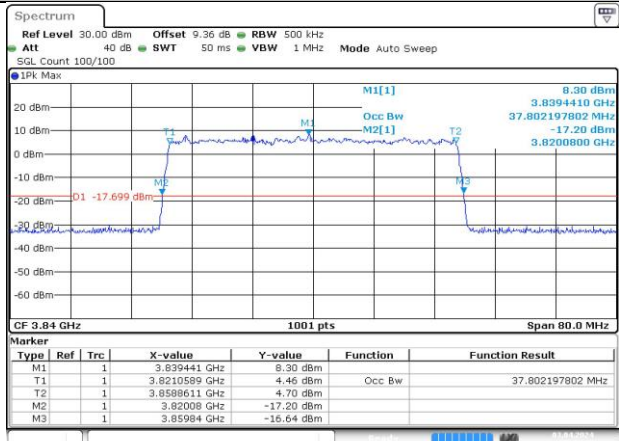
Date: 3 APR 2024 15:43:00

MCH_CP-64QAM / Outer_Full



Date: 3 APR 2024 15:43:12

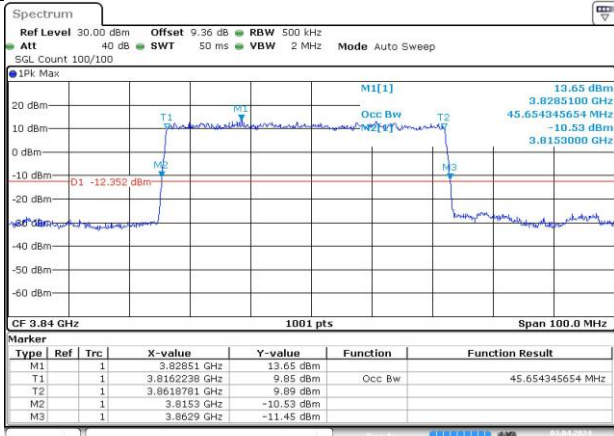
MCH_CP-256QAM / Outer_Full



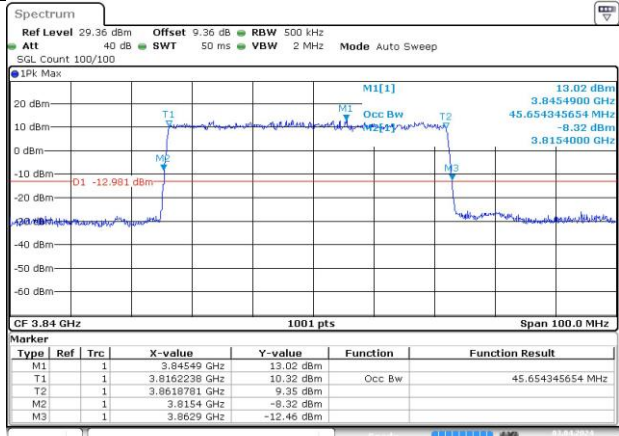
Date: 3 APR 2024 15:43:25

DC_41A_n77A / 30KHz / 50MHz

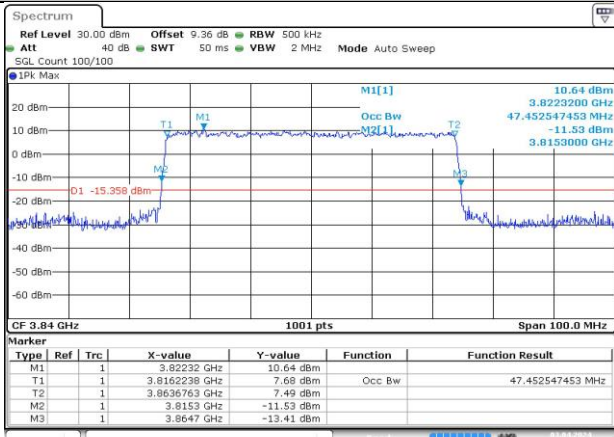
MCH_DFT-Pi2BPSK / Outer_Full



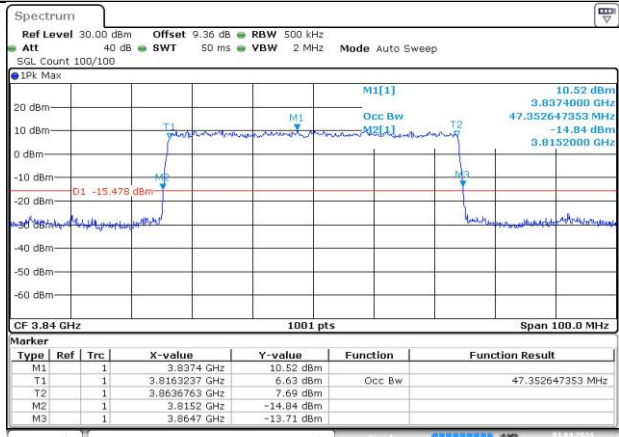
MCH_DFT-QPSK / Outer_Full



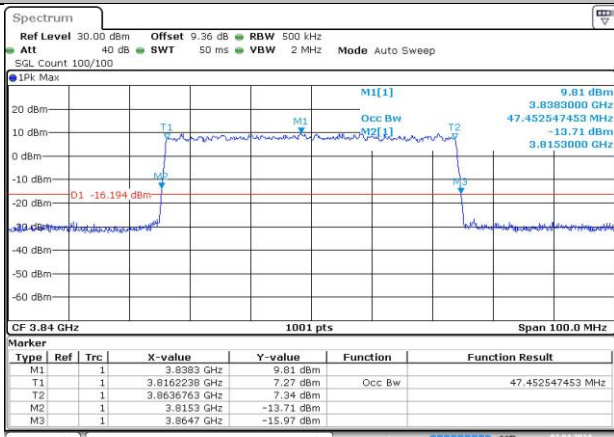
MCH_CP-QPSK / Outer_Full



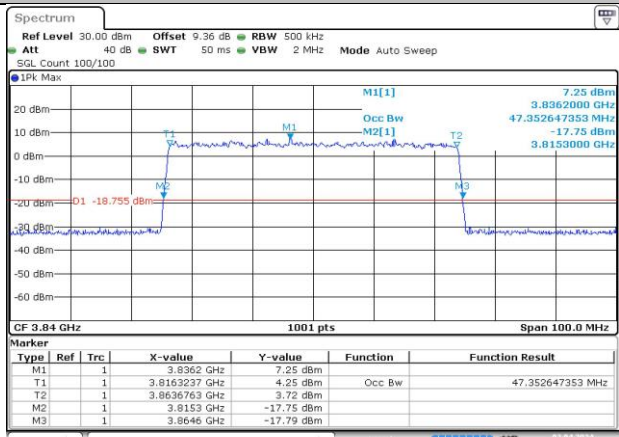
MCH_CP-16QAM / Outer_Full

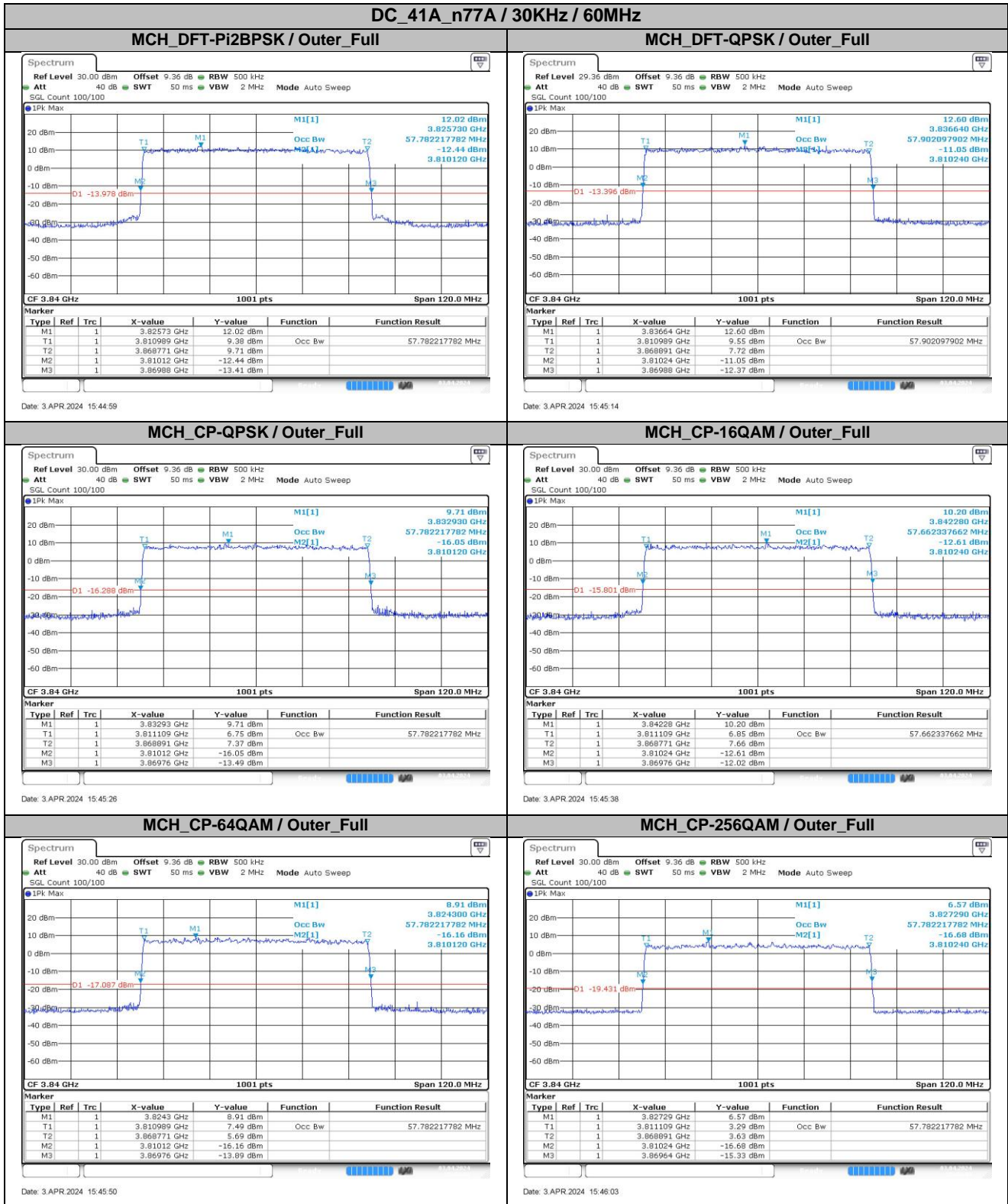


MCH_CP-64QAM / Outer_Full



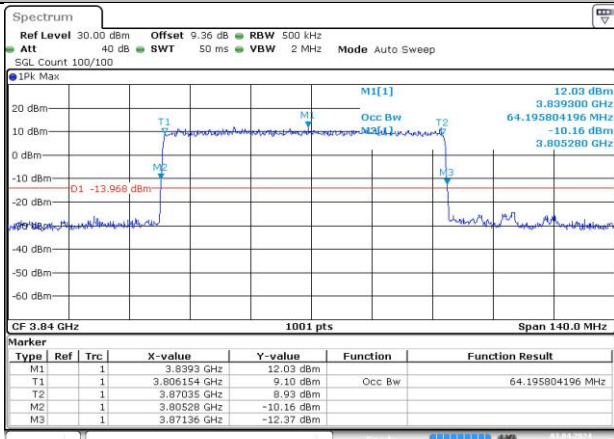
MCH_CP-256QAM / Outer_Full





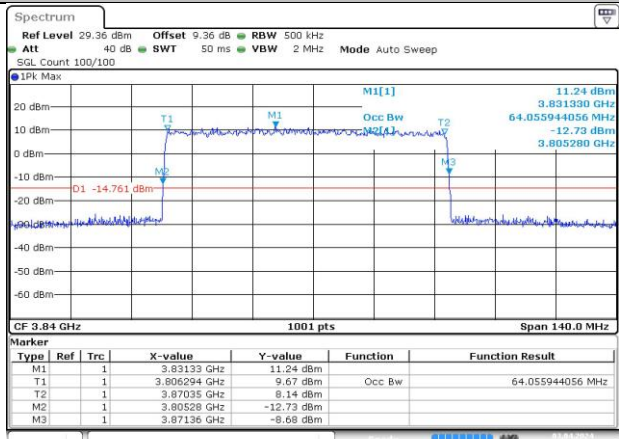
DC_41A_n77A / 30KHz / 70MHz

MCH_DFT-Pi2BPSK / Outer_Full



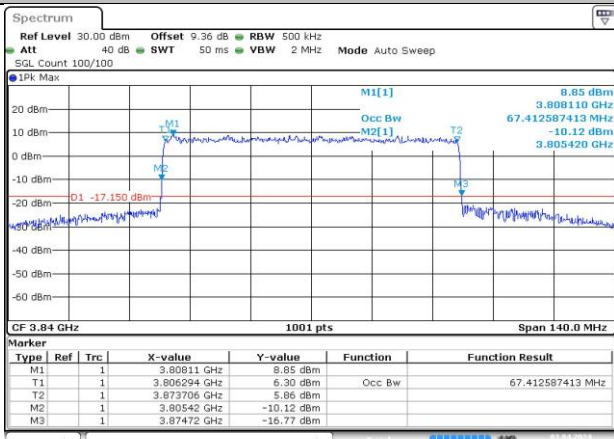
Date: 3 APR 2024 15:46:19

MCH_DFT-QPSK / Outer_Full



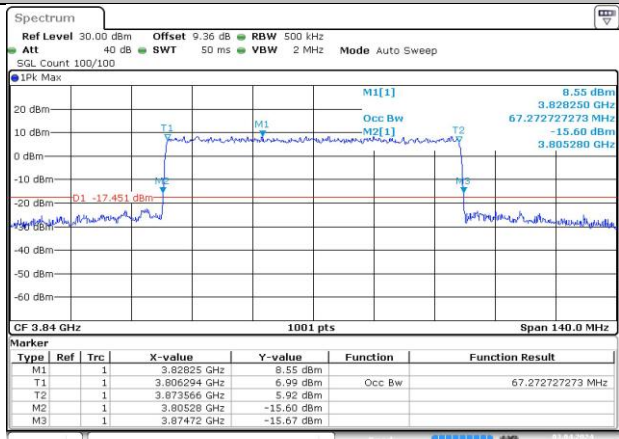
Date: 3 APR 2024 15:46:34

MCH_CP-QPSK / Outer_Full



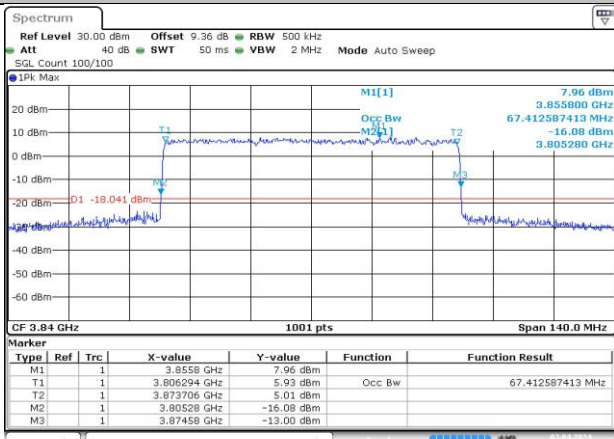
Date: 3 APR 2024 15:46:46

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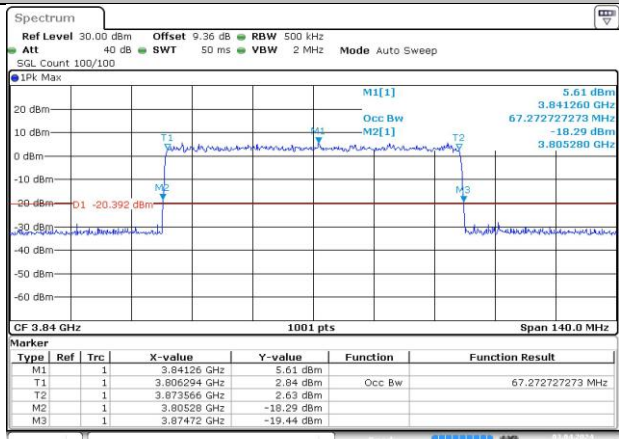
Date: 3 APR 2024 15:46:58

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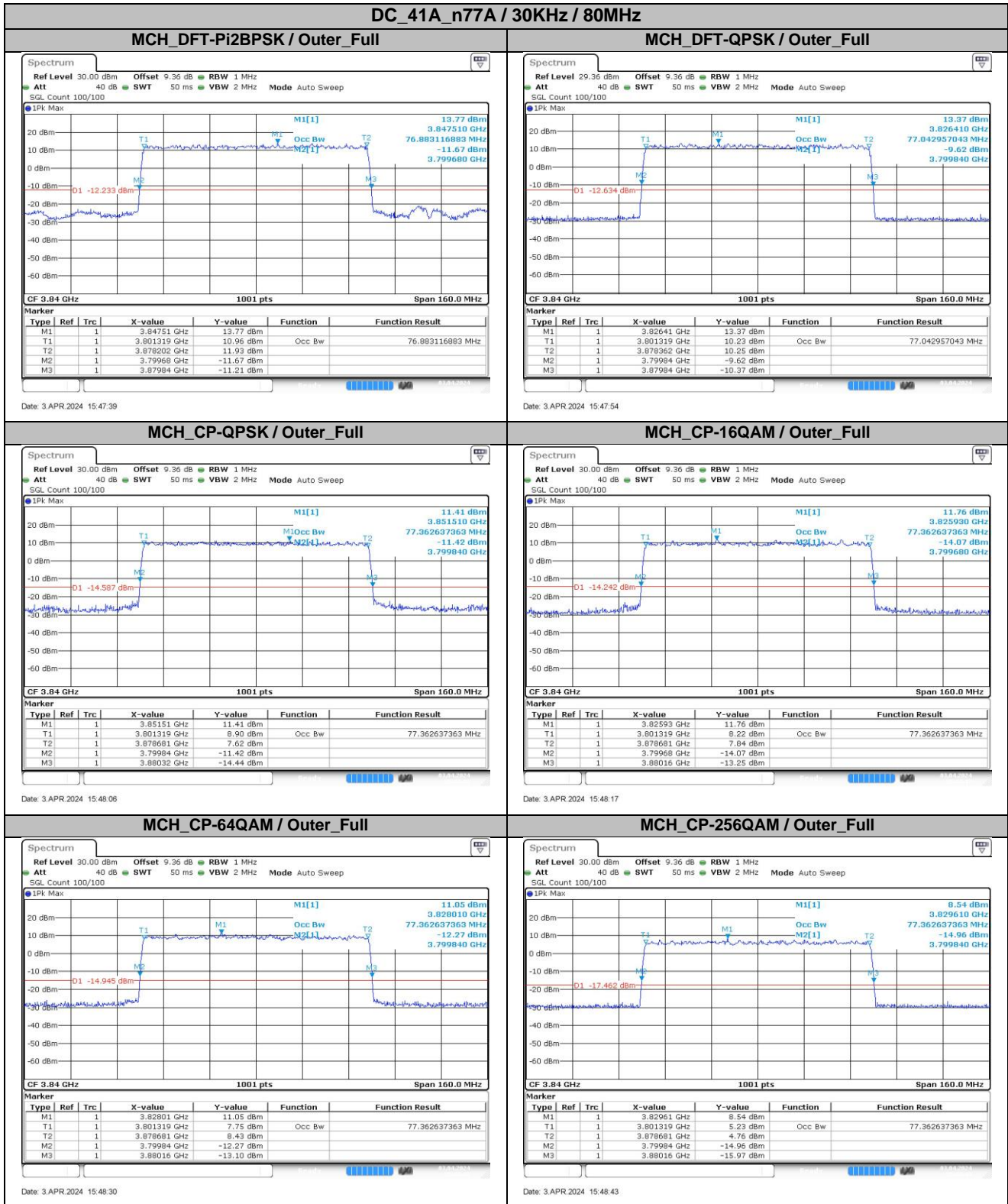


Date: 3 APR 2024 15:47:10

MCH_CP-256QAM / Outer_Full

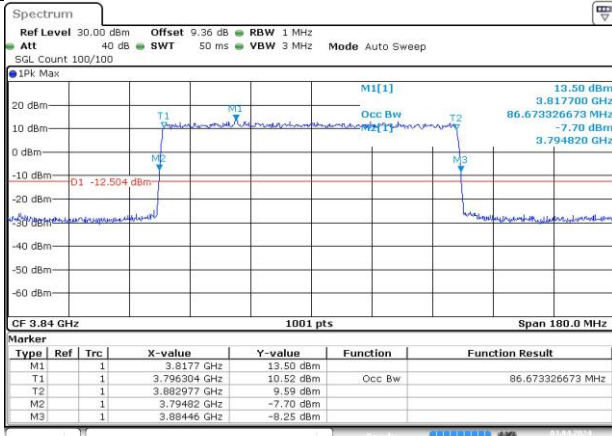


Date: 3 APR 2024 15:47:23



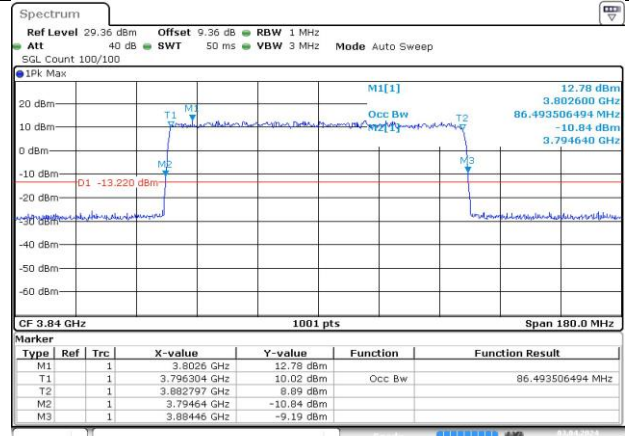
DC_41A_n77A / 30KHz / 90MHz

MCH_DFT-Pi2BPSK / Outer_Full



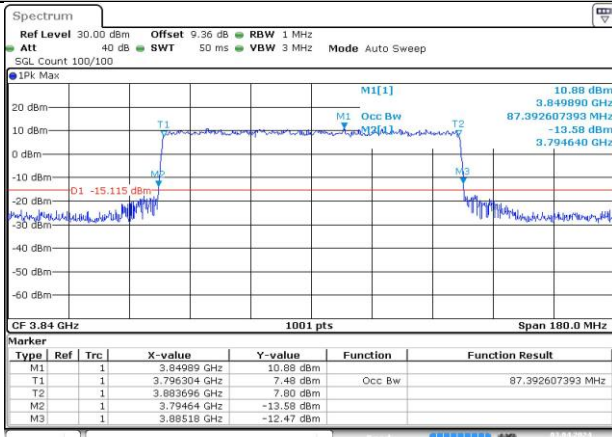
Date: 3 APR 2024 15:48:59

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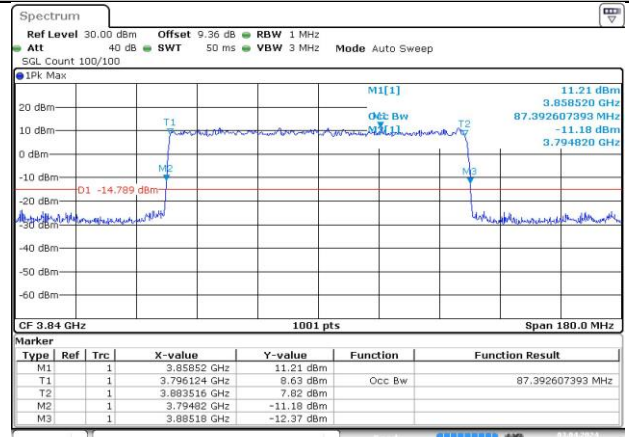
Date: 3 APR 2024 15:48:14

MCH_CP-QPSK / Outer_Full



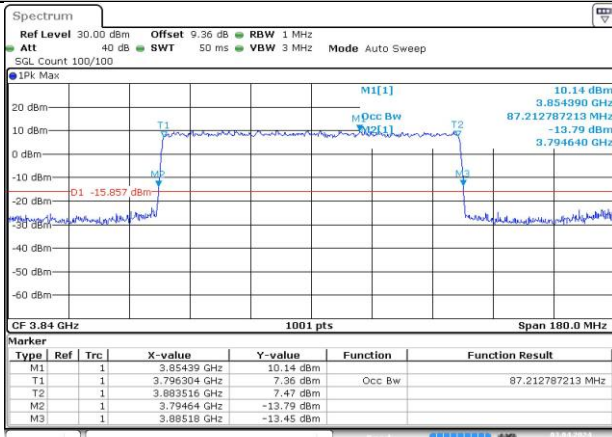
Date: 3 APR 2024 15:48:26

MCH_CP-16QAM / Outer_Full



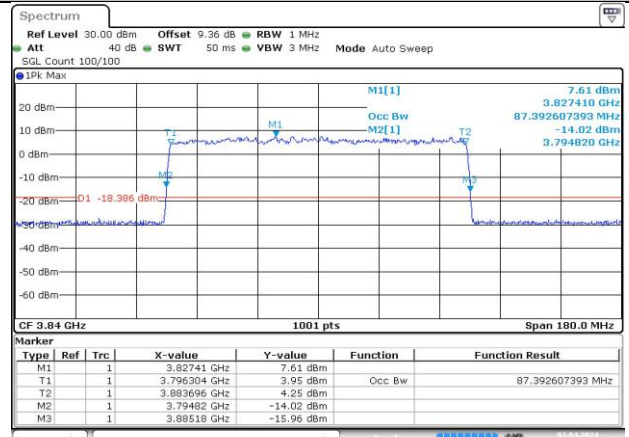
Date: 3 APR 2024 15:48:38

MCH_CP-64QAM / Outer_Full



Date: 3 APR 2024 15:49:50

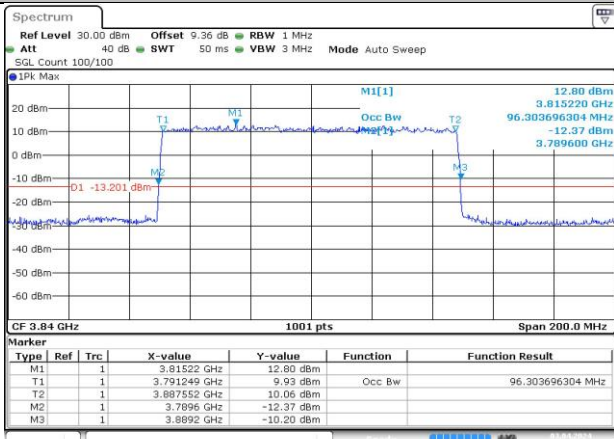
MCH_CP-256QAM / Outer_Full



Date: 3 APR 2024 15:50:03

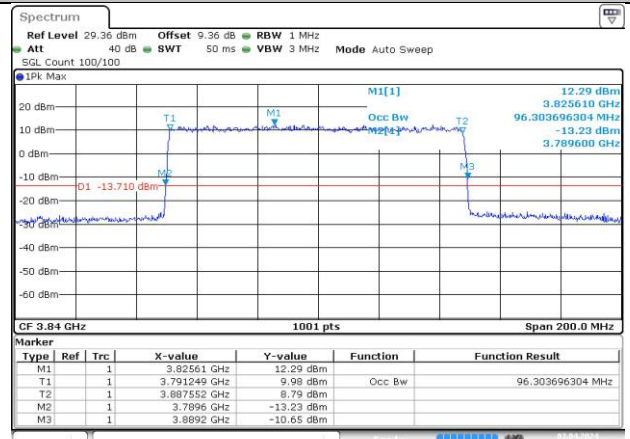
DC_41A_n77A / 30KHz / 100MHz

MCH_DFT-Pi2BPSK / Outer_Full



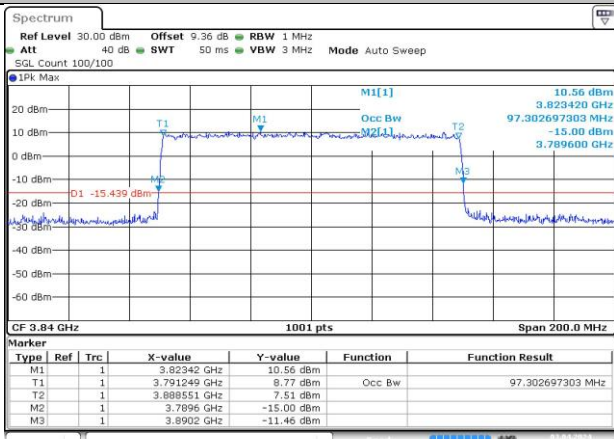
Date: 3 APR 2024 15:50:18

MCH_DFT-QPSK / Outer_Full



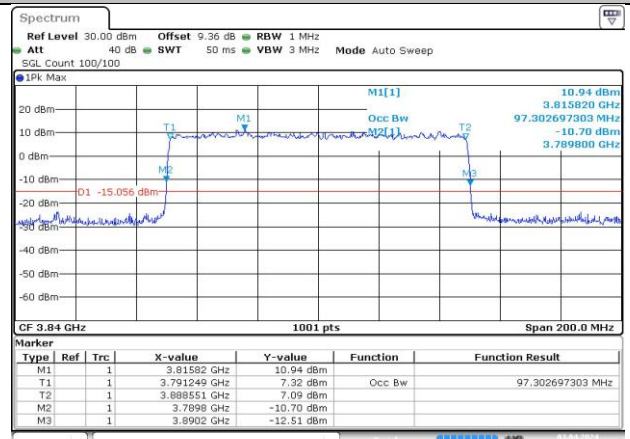
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MCH_CP-QPSK / Outer_Full



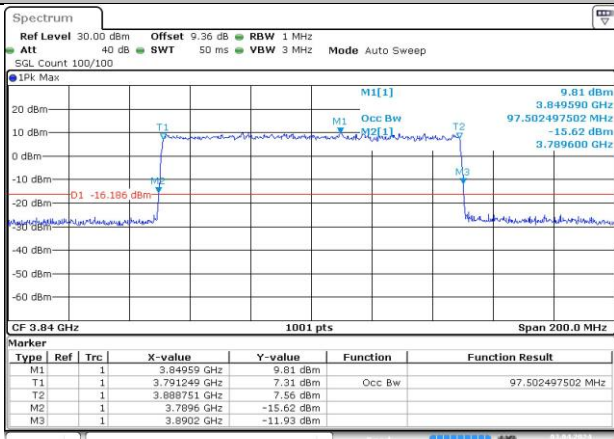
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MCH_CP-16QAM / Outer_Full



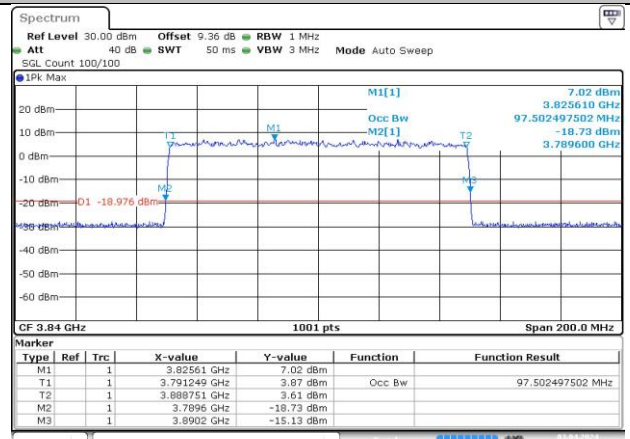
Date: 3 APR 2024 15:50:58

MCH_CP-64QAM / Outer_Full



Date: 3 APR 2024 15:51:10

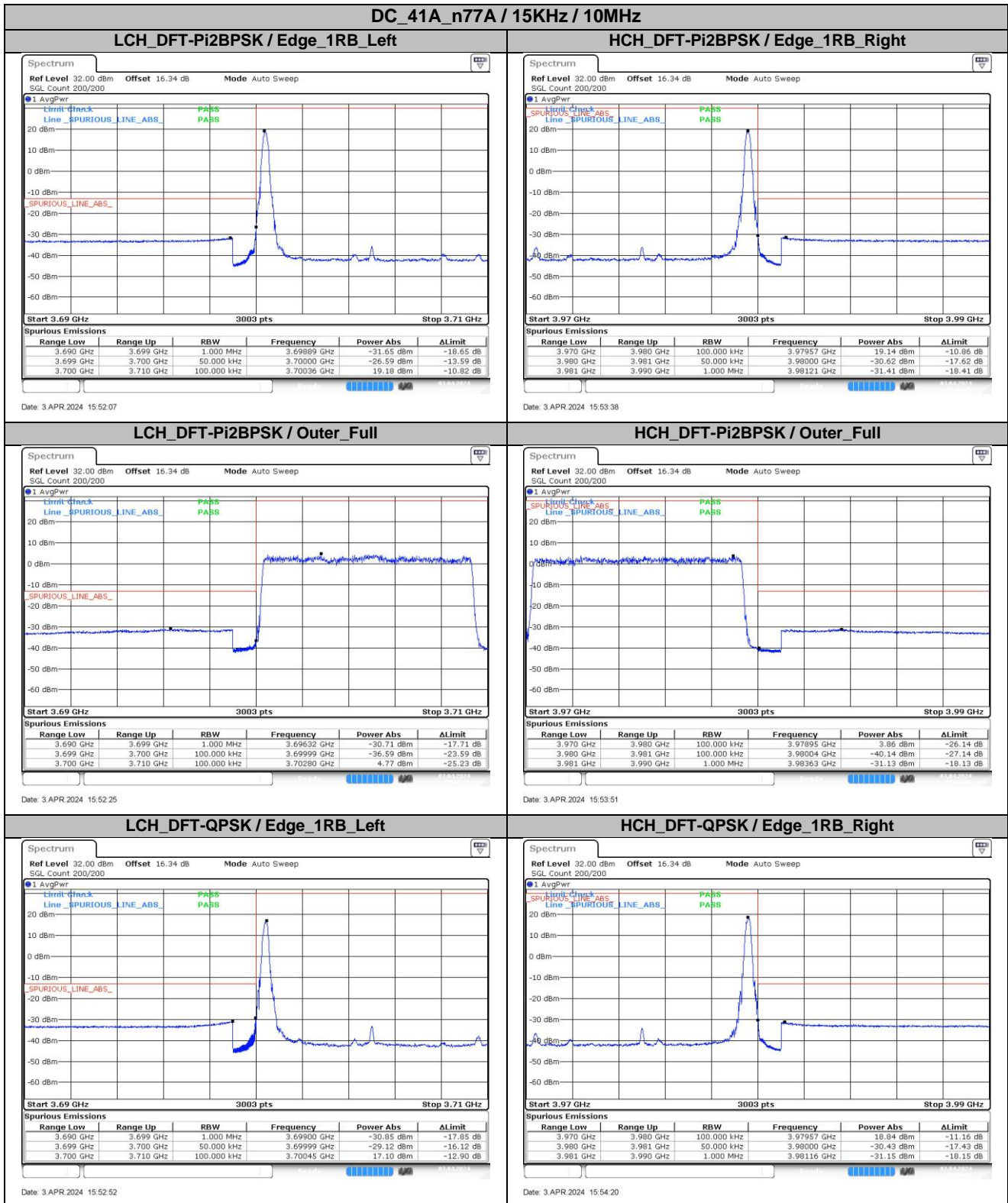
MCH_CP-256QAM / Outer_Full

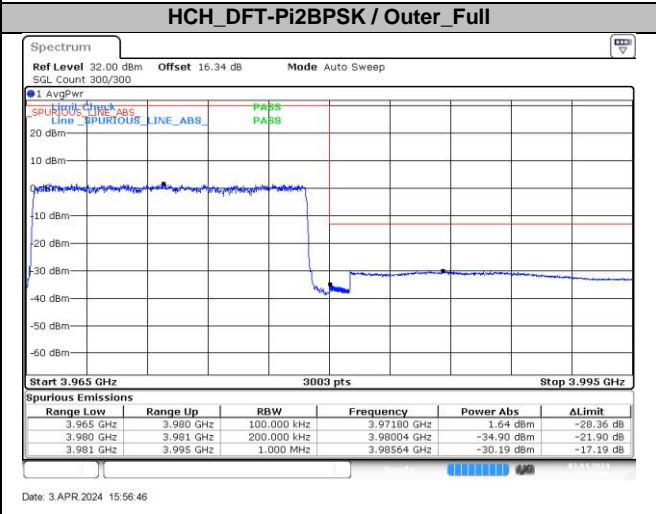
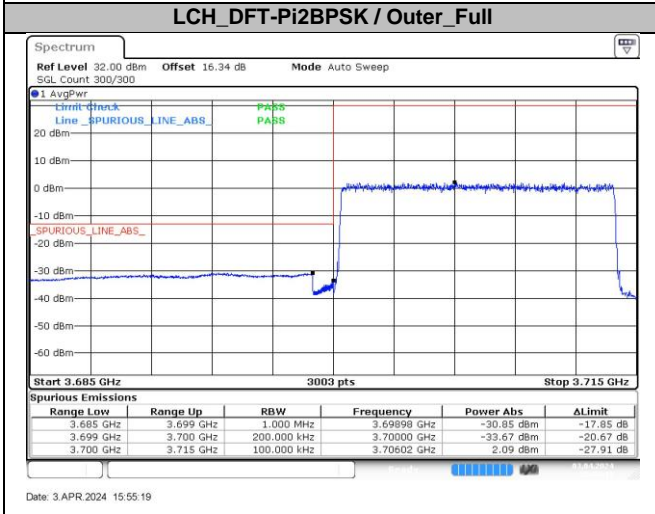
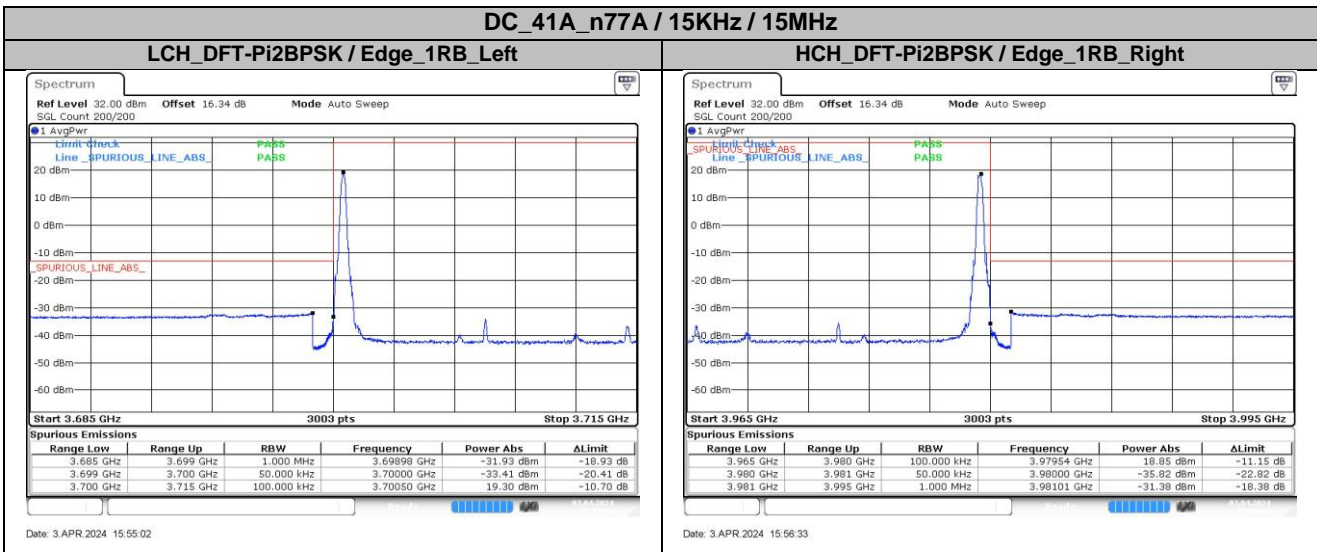
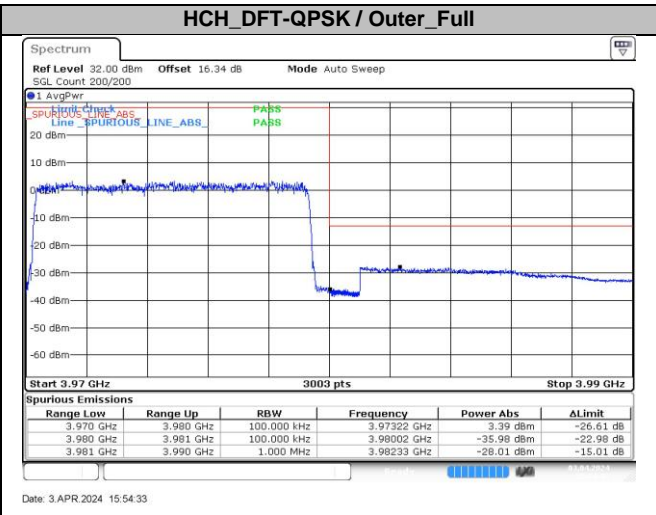
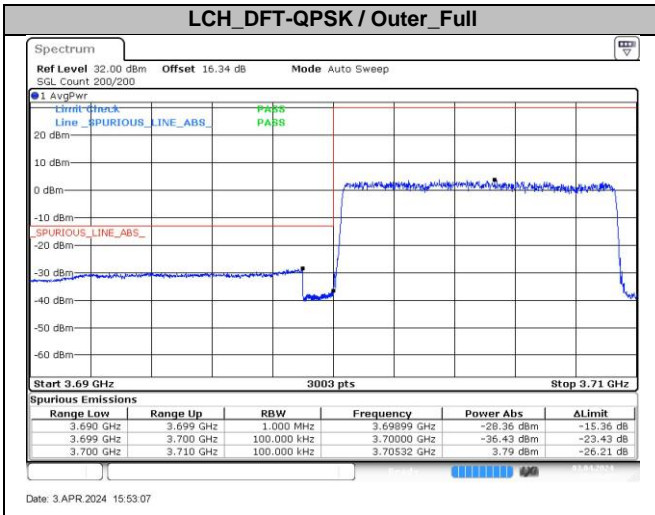


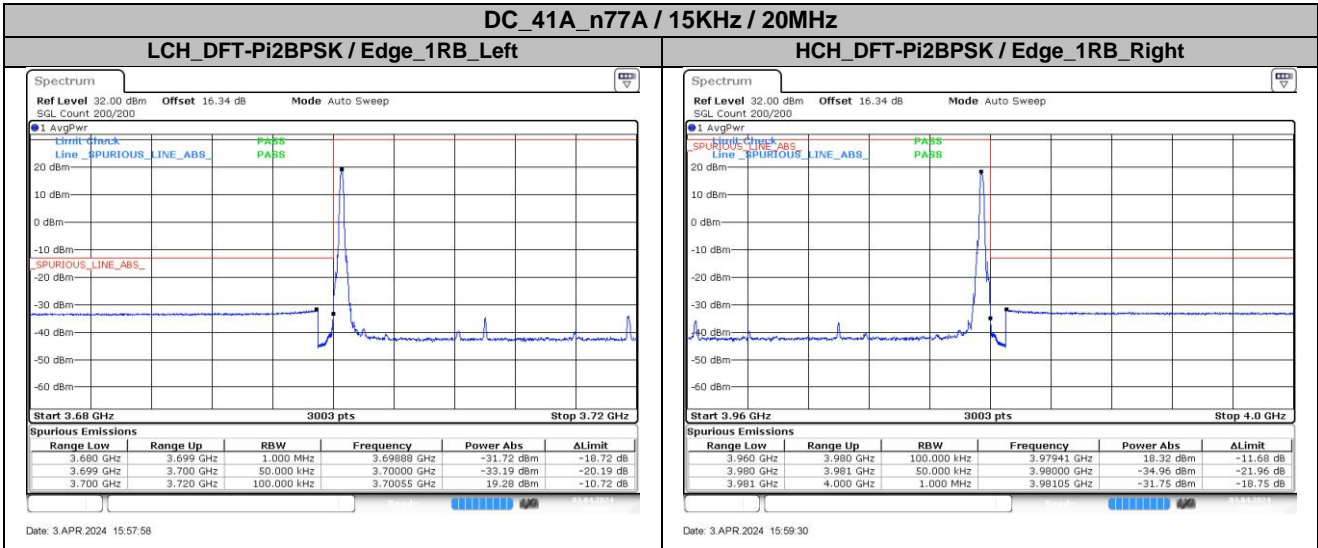
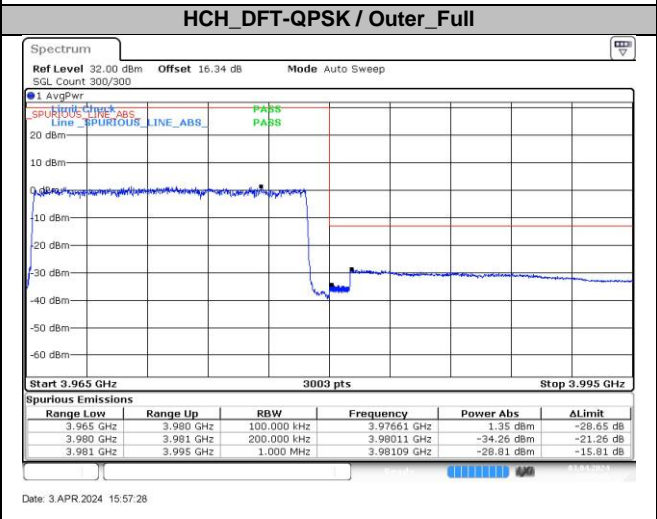
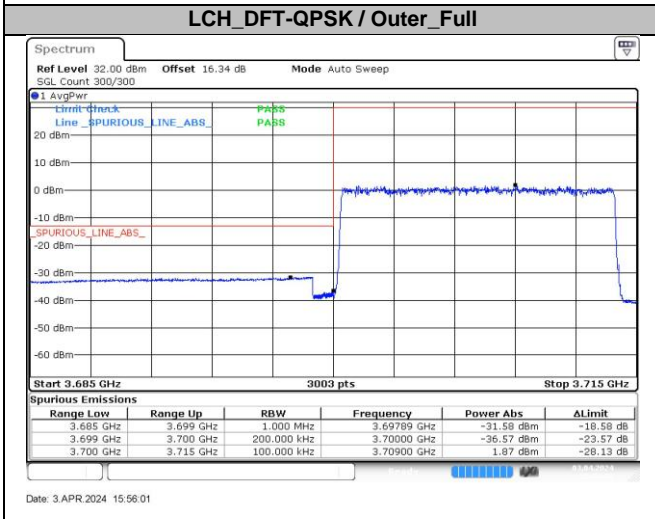
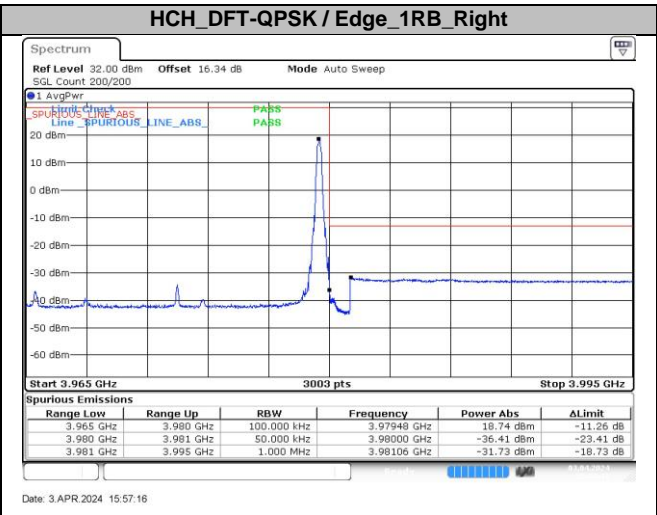
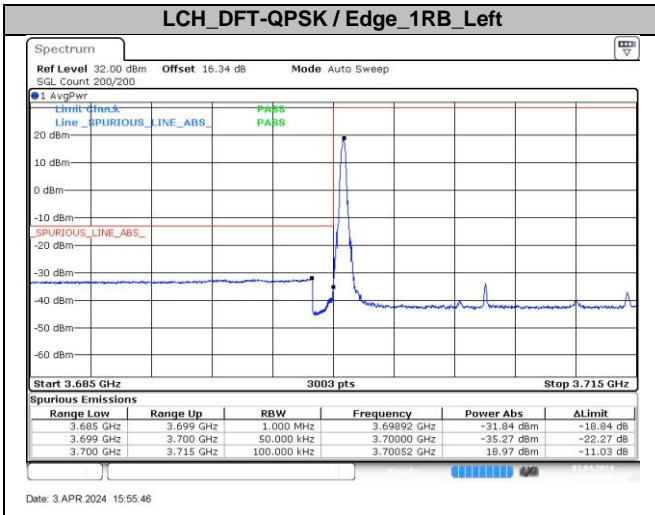
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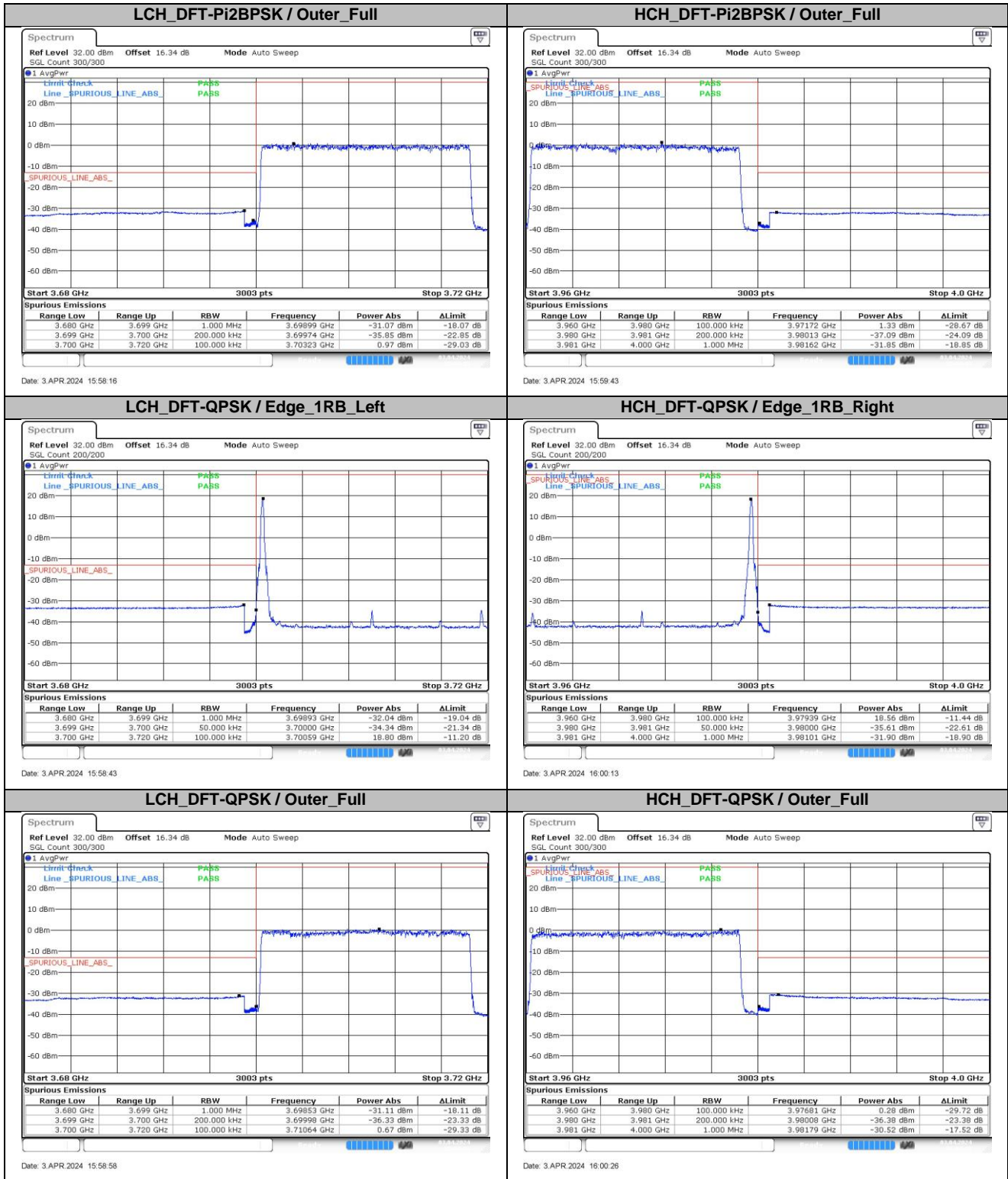
5. Conducted Band Edges

5.1. Test Plots



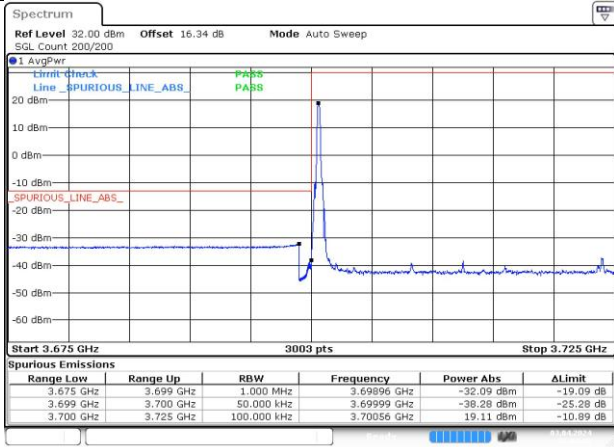






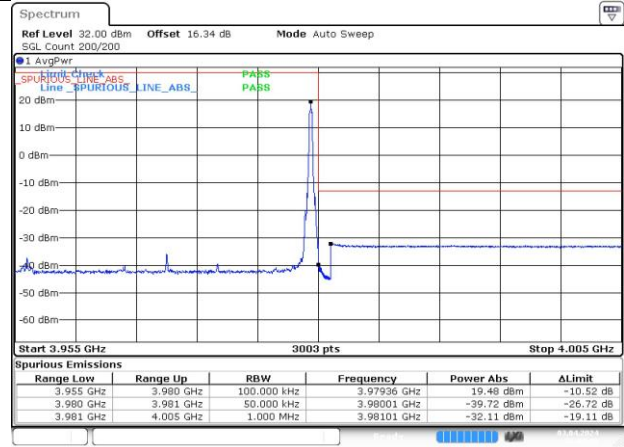
DC_41A_n77A / 15KHz / 25MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



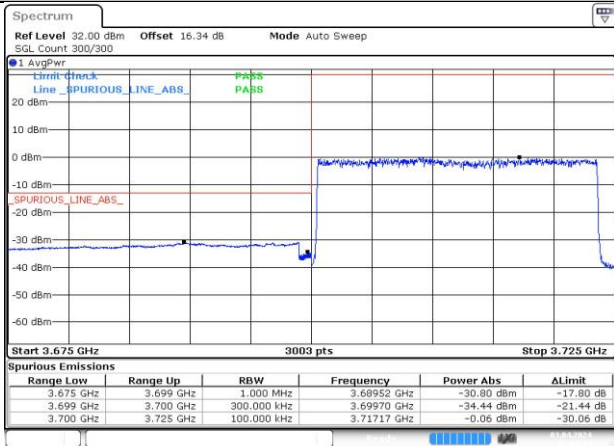
Date: 3 APR 2024 16:00:57

HCH_DFT-Pi2BPSK / Edge_1RB_Right



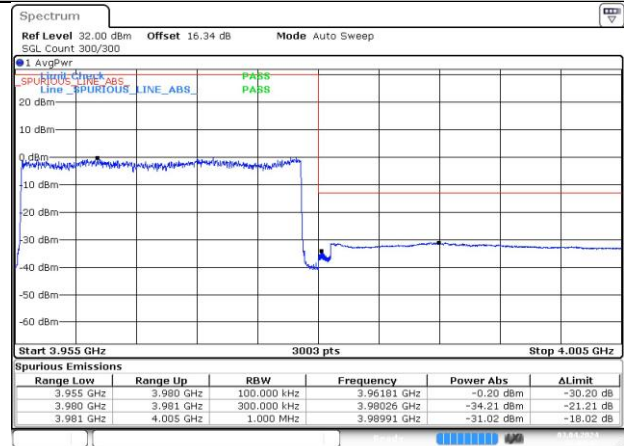
Date: 3 APR 2024 16:02:29

LCH_DFT-Pi2BPSK / Outer_Full



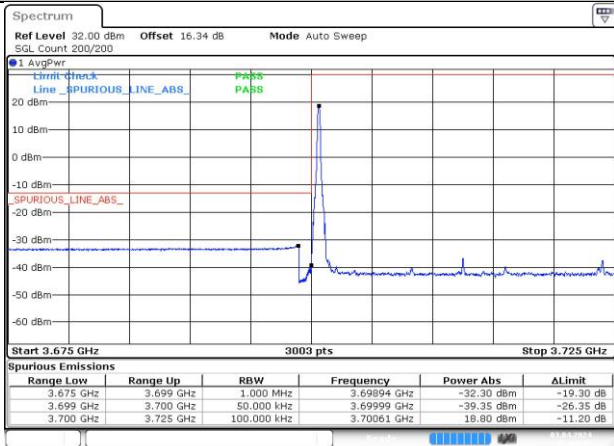
Date: 3 APR 2024 16:01:14

HCH_DFT-Pi2BPSK / Outer_Full



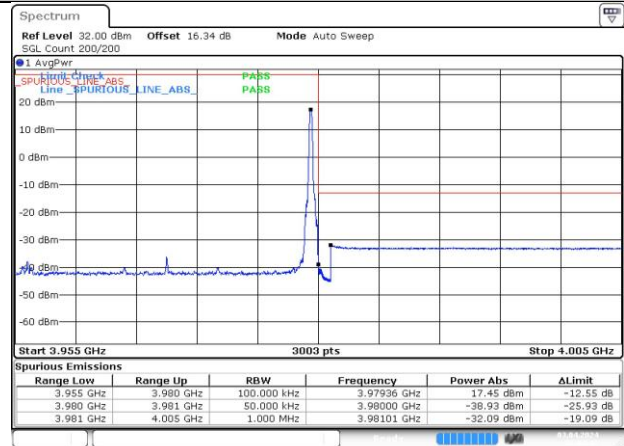
Date: 3 APR 2024 16:02:42

LCH_DFT-QPSK / Edge_1RB_Left



Date: 3 APR 2024 16:01:42

HCH_DFT-QPSK / Edge_1RB_Right



Date: 3 APR 2024 16:03:12

