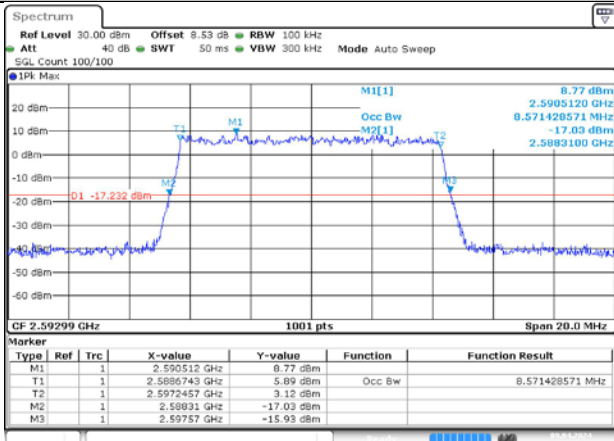


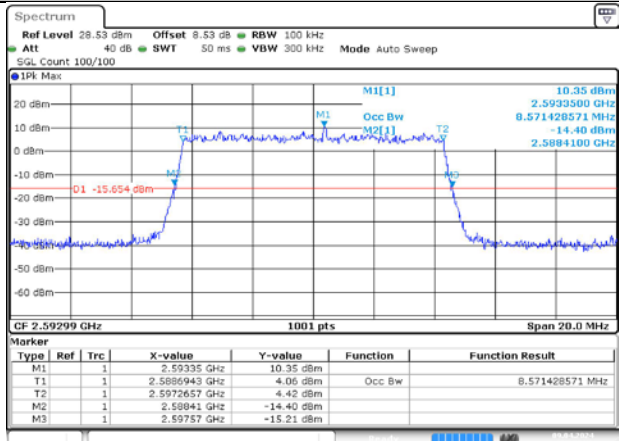
DC_66A_n41A / 30KHz / 10MHz

MCH_DFT-Pi2BPSK / Outer_Full



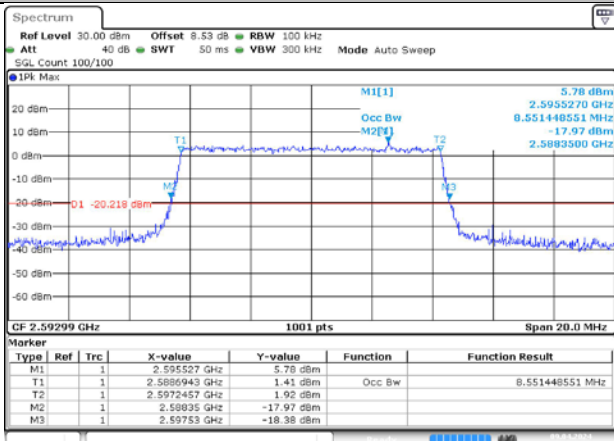
Date: 9 APR 2024 00:26:14

MCH_DFT-QPSK / Outer_Full



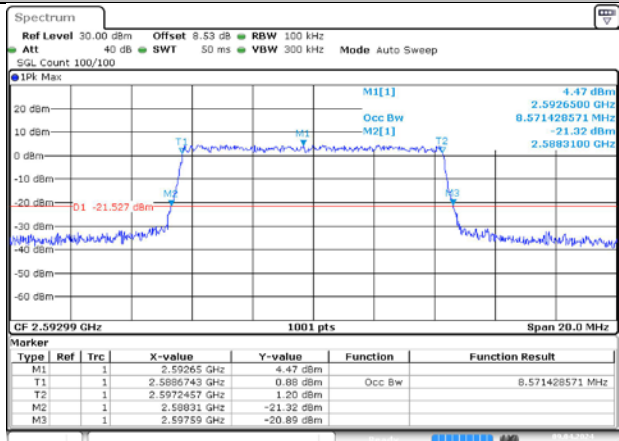
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MCH_CP-QPSK / Outer_Full



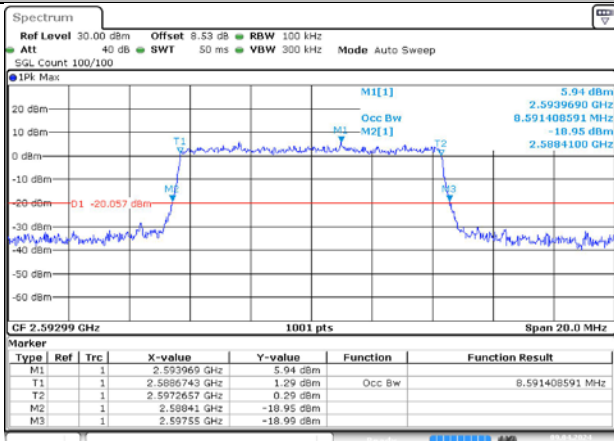
Date: 9 APR 2024 00:27:04

MCH_CP-16QAM / Outer_Full



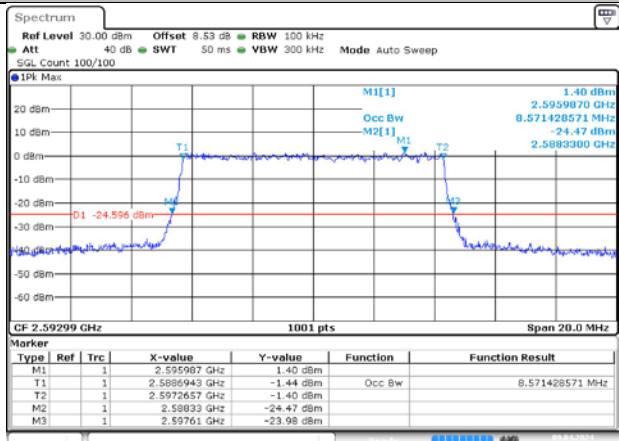
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MCH_CP-64QAM / Outer_Full

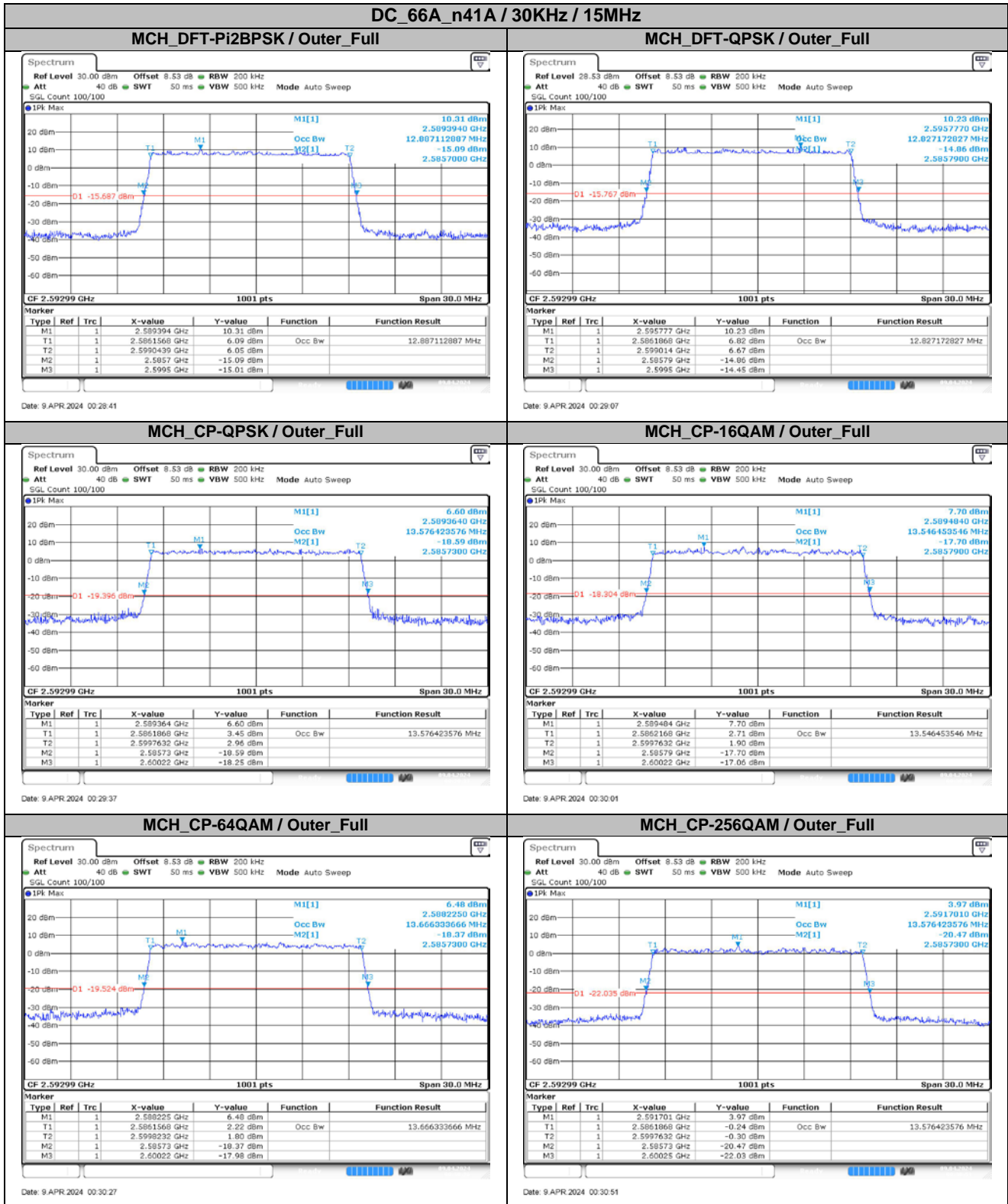


Date: 9 APR 2024 00:27:57

MCH_CP-256QAM / Outer_Full

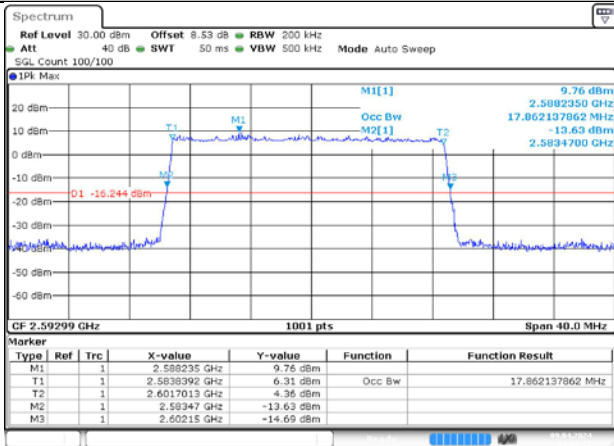


Date: 9 APR 2024 00:28:16



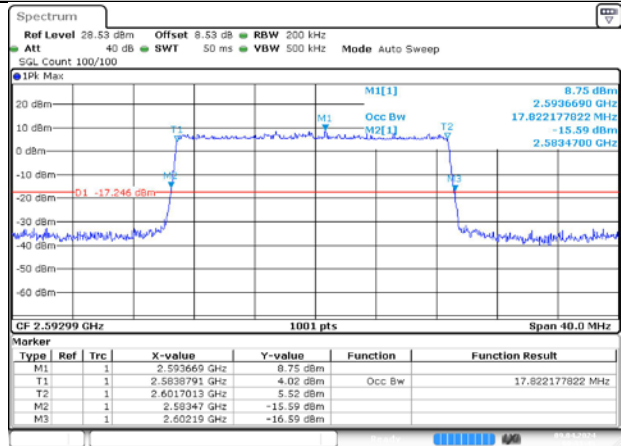
DC_66A_n41A / 30KHz / 20MHz

MCH_DFT-Pi2BPSK / Outer_Full



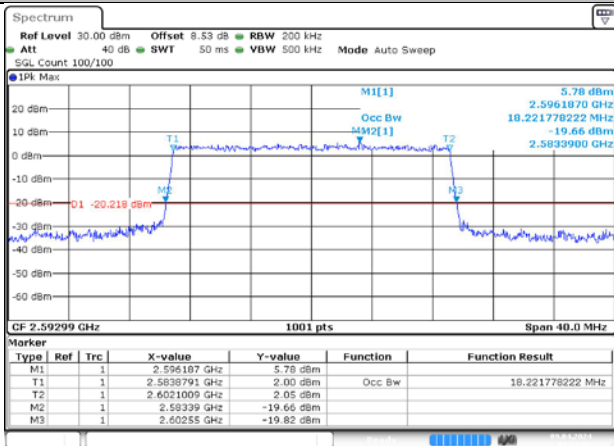
Date: 9 APR 2024 00:31:17

MCH_DFT-QPSK / Outer_Full



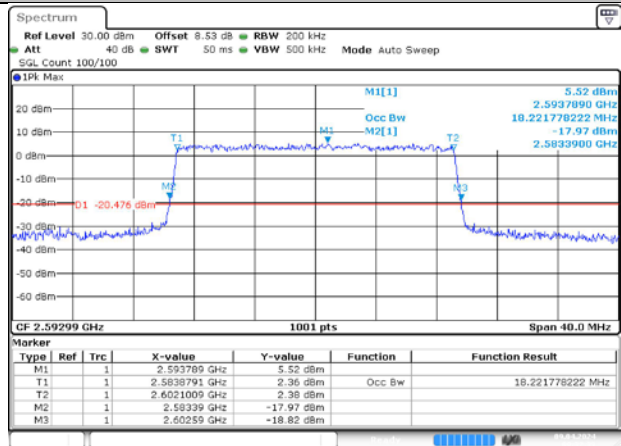
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MCH_CP-QPSK / Outer_Full



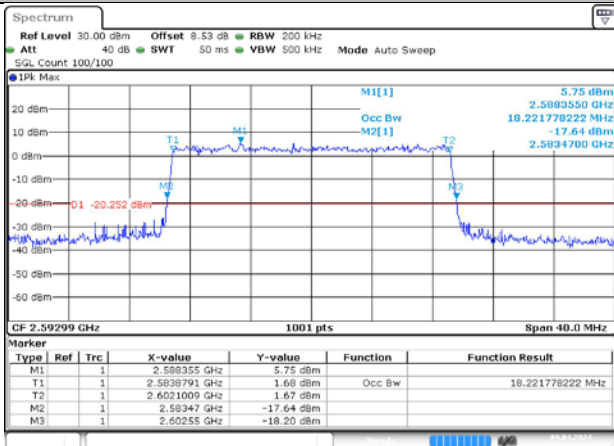
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MCH_CP-16QAM / Outer_Full



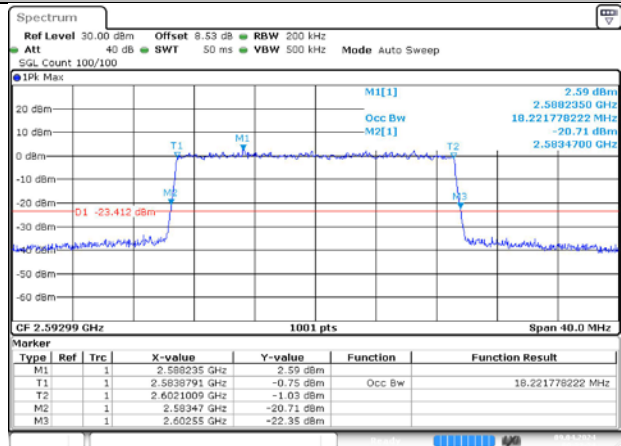
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MCH_CP-64QAM / Outer_Full

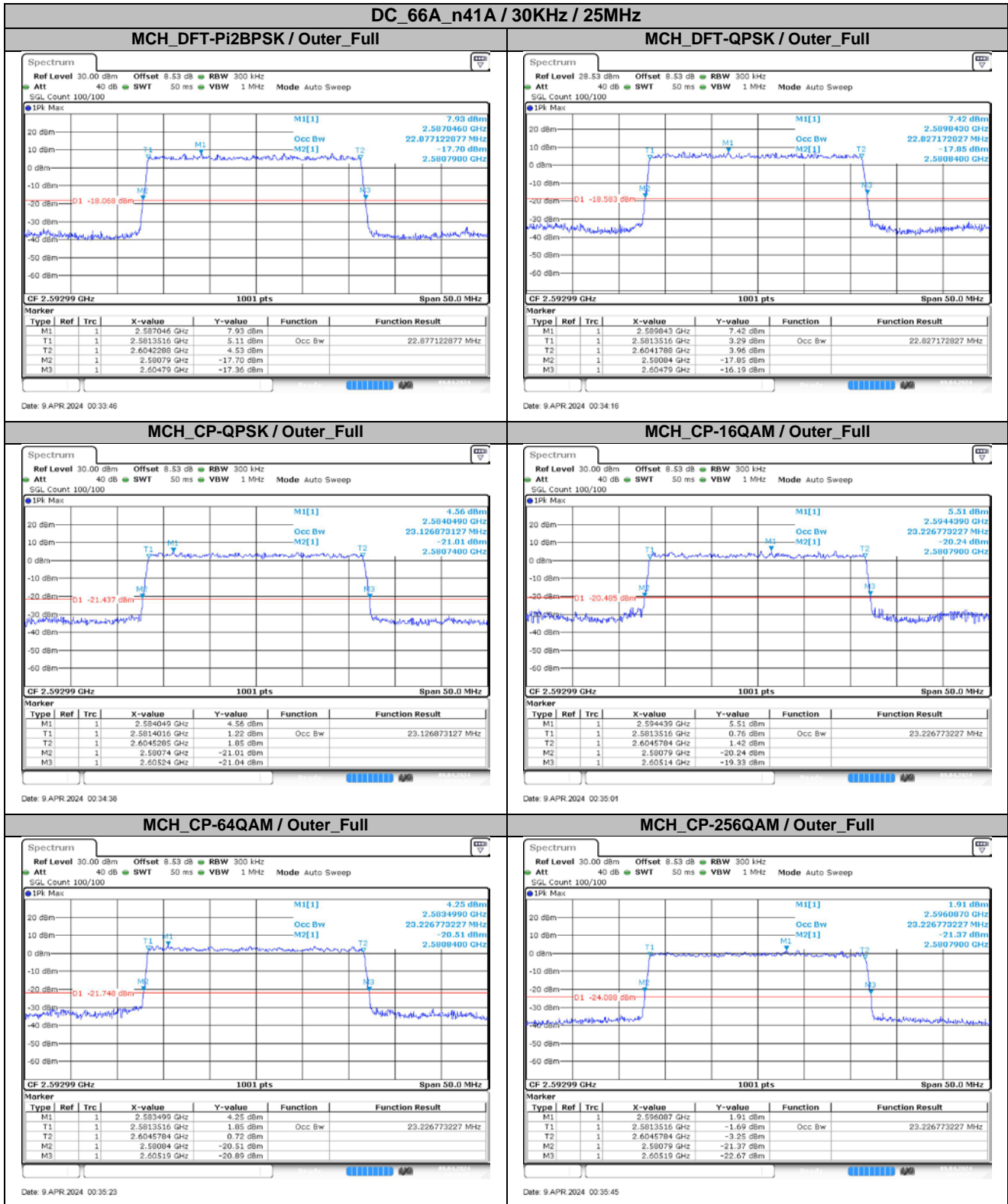


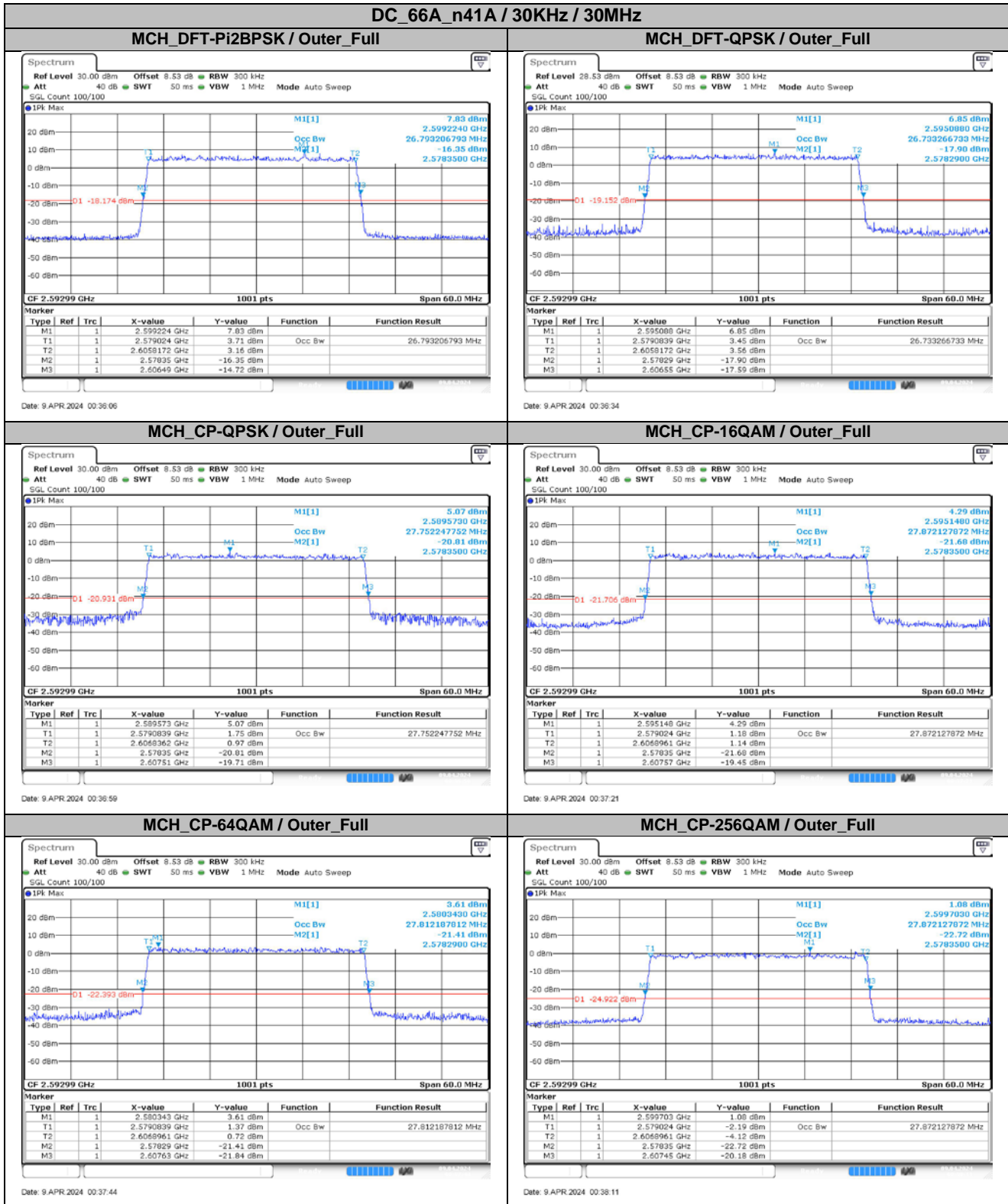
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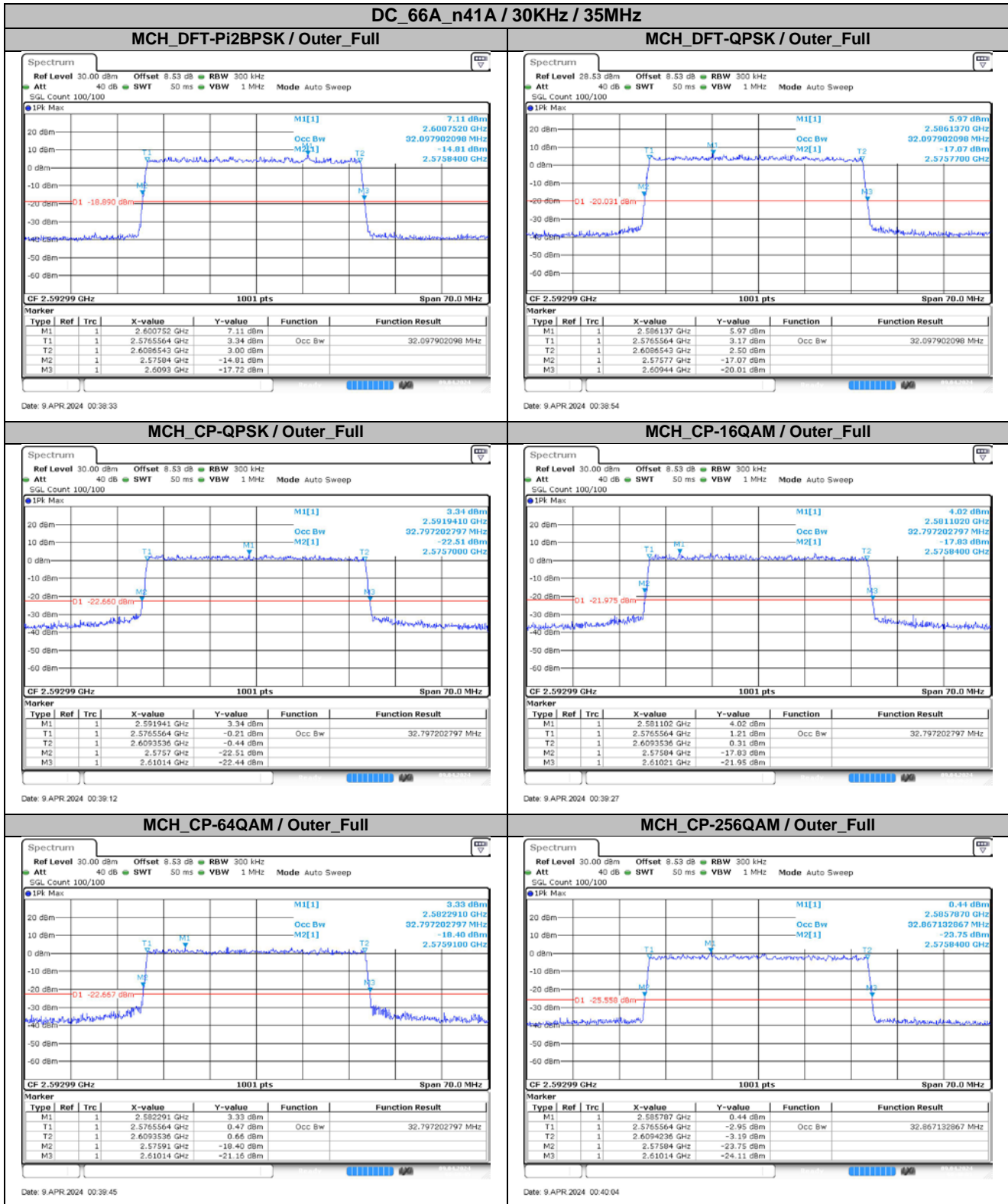
MCH_CP-256QAM / Outer_Full

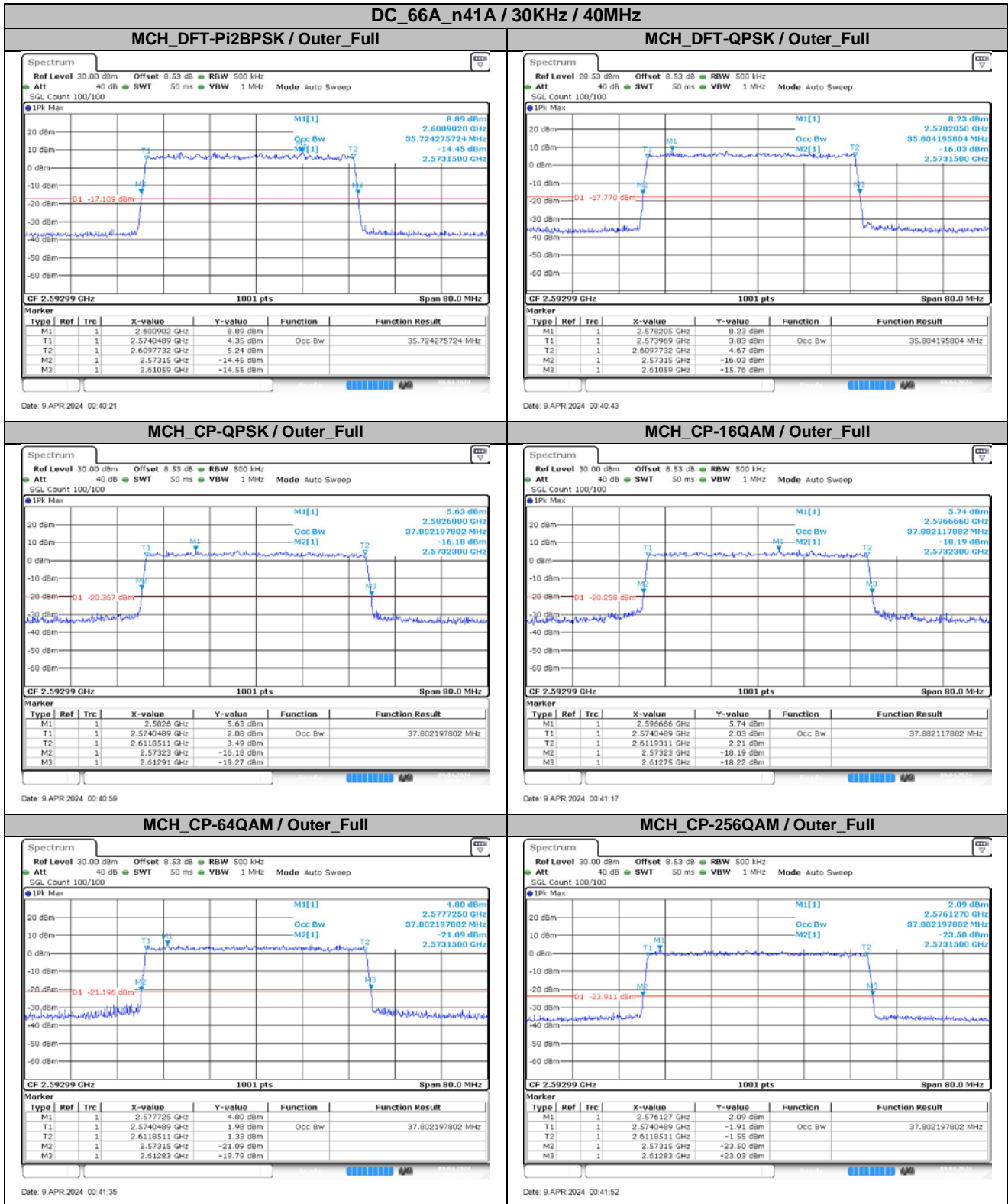


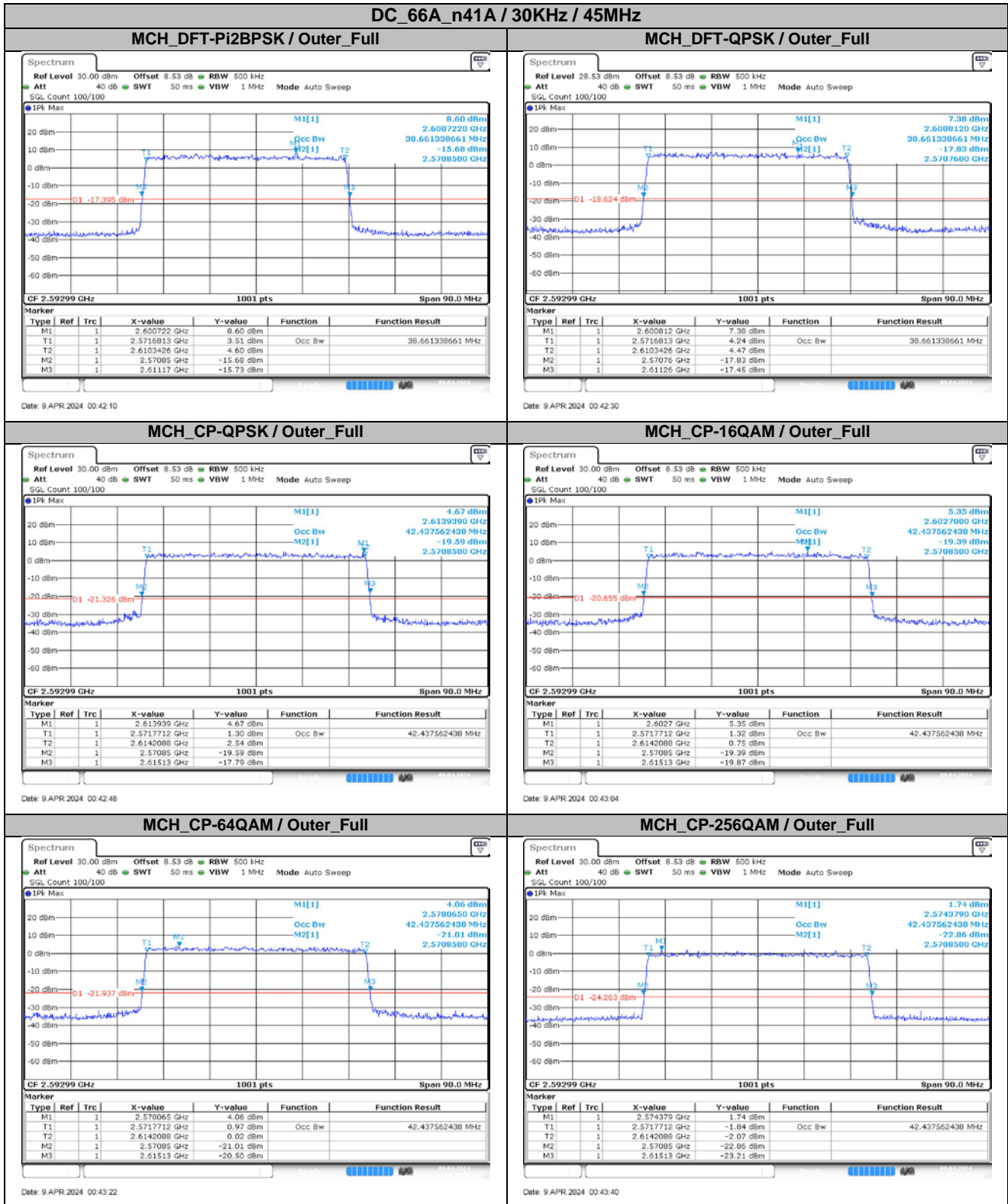
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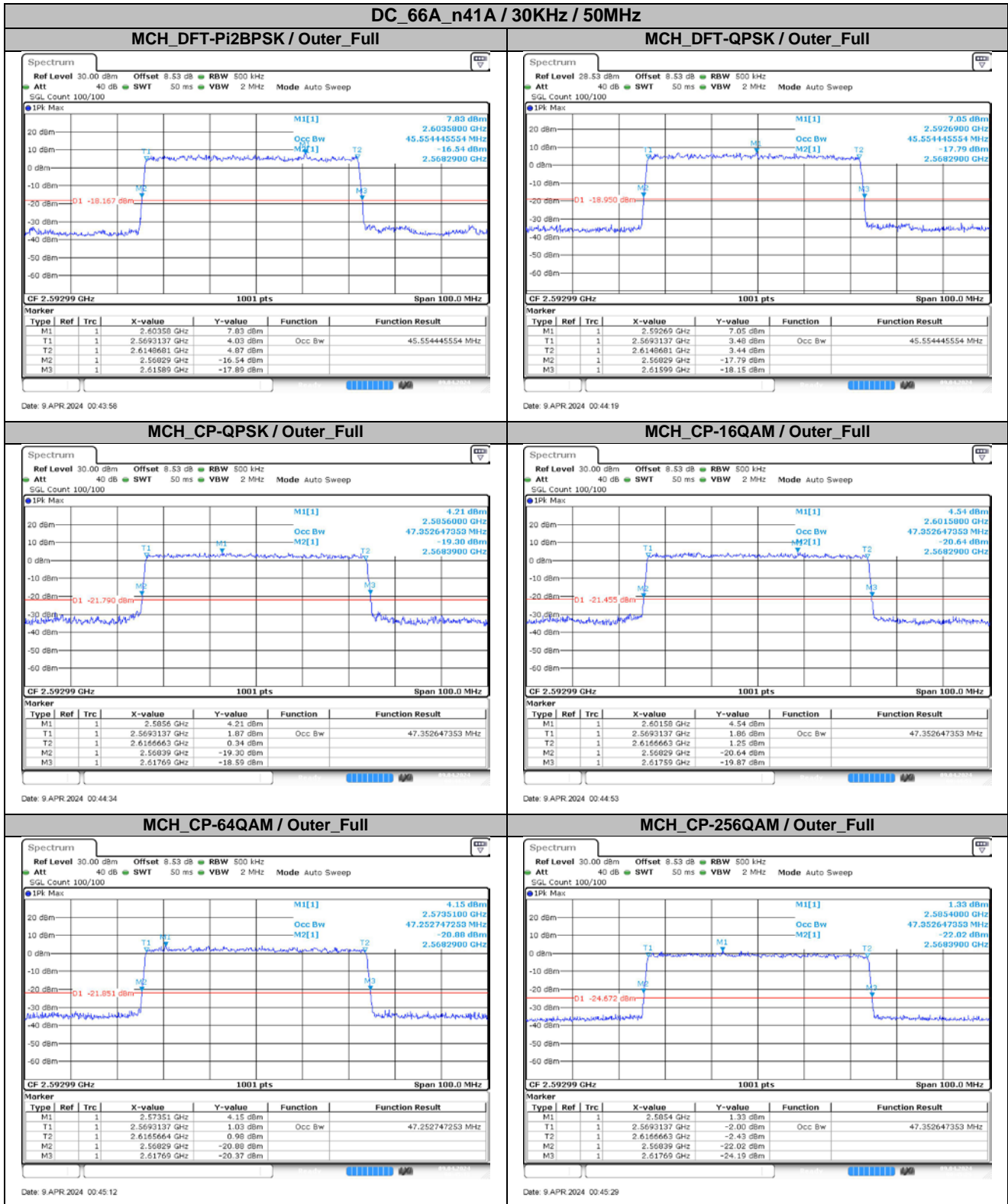


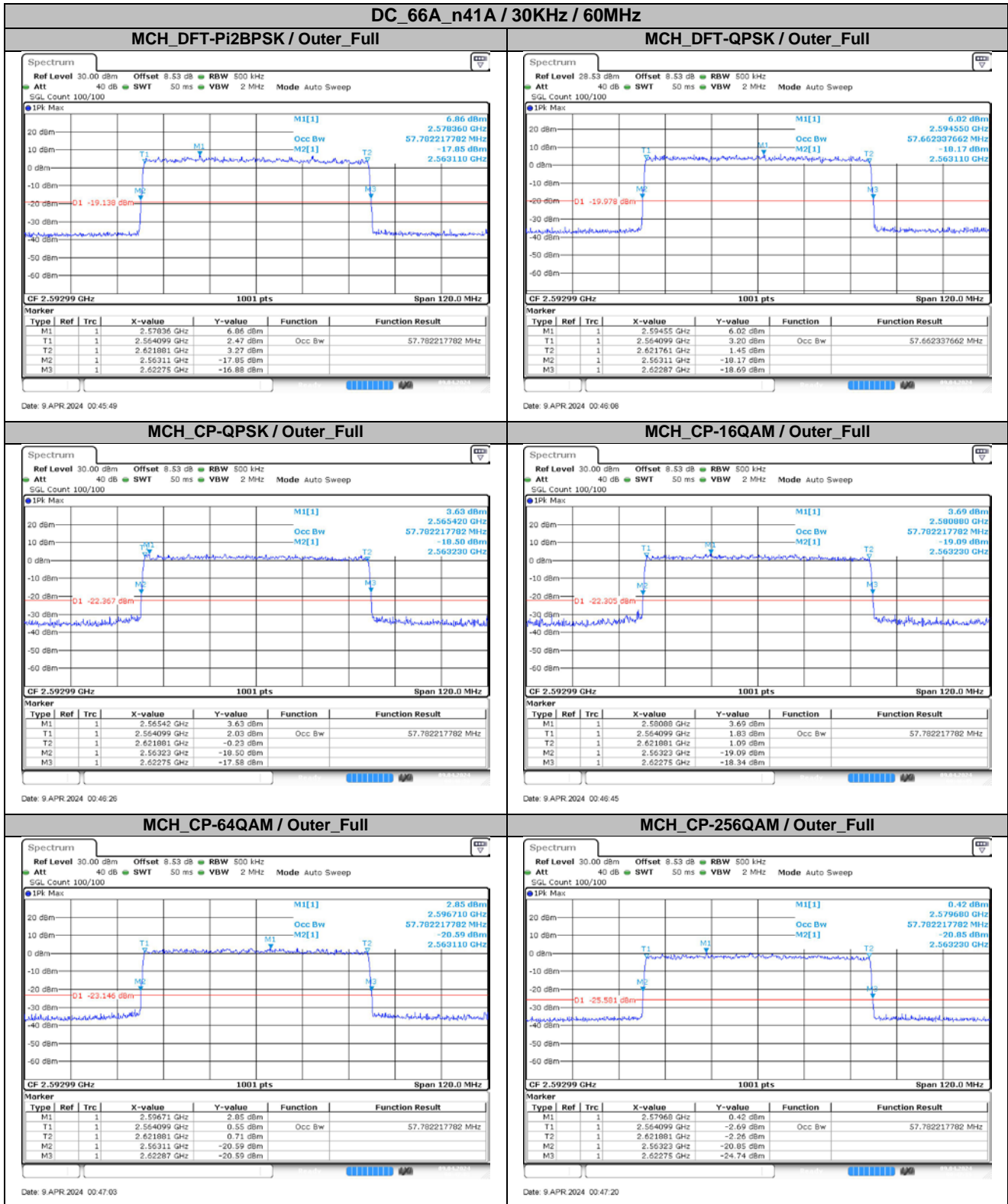


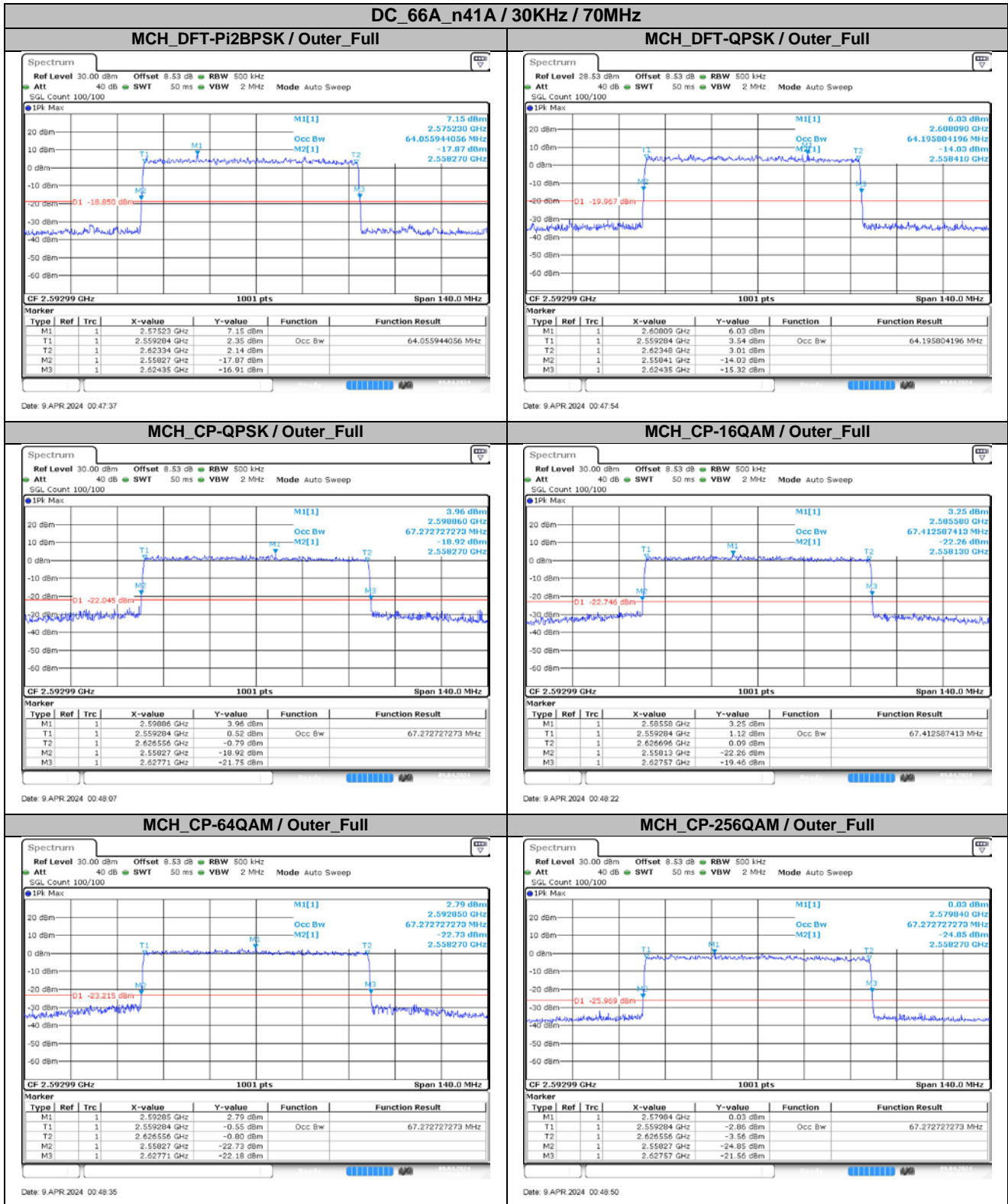


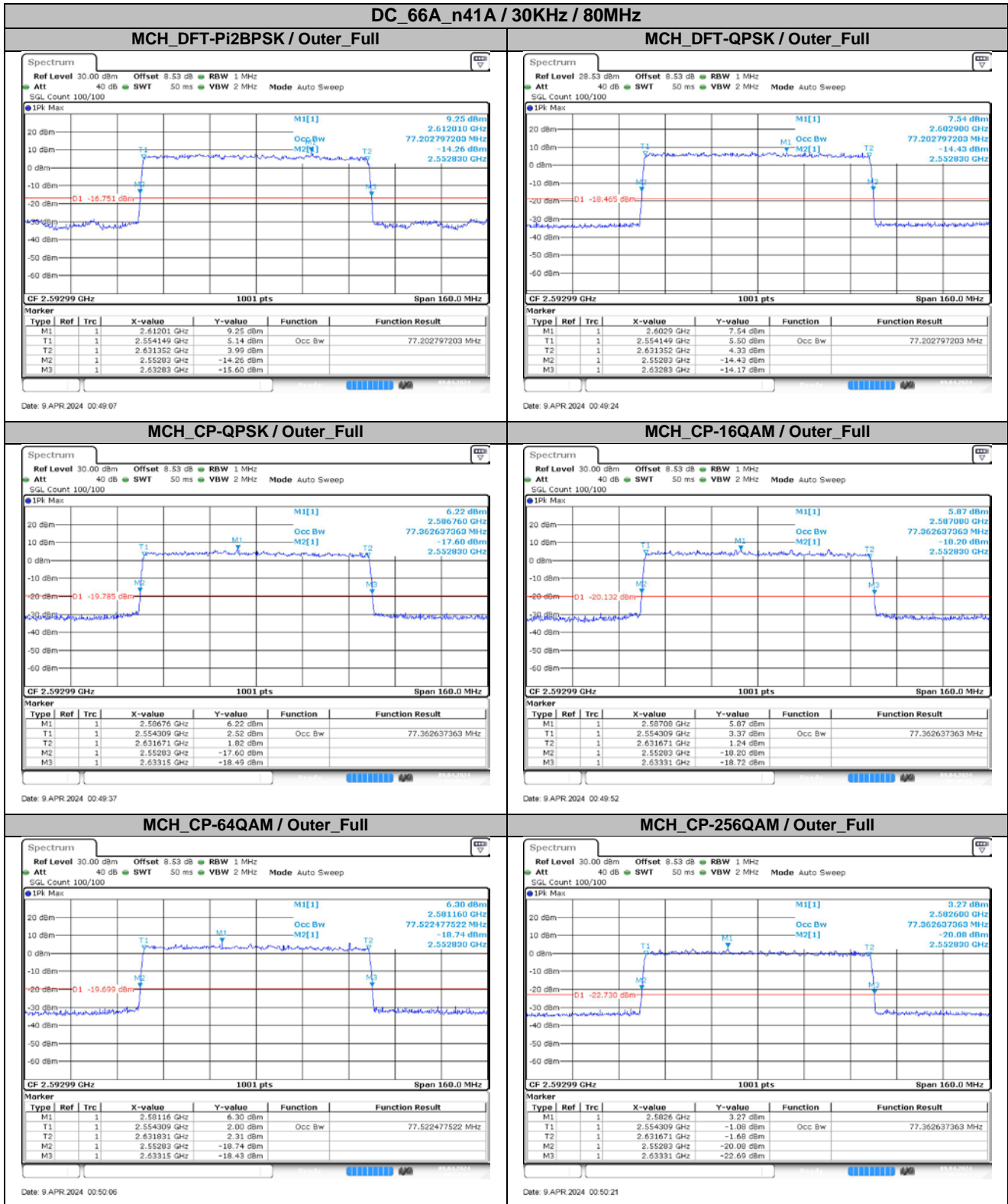


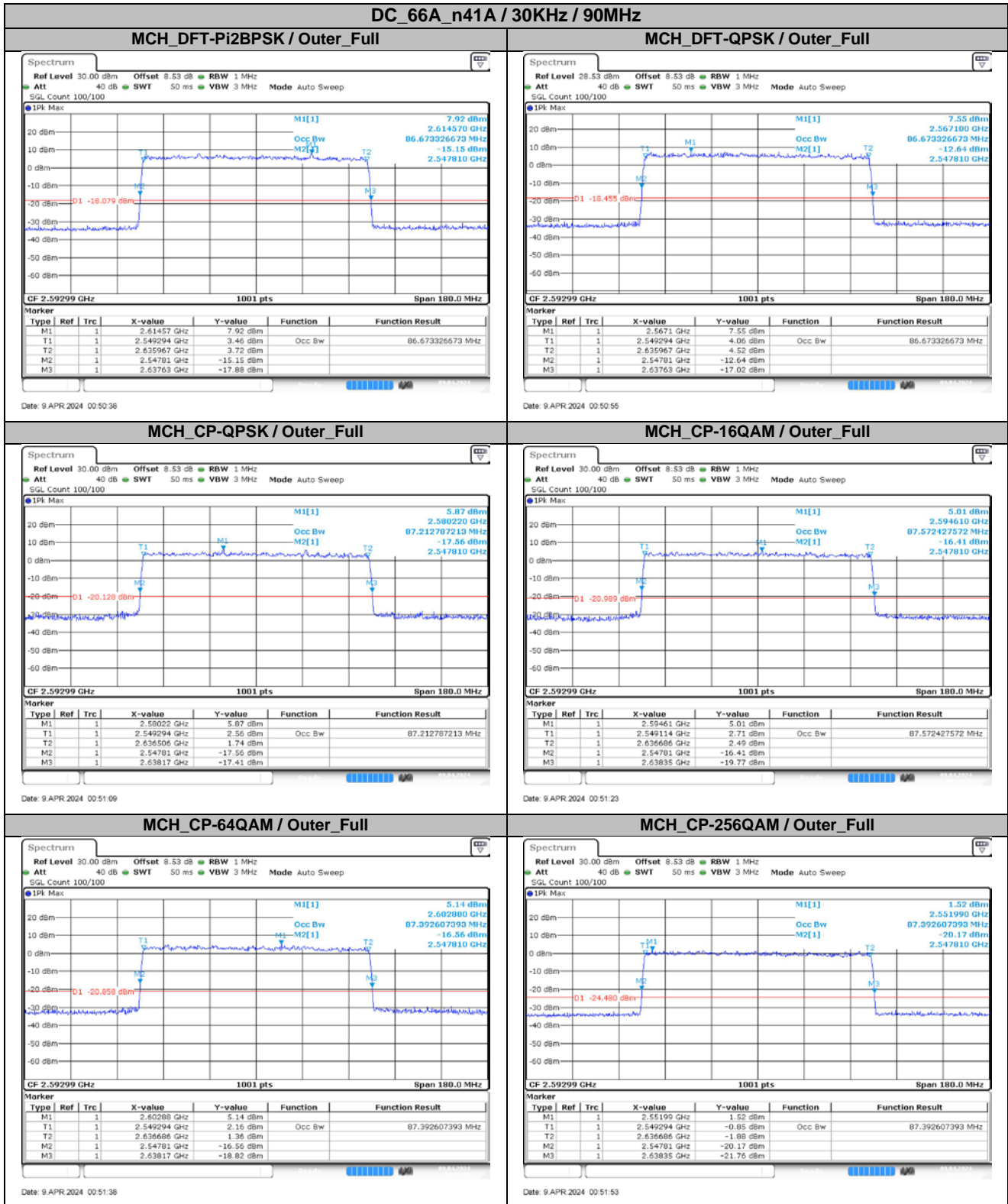






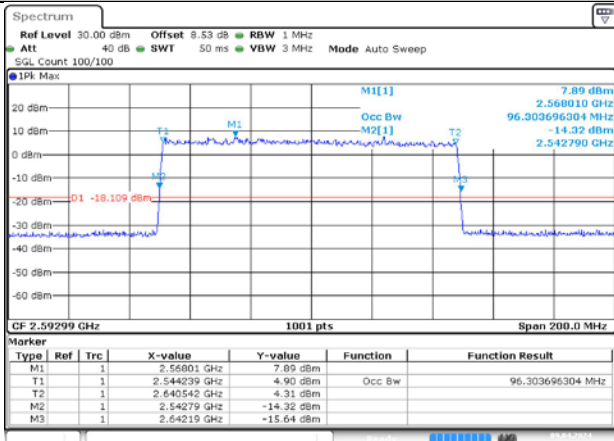




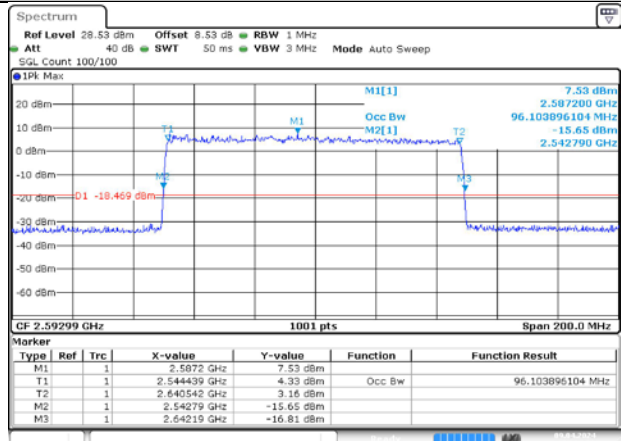


DC_66A_n41A / 30KHz / 100MHz

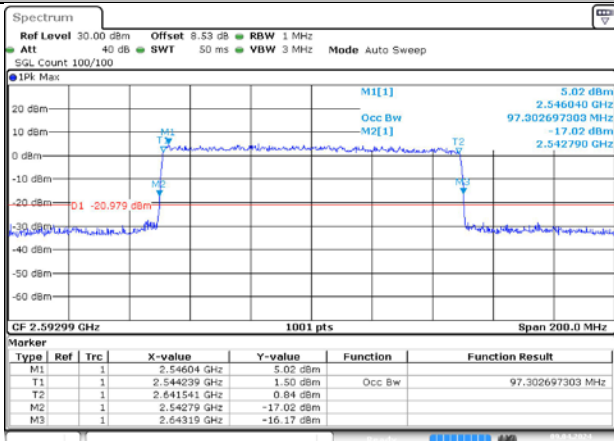
MCH_DFT-Pi2BPSK / Outer_Full



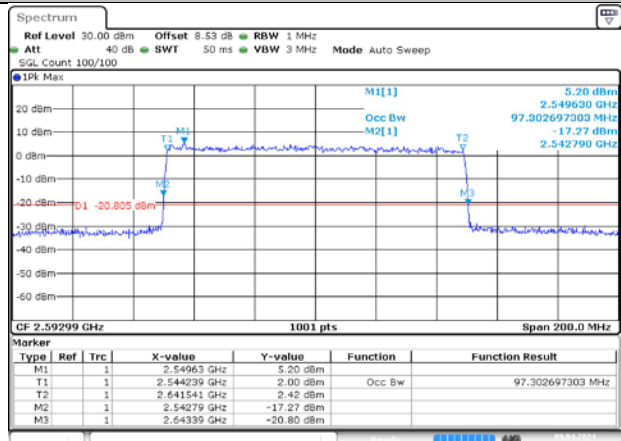
MCH_DFT-QPSK / Outer_Full



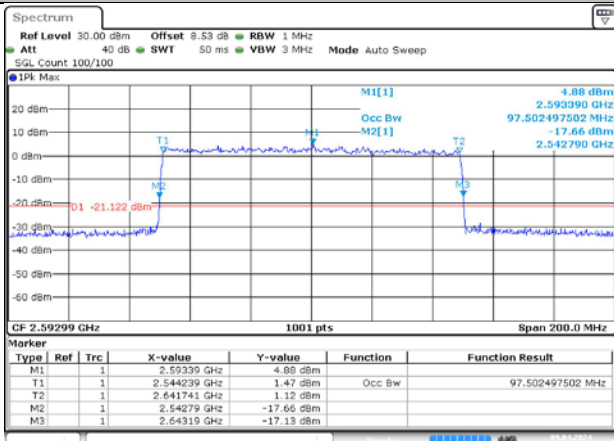
MCH_CP-QPSK / Outer_Full



MCH_CP-16QAM / Outer_Full



MCH_CP-64QAM / Outer_Full



MCH_CP-256QAM / Outer_Full

