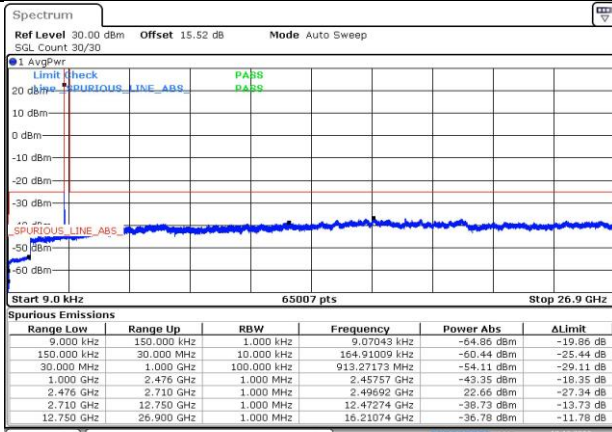


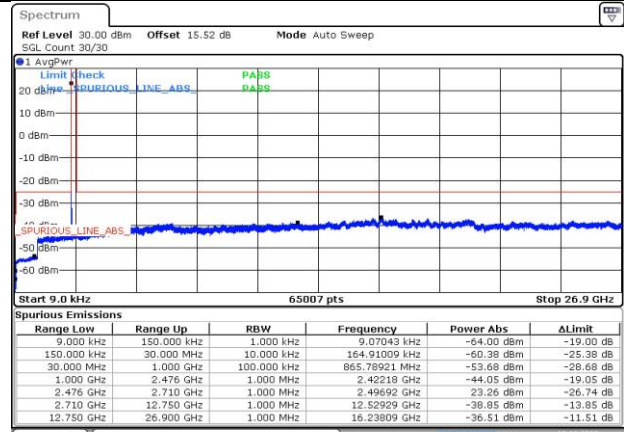
n41A / 30KHz / 20MHz

LCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



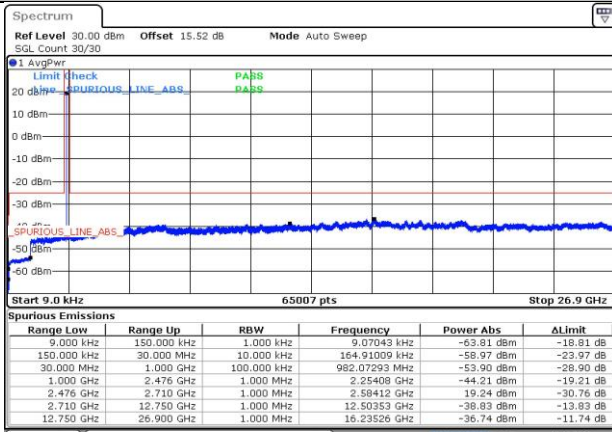
Date: 19 DEC 2023 03:16:48

LCH / DFT-QPSK\_Edge\_1RB\_Left



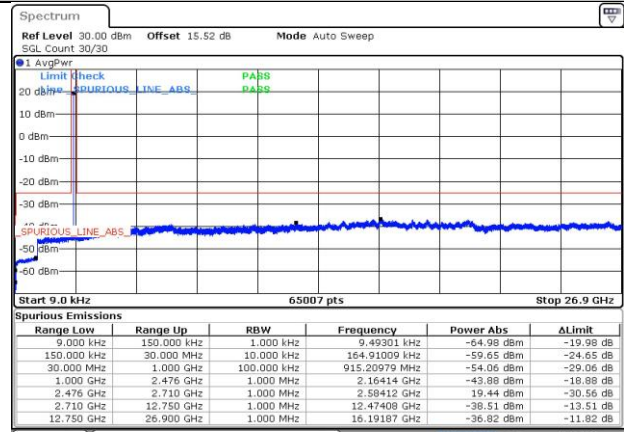
Date: 19 DEC 2023 03:17:08

MCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



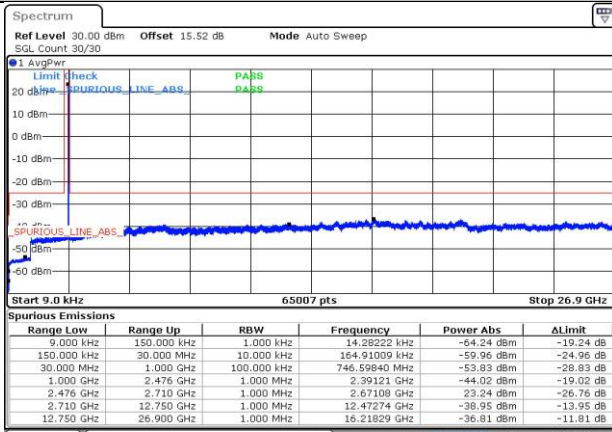
Date: 19 DEC 2023 03:17:39

MCH / DFT-QPSK\_Edge\_1RB\_Left



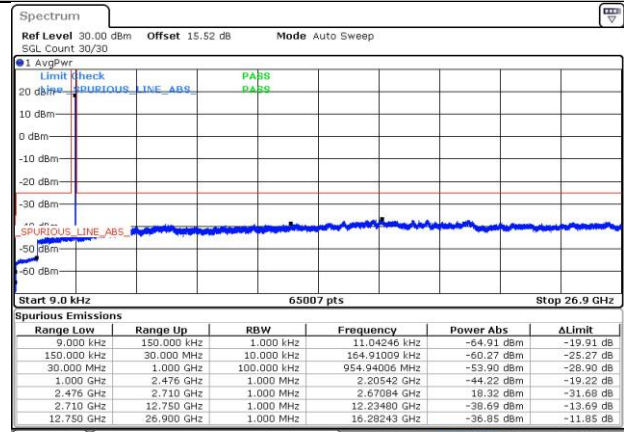
Date: 19 DEC 2023 03:18:07

HCH / DFT-Pi2BPSK\_Edge\_1RB\_Left

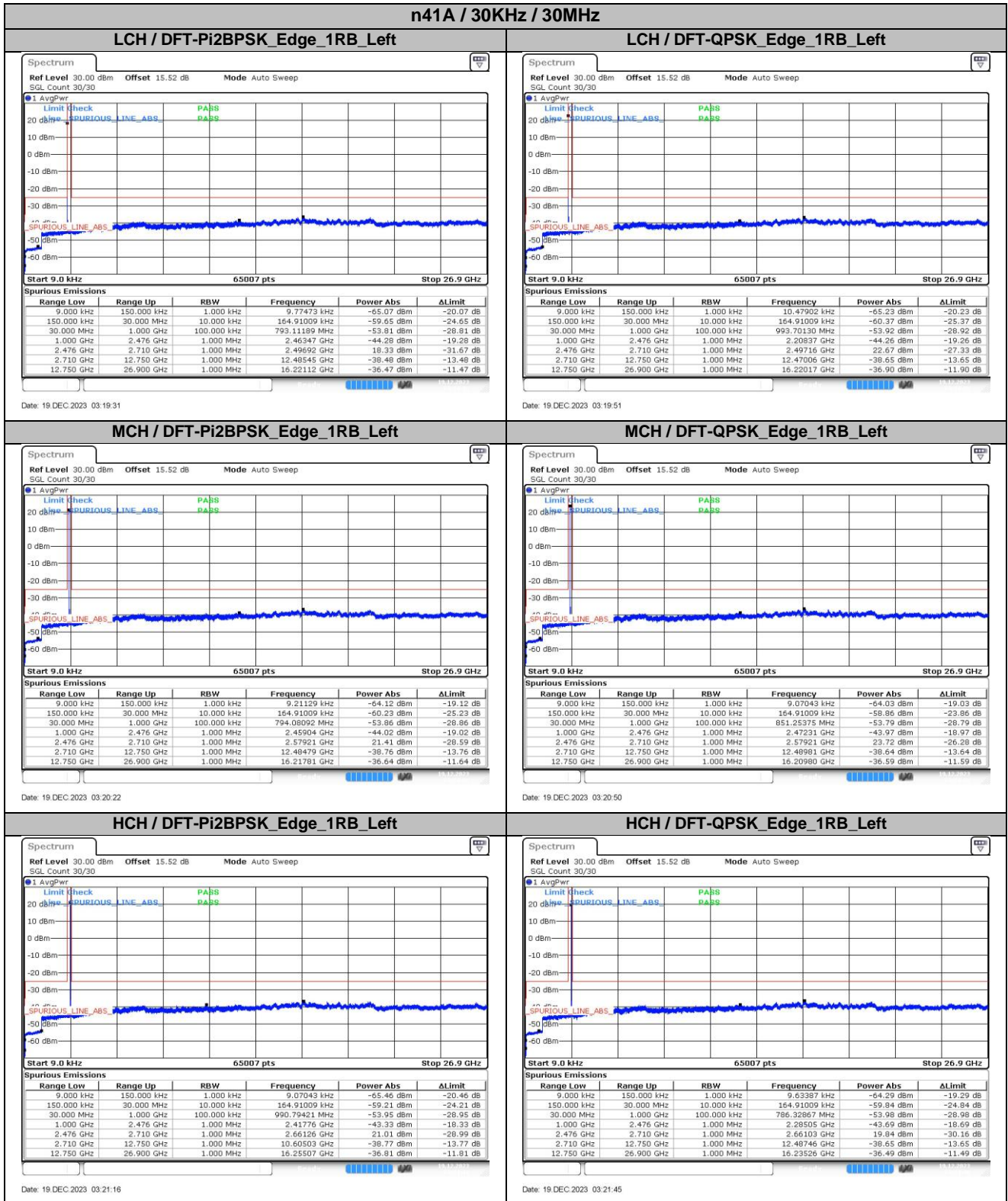


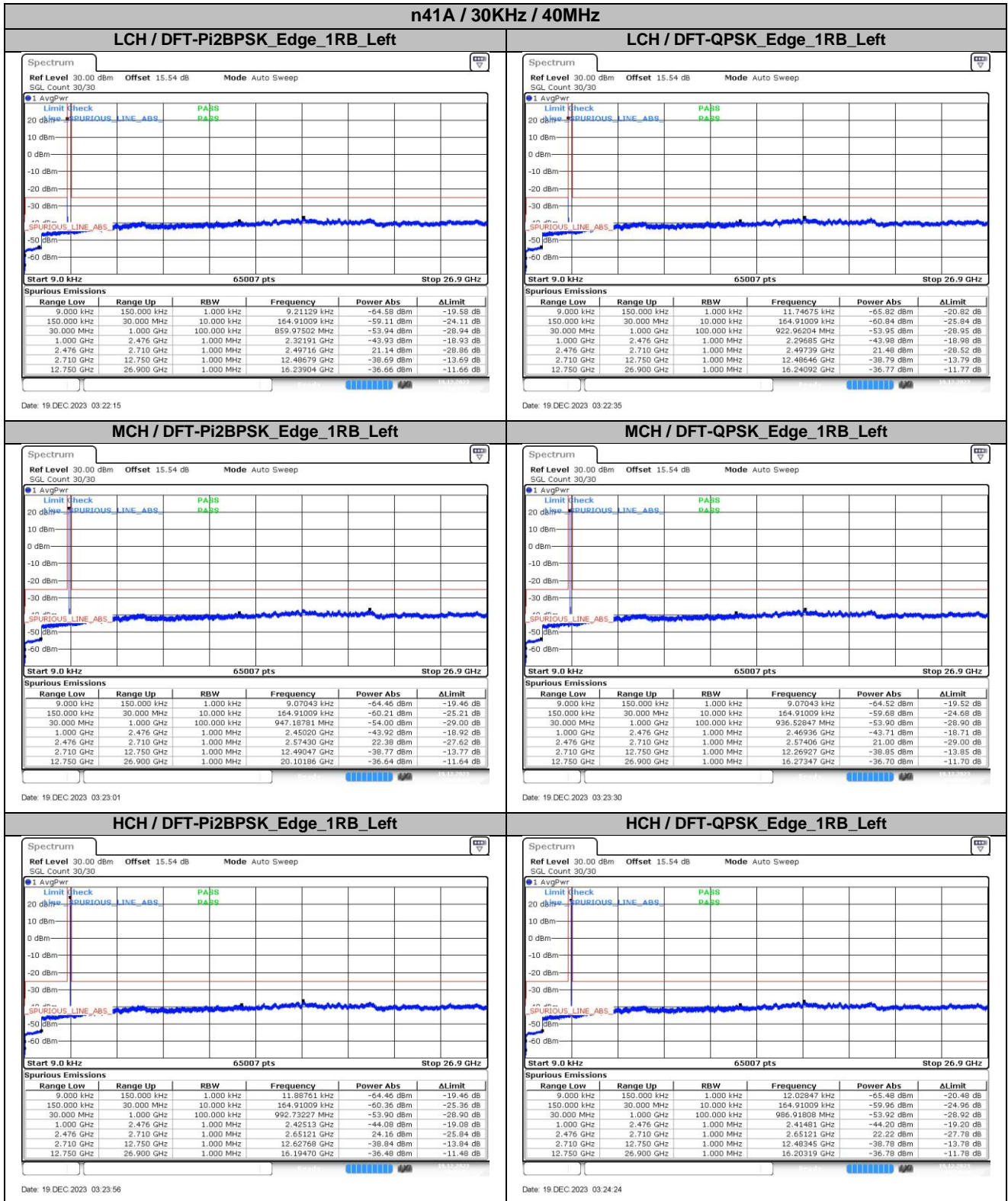
Date: 19 DEC 2023 03:18:33

HCH / DFT-QPSK\_Edge\_1RB\_Left



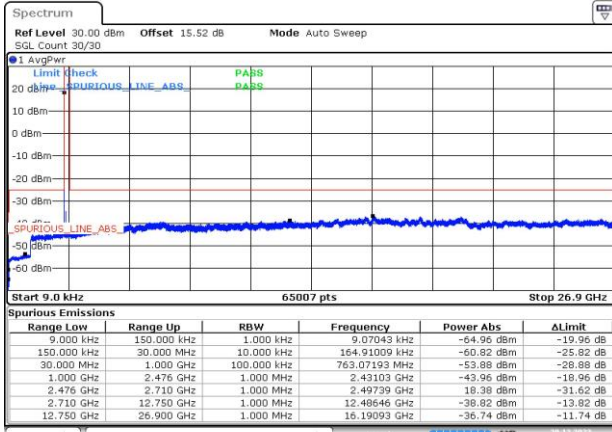
Date: 19 DEC 2023 03:19:01





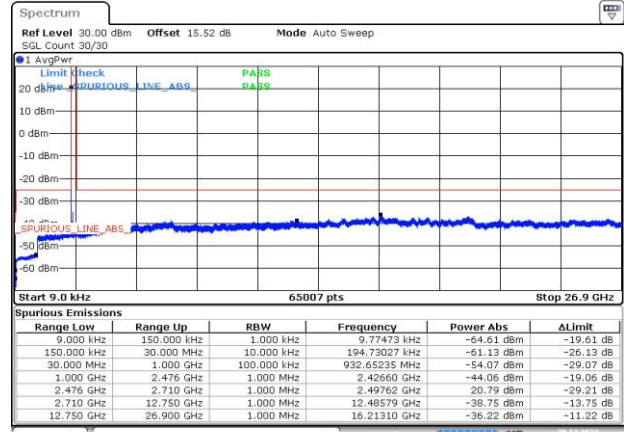
n41A / 30KHz / 50MHz

LCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



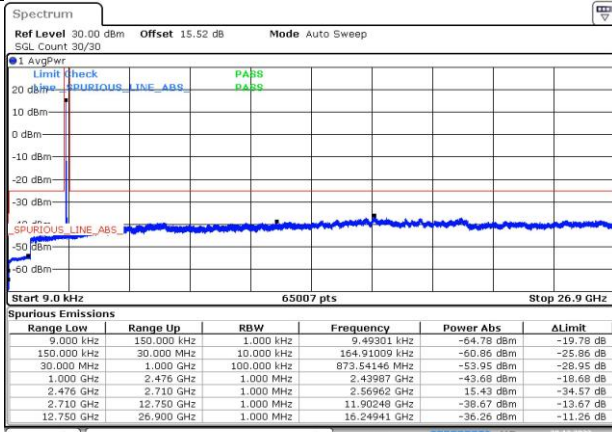
Date: 29 DEC 2023 09:18:29

LCH / DFT-QPSK\_Edge\_1RB\_Left



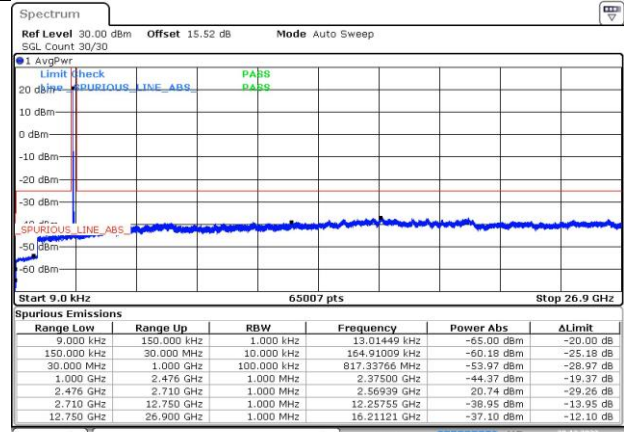
Date: 29 DEC 2023 09:18:58

MCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



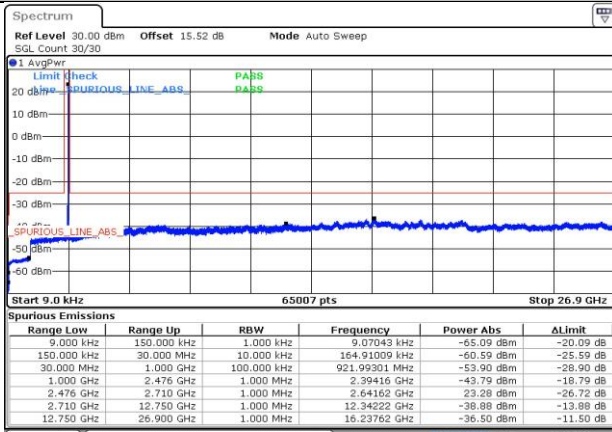
Date: 29 DEC 2023 09:19:59

MCH / DFT-QPSK\_Edge\_1RB\_Left



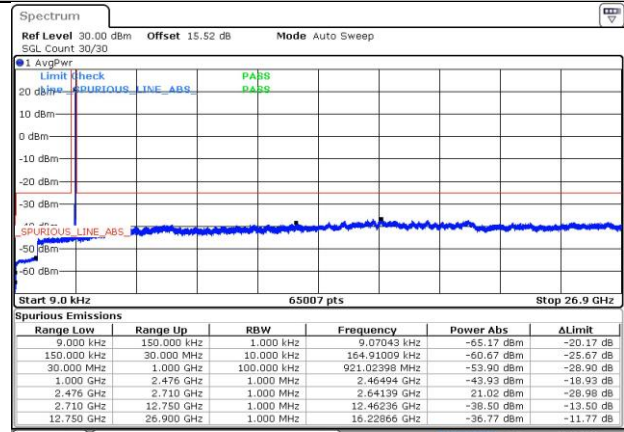
Date: 29 DEC 2023 09:20:27

HCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



Date: 29 DEC 2023 09:20:56

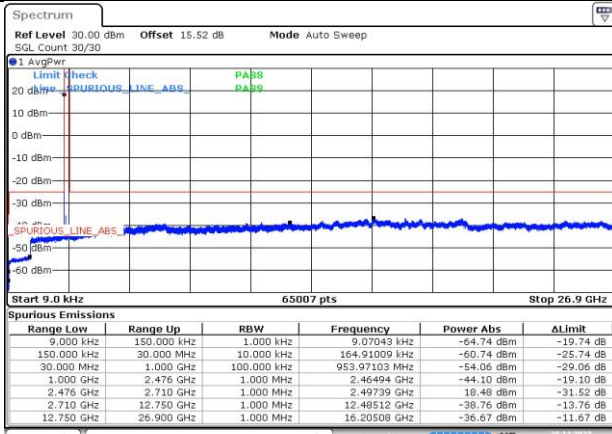
HCH / DFT-QPSK\_Edge\_1RB\_Left



Date: 29 DEC 2023 09:21:24

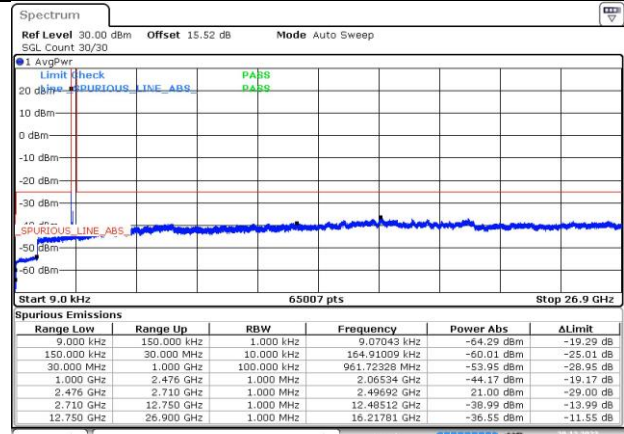
n41A / 30KHz / 60MHz

LCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



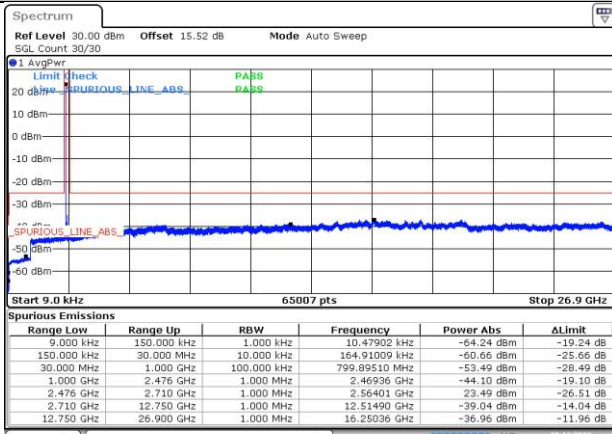
Date: 29 DEC 2023 11:48:31

LCH / DFT-QPSK\_Edge\_1RB\_Left



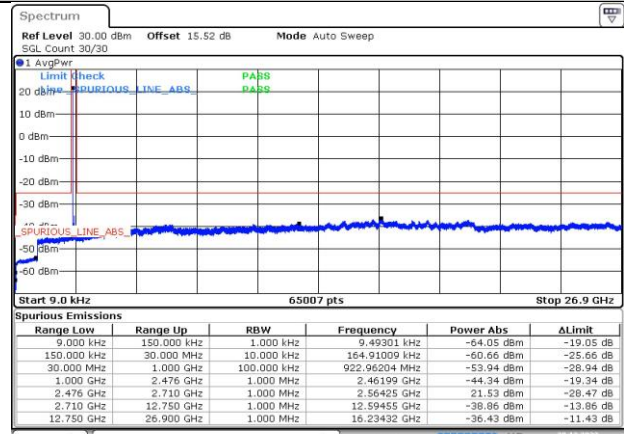
Date: 29 DEC 2023 11:49:00

MCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



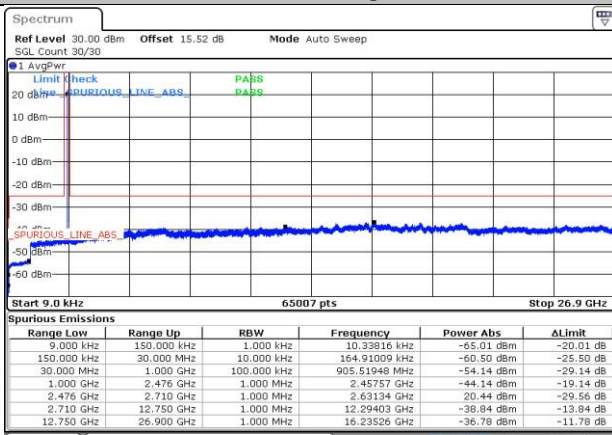
Date: 29 DEC 2023 11:49:24

MCH / DFT-QPSK\_Edge\_1RB\_Left



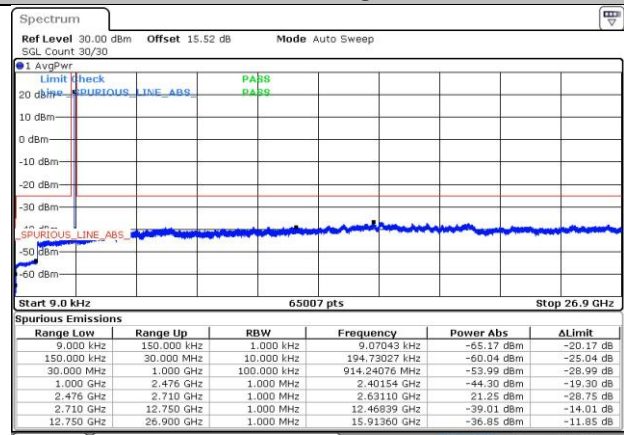
Date: 29 DEC 2023 11:49:53

HCH / DFT-Pi2BPSK\_Edge\_1RB\_Left

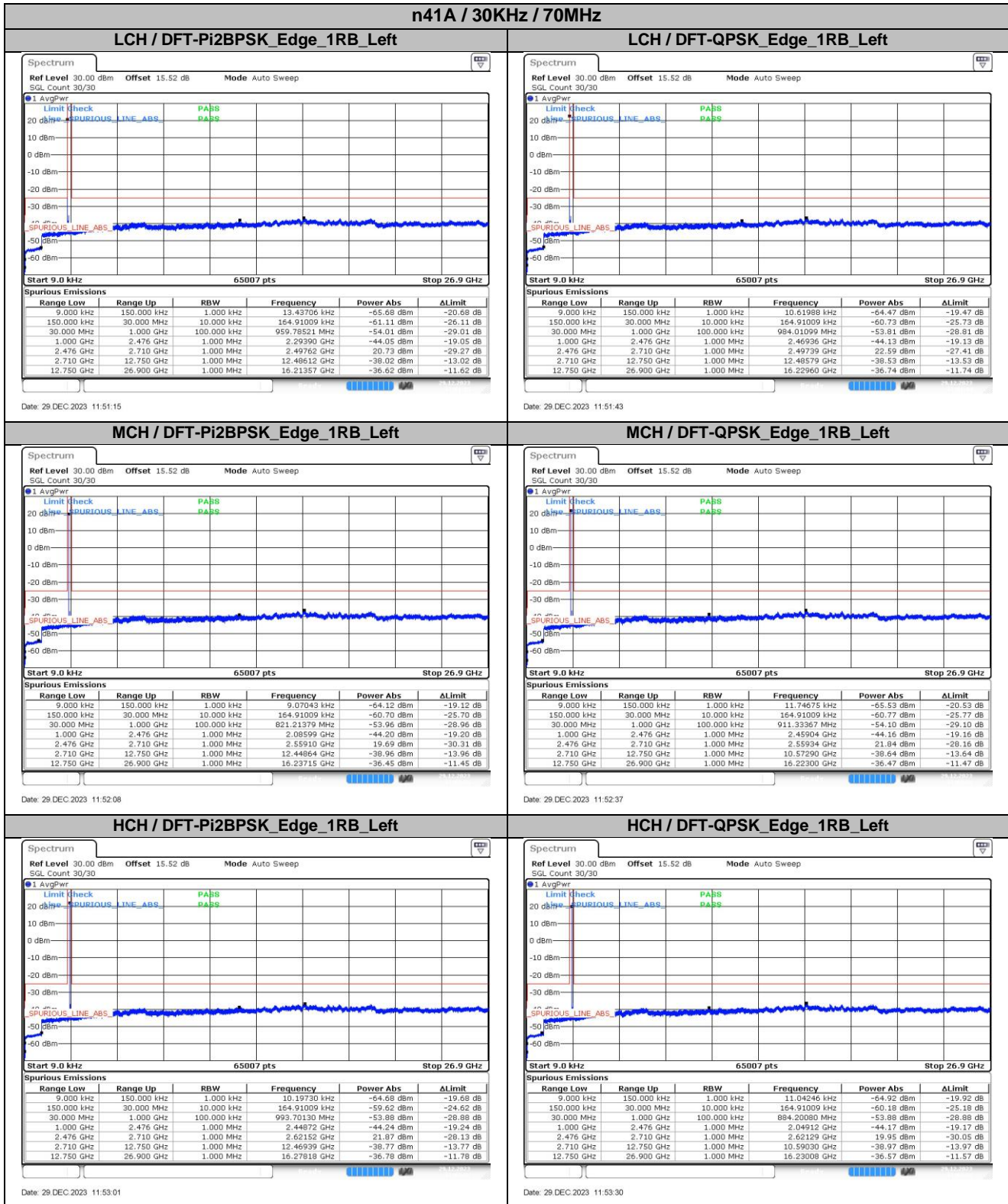


Date: 29 DEC 2023 11:50:18

HCH / DFT-QPSK\_Edge\_1RB\_Left

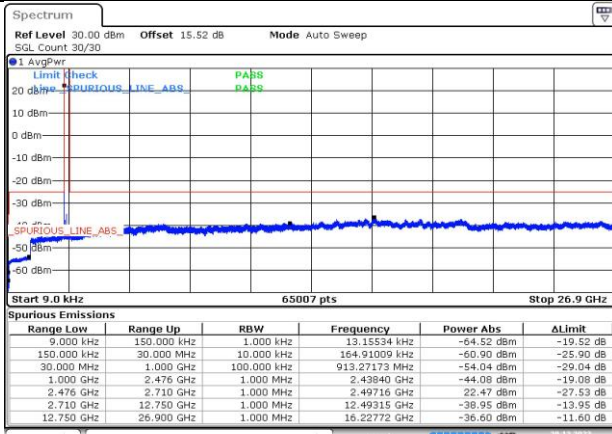


Date: 29 DEC 2023 11:50:46



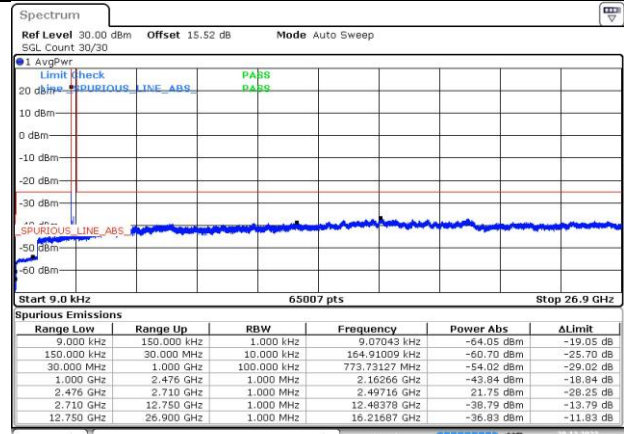
n41A / 30KHz / 80MHz

LCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



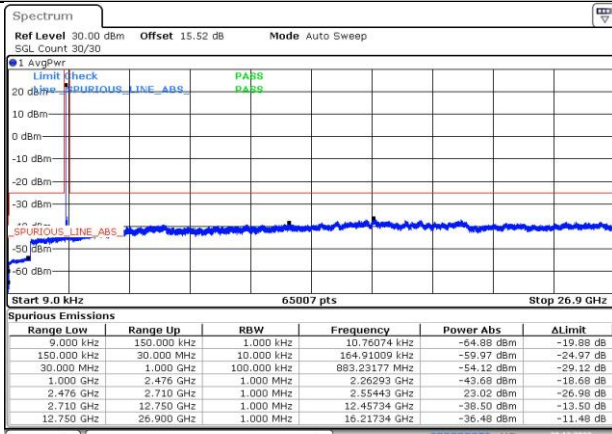
Date: 29 DEC 2023 11:53:59

LCH / DFT-QPSK\_Edge\_1RB\_Left



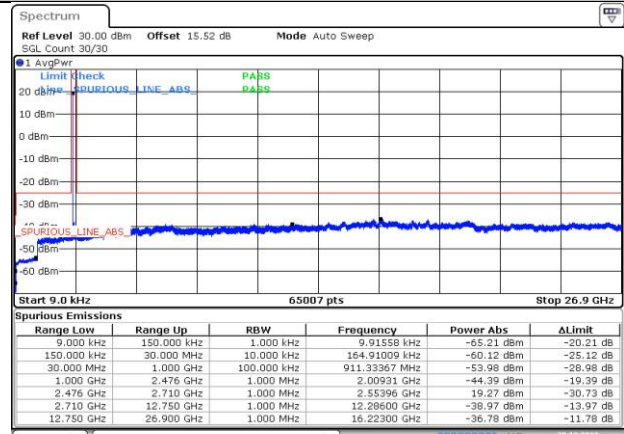
Date: 29 DEC 2023 11:54:28

MCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



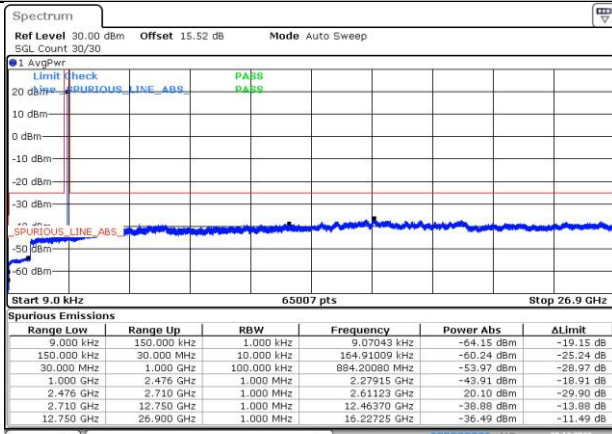
Date: 29 DEC 2023 11:55:13

MCH / DFT-QPSK\_Edge\_1RB\_Left



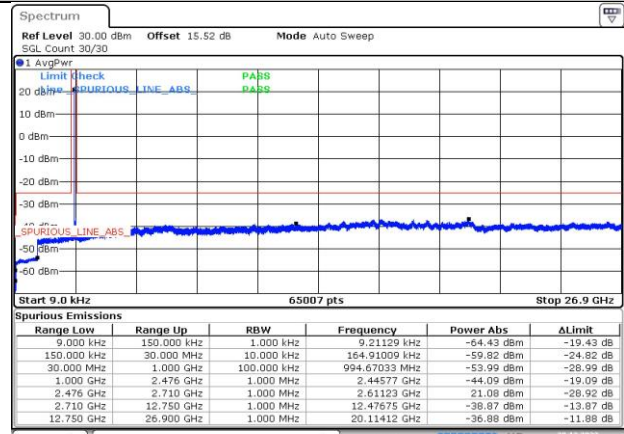
Date: 29 DEC 2023 11:55:42

HCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



Date: 29 DEC 2023 11:56:19

HCH / DFT-QPSK\_Edge\_1RB\_Left

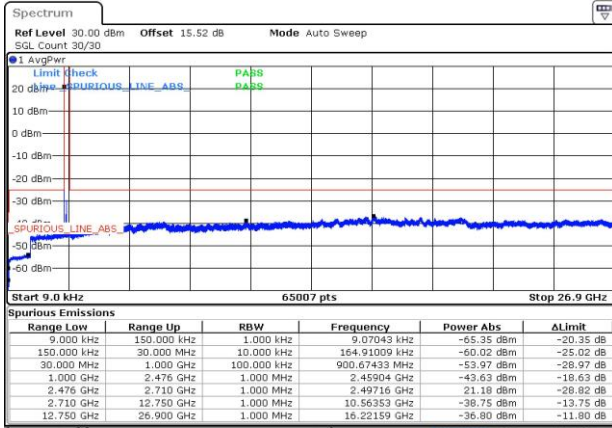


Date: 29 DEC 2023 11:56:48



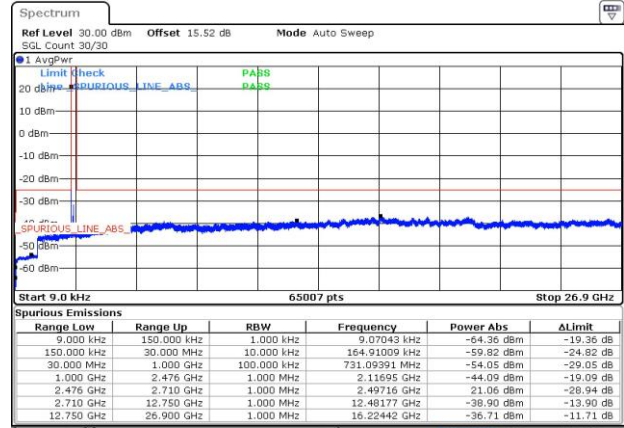
n41A / 30KHz / 90MHz

LCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



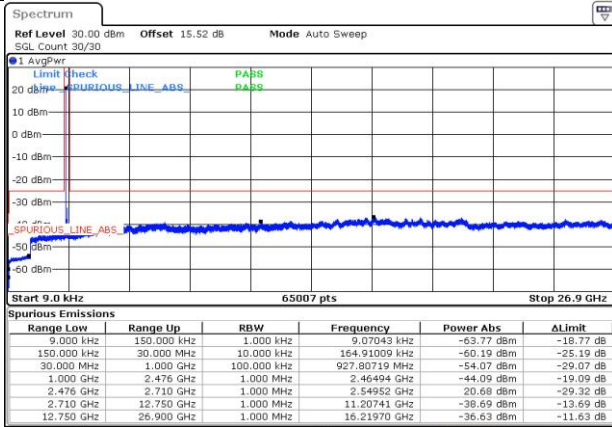
Date: 29 DEC 2023 11:57:17

LCH / DFT-QPSK\_Edge\_1RB\_Left



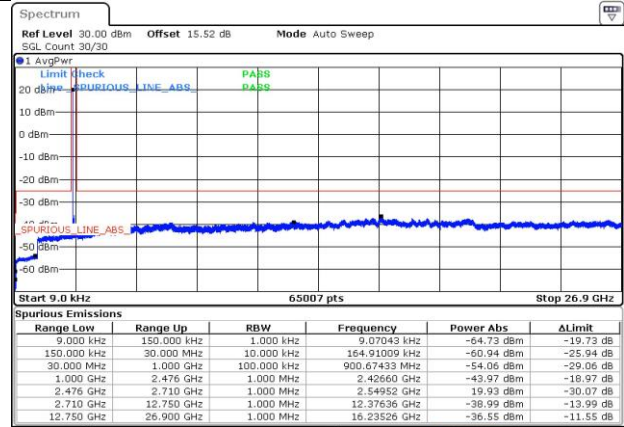
Date: 29 DEC 2023 11:57:46

MCH / DFT-Pi2BPSK\_Edge\_1RB\_Left



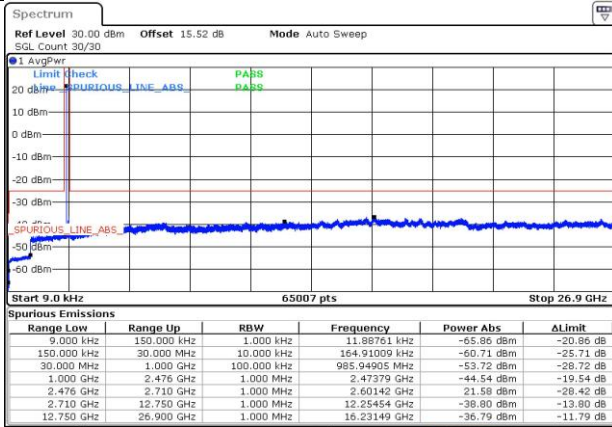
Date: 29 DEC 2023 11:58:31

MCH / DFT-QPSK\_Edge\_1RB\_Left



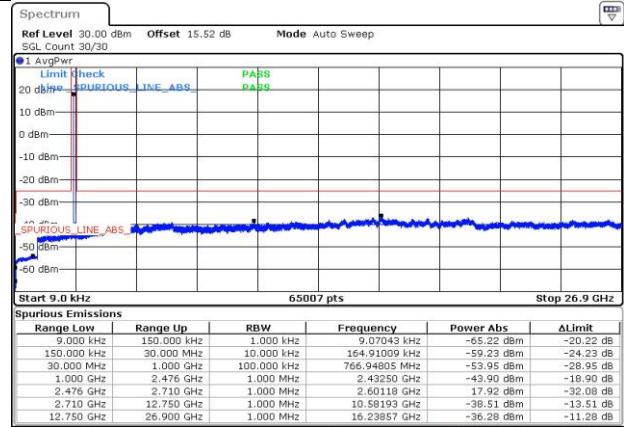
Date: 29 DEC 2023 11:59:00

HCH / DFT-Pi2BPSK\_Edge\_1RB\_Left

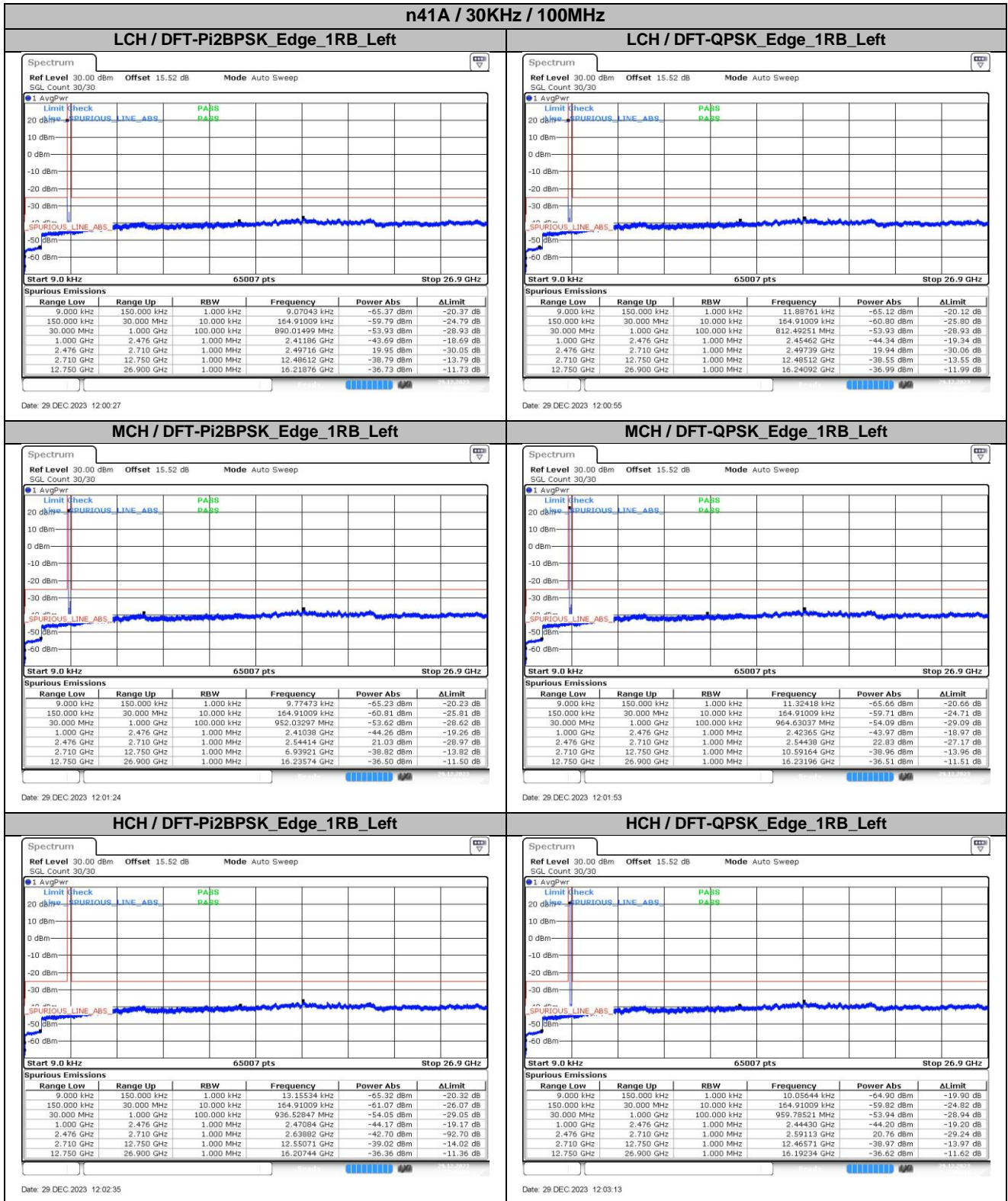


Date: 29 DEC 2023 11:59:29

HCH / DFT-QPSK\_Edge\_1RB\_Left



Date: 29 DEC 2023 11:59:58



## 7. Frequency Stability

### 7.1. Test Results

#### 7.1.1. Frequency Error Vs Voltage

SCS	Bandwidth	Channel	RB Config	Modulation	Temperature	Voltage	Deviation Result		Verdict
							(Hz)	(ppm)	
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	LV	-10.60	-0.004088	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	NV	-10.20	-0.003934	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	HV	-7.30	-0.002815	Pass

#### 7.1.2. Frequency Error Vs Temperature

SCS	Bandwidth	Channel	RB Config	Modulation	Temperature	Voltage	Deviation Result		Verdict
							(Hz)	(ppm)	
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-30°C	NV	-5.50	-0.002121	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-20°C	NV	-15.60	-0.006016	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-10°C	NV	-11.40	-0.004396	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	0°C	NV	-8.80	-0.003394	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	10°C	NV	-15.00	-0.005785	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	20°C	NV	-13.50	-0.005206	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	30°C	NV	-13.60	-0.005245	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	40°C	NV	-11.90	-0.004589	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	50°C	NV	0.40	0.000154	Pass

The End