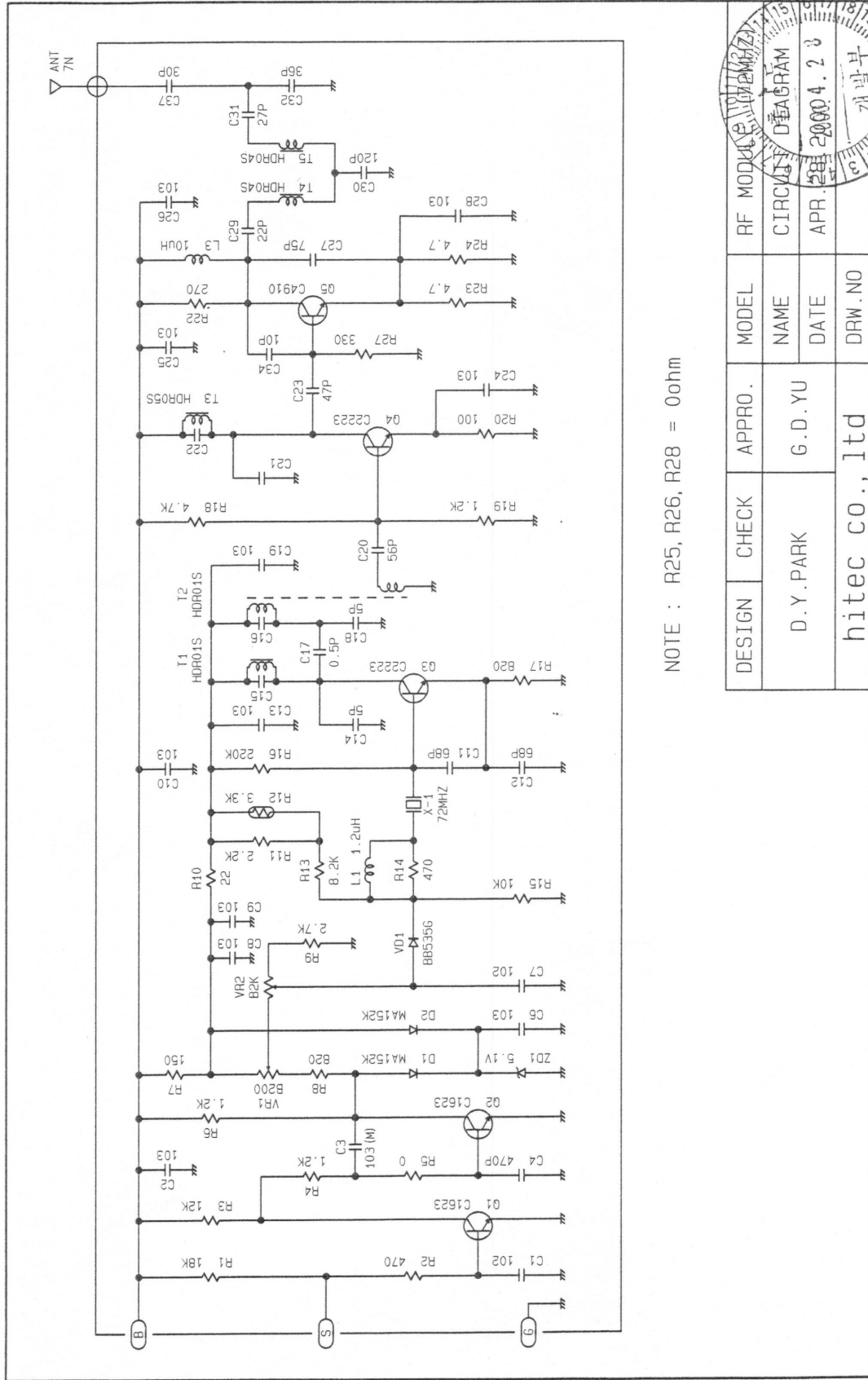




DESIGN	CHECK	APPRO.	MODEL	RF MODULE
D.Y.PARK		G.D.YU	NAME	CIRCUIT
			DATE	DIAGRAM
			DATE	APR 2000 4.23
			DRW.NO	hitec co., ltd