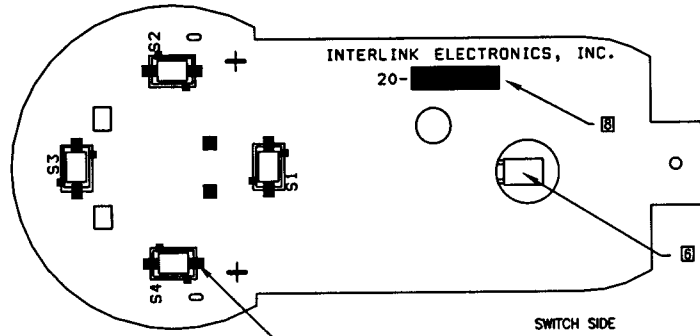
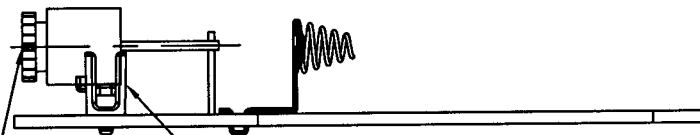


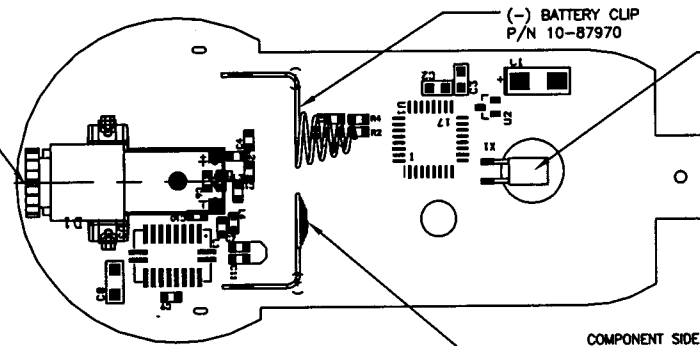
REVISION HISTORY			
REV	DESCRIPTION	DATE	APPROVED
P6	RELEASE DWG PER PCO	8/20/02	J.JANGCANAN



TACT SWITCHES (4X)
P/N 10-34323



LASER BRACE
P/N 10-42355



(-) BATTERY CLIP
P/N 10-87970

(+) BATTERY CLIP
P/N 10-78644

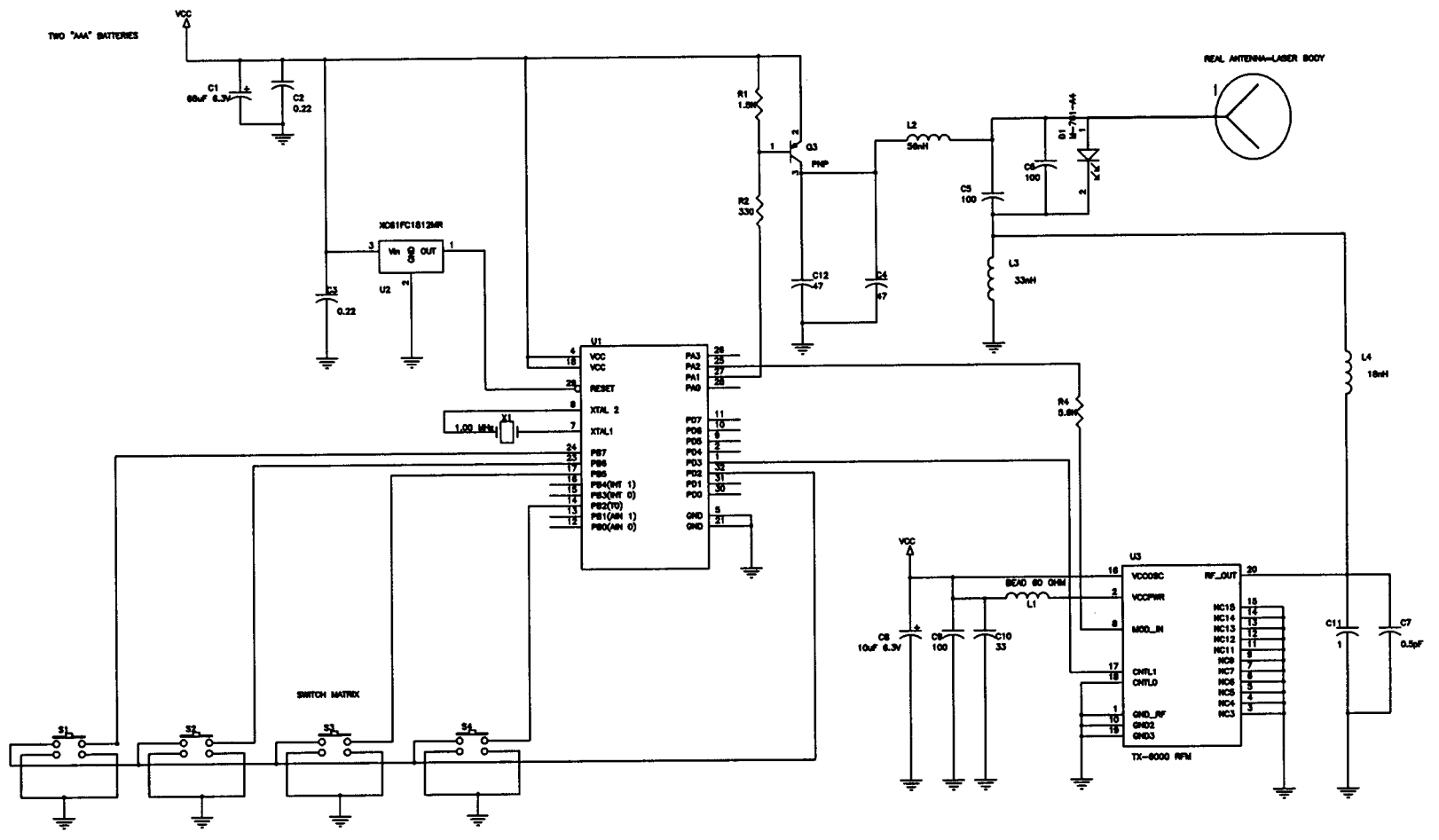
LASER
P/N 10-33333

- 8 THE REST OF THE ASSEMBLY PART NUMBER AND REVISION LETTER SHOULD BE STAMPED WITH INDELIBLE INK IN THE SILKSCREENED BLOCK NEXT TO THE 20-. AFTER STAMPING, THE PART NUMBER AND REVISION SHOULD MATCH THIS DOCUMENT.
- 7 INSTALL LASER BRACE ON COMPONENT SIDE OF PCB AS SHOWN. INSTALL LASER ASSY ON TOP OF BRACE, SOLDERING 2 LEADS TO LASER MOUNTING HOLES. LASER ASSY SHOULD BE FULLY SEATED ON BRACE. TRIM EXCESS LEADS FLUSH TO THE PCB.
- 6 MOUNT RESONATOR ON COMPONENT SIDE SO THAT IT LAYS PARALLEL TO THE PCB AND IS ORIENTED AS SHOWN.
- 5 THIS ASSEMBLY CONTAINS ESD SENSITIVE COMPONENTS. THEREFORE, ESD PRECAUTIONARY MEASURES SHOULD BE TAKEN IN PRODUCTION, TEST, AND HANDLING OF THIS ASSY.
4. ASSEMBLY MUST MEET ANSI/ IPC-A-610C OR BETTER.
- 3 VERIFY POLARITY OF FOLLOWING COMPONENTS:
C1,C8,D1,Q3, U1-U3
2. REMOVE ALL BURRS FROM THE EDGE OF THE PCB AFTER ASSEMBLY AND BREAK AWAY (FROM ARRAY). ALL EDGES SHOULD BE SMOOTH.
1. SEE SEPARATE BILL OF MATERIALS.

NOTES : UNLESS OTHERWISE SPECIFIED.

SCALE, DIMENSIONS, UNITS: DIMENSIONS ARE IN INCHES. TOLERANCES ARE: FRACTIONS DECIMALS ANGLES .XX 0.01 .005		THIS MATERIAL CONTAINED IN THIS DOCUMENT IS UNCLASSIFIED EXCEPT WHERE SHOWN OTHERWISE AND SHOULD NOT BE COPIED OR REPRODUCED IN WHOLE OR IN PART WITHOUT THE EXPRESS WRITTEN PERMISSION OF INTERLINK ELECTRONICS.		INTERLINK ELECTRONICS 546 Flynn Road, Camarillo, CA 93012	
DESCRIPTION: PCBA,Tx,IE,Celica		DATE: 8/30/02		REV. NO.: 20-89778	
DESIGNED BY: J.JANGCANAN		CHECKED BY:		SCALE: NONE	
DATE:		FILE:		DO NOT SCALE DRAWING	
				SHEET 1 OF 2	

REVISION HISTORY	
REV	DESCRIPTION
P6	SEE SHEET 1



REV	20-89778	REV	P6
SCALE	NONE	DO NOT SCALE DRAWING	SHEET 2 OF 2