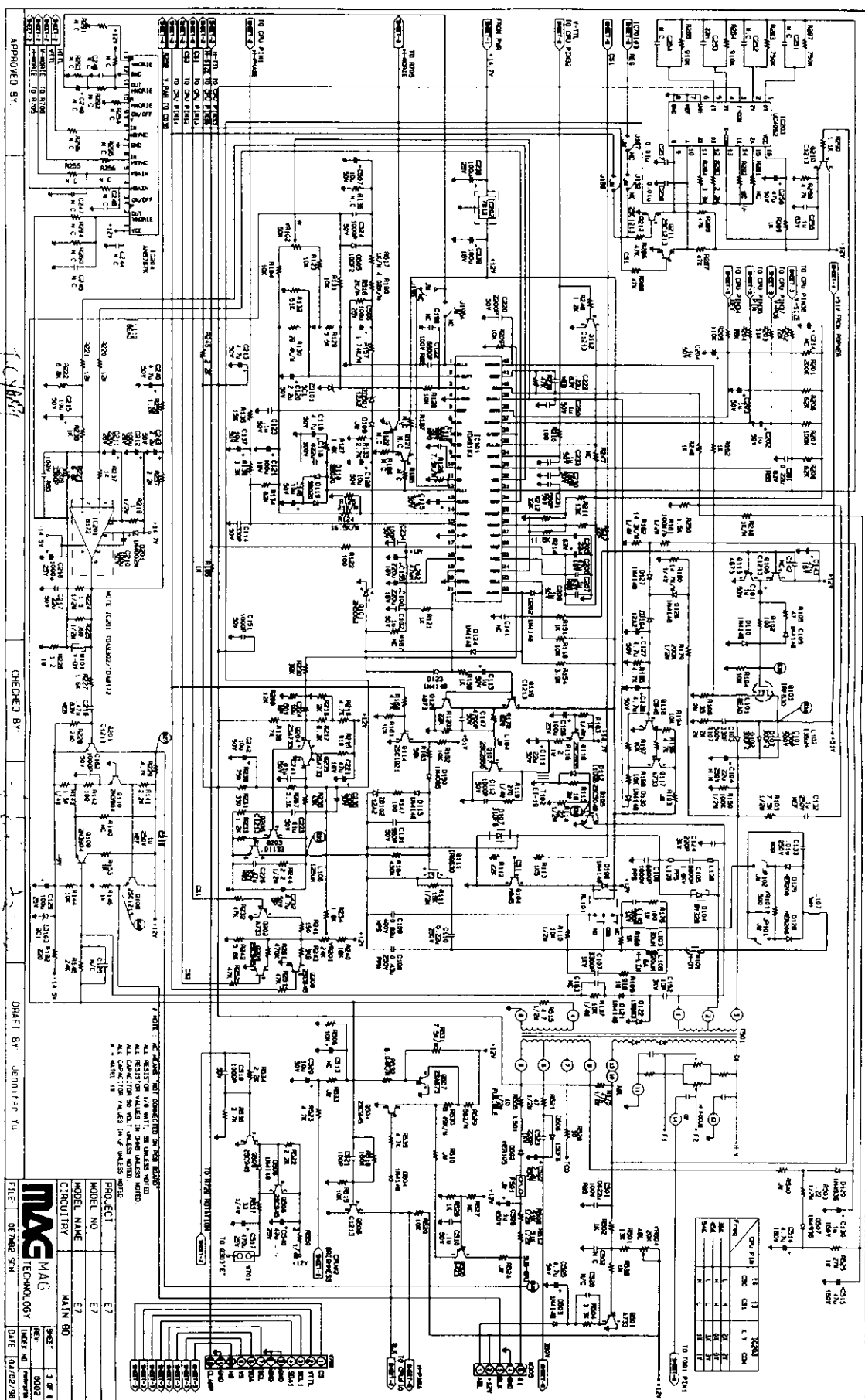




APPROVED BY

CHECKED BY

DRAFT 8Y Jennifer Yu

FILE	067082.SCH
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DATE	04/02/94
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PLA

Circuitry

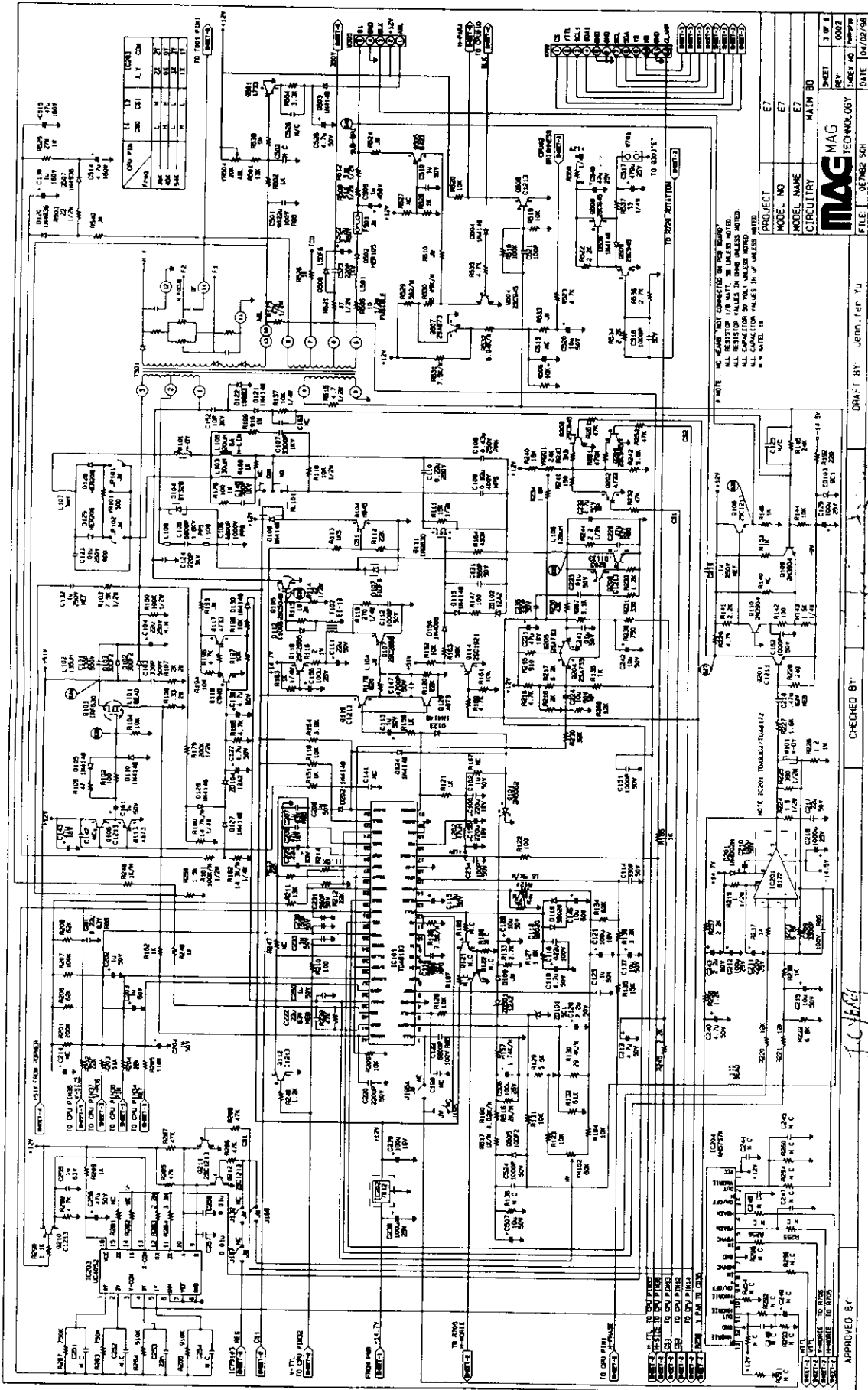
MODEL NAME

MODEL NO

PROJECT	
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NOTED

03/04/2007



PROJECT	E7
MODEL NAME	E7
CIRCUIT	MAIN BO
SHEET	3 OF 4
REV	0002
DATE	04/02/98

MAG TECHNOLOGY

DRAFT BY: Jennifer Yu

CHECKED BY:

APPROVED BY:

NOTE: 1. ALL VALUES ARE IN UNITS OF 100 UNLESS NOTED.
2. ALL RESISTOR VALUES IN OHMS UNLESS NOTED.
3. ALL CAPACITOR VALUES IN PICO UNLESS NOTED.
4. ALL DIMENSIONS ARE IN INCHES UNLESS NOTED.

NOTE: 1. IC01 TRANSISTOR/TRANSISTOR

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