

May 25, 1999

Federal Communications Commission
Equipment Services
P.O. Box 358315
Pittsburgh, PA 15251-5315

TO WHOM IT MAY CONCERN:

In applying for class II permissive change to the previous Type Acceptance for the SCLPA 800 linear power amplifier, Spectrian requests that certain items be withheld from public inspection upon such type acceptance authorization. This request is made in accordance with FCC section 0.459 (a) and (b). If this request for confidentiality is denied, Spectrian requests that the materials be returned without consideration pursuant to FCC section 0.459 (e)

The listed materials, submitted for review of the request for type acceptance, include trade secrets which, if made available for public inspection, would seriously damage Spectrian's competitive advantage. Spectrian's competition could gain advantage by studying these documents, and deduce the unique aspects of our amplifier architecture. These trade secrets allow Spectrian to produce an amplifier with lower labor cost, more production performance margin, and better performance over temperature, time frequency, power level, and number of carriers. In particular, those documents (submitted in Exhibits VI, and VIII, .) are:

- 1) **Schematic Diagram :Control Board: SD-9A-000047P1**. This document contains specific transistor biasing, protection circuitry and descriptions sensitive to Spectrian's competitive advantage. The document also contains reference designators and details that, if disclosed, would reveal trade secrets, undermining Spectrian's competitive advantage.
- 2) **Schematic Diagram :Oriole: SD-9A-000048B2**. This document contains specific transistor matching circuitry, transistor biasing, and descriptions sensitive to Spectrian's competitive advantage. The document also contains reference designators and details that, if disclosed, would reveal trade secrets, undermining Spectrian's competitive advantage.
- 3) **Technical Description**, included in Exhibit VI, in response to paragraph 2.983 (d) (6). This technical description contains descriptions of devices, circuits, operating power levels, gains, bias levels, and linearization technology considered to be trade secrets. These descriptions, if disclosed, could seriously damage Spectrian's competitive advantage.
- 4) **Block Diagram BD-02-000065B1**. This document contains operating levels and circuit details sensitive to Spectrian's competitive advantage. These details, if disclosed, could seriously damage Spectrian's technical and competitive advantage.
- 5) **Complete Amplifier Specification**. The complete specification for the amplifier contains operating levels and performance related to the system which are proprietary and confidential to the customer, and which would, if made known, could seriously damage the customers competitive advantage. Spectrian is filing for FCC Part 24 Type Acceptance on behalf of our customer.

Several technologies designed into this amplifier are covered by Spectrian-owned patents and/or patent filings. The following is a summary of some of those technologies:

Approved Patents

- **Wideband Feeds** are used in the RF amplifier stages. This technology is covered by Patent #5,272,450, issued 12/21/93.
- The Spectrian-manufactured transistors use technology and processes which are covered by patent #5,001,083, issued 3/19/91, **Method of Priming Semiconductor Substrate for Subsequent Photoresist Masking and Etching**.
- **The RF Power Transistor Package** used in the Q500, Q600 and Q700, on the BPM is covered by Patent #5338,974, issued 8/16/94.
- **The Method of Thermal Balancing RF Transistor Array** is covered by Patent #5,023,189, issued 6/11/91.
- The Spectrian-manufactured transistors also use processes covered by **Feed Bus RF Power Transistors**, Patent #5,329,156, issued 7/12/94.

Sincerely

William J. Hennig Jr.
Manager, Amplifier Engineering