December 20, 1998

Federal Communications Commission
Equipment Approval Services
P.O. Box 358315
Pittsburgh, PA 15251-5315

Reference: FCC Confirmation Number: EA92496
FCC ID: I2OMCPS200 (formally requested as I2OMCPS23504800)

To Whom It May Concern:

In applying for type acceptance for the MCPS2000 Multi-channel power amplifier system, Spectrian requests that certain items be witheld from public inspection upon such type acceptance authorization. This request is made in accordance with FCC section 0.459 (a) and (b). If this request for confidentiality is denied, Spectrian requests that material is returned without consideration pursuant to FCC section 0.459 (e).

The listed materials, submitted for review of the request for type acceptance, include trade secrets, which, if made available for public inspection, would seriously damage Spectrian's competitive advantage. Spectrian's competition could gain advantage by studying these documents and deduce the unique aspects of our system/amplifier architecture. These trade secrets allow Spectrian to produce an amplifier with lower labor cost, more production performance margin, and better performance over temperature, time, frequency, power level and a number of carriers. In particular, those documents are:

Description

1) Schematic Diagram
2) Block Diagram
3) Interconnect Diagrams
4) Function of Active Devices portion of Technical Description
5) Internal Photographs
6) Installation Guidelines

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Spectrian owned patents and/or patent filings covers several technologies designed into this amplifier. The following is a summary of some of those technologies:

Approved Patents

Planer Microstrip Baluns are used in the RF amplifier stages. This technology is covered by Patent # 5,304,959 issued 4/19/94.

The **RF Power Transistor Package** used in the RF devices throughout the amplifier is covered by Patent # 5,338,974 issued 8/16/94.

The Method of Thermal Balancing RF Power Transistor Array is covered by Patent # 5,023,189 issued 6/11/94.

Patents Pending

A patent application has been filed for the **Power Stack Basic Power Module (BPM) Architecture** used in the main amplification path to maximize power and reliability.

Sincerely,

John Pelose
General Manager, Multi-Carrier