



Testing Tomorrow's Technology

October 11, 2007
Mr. Doug Noble
ATCB

RE: Cirronet

FCC ID:HSW-2430HP

Mr. Noble,

Thank you for your comments in your letter of October 11, 2007. Below is my response to your comments on this application. I have included your original comments in my response.

I have a few comments on this Application.

1. For block diagram exhibit all crystal frequencies must be listed.
- 2.1033(b) (5) A block diagram showing the frequency of all oscillators in the device. The signal path and frequency shall be indicated at each block. The tuning range(s) and intermediate frequency(ies) shall be indicated at each block.

Corrected and Uploaded.

2. Please upload REL Listing letter for Industry Canada.

Uploaded

- 3 The confidentiality letter asks that Parts List be kept confidential. Please upload Parts List exhibit or revise the confidentiality letter to exclude parts list.

Uploaded corrected Confidentiality letter excluding Parts List.

Please contact me with any further questions.

Sincerely,

Louis A. Feudi
Vice President of Operations and Engineering

3505 Francis Circle Alpharetta, GA 30004
PH: 770-740-0717 Fax: 770-740-1508
www.ustech-lab.com