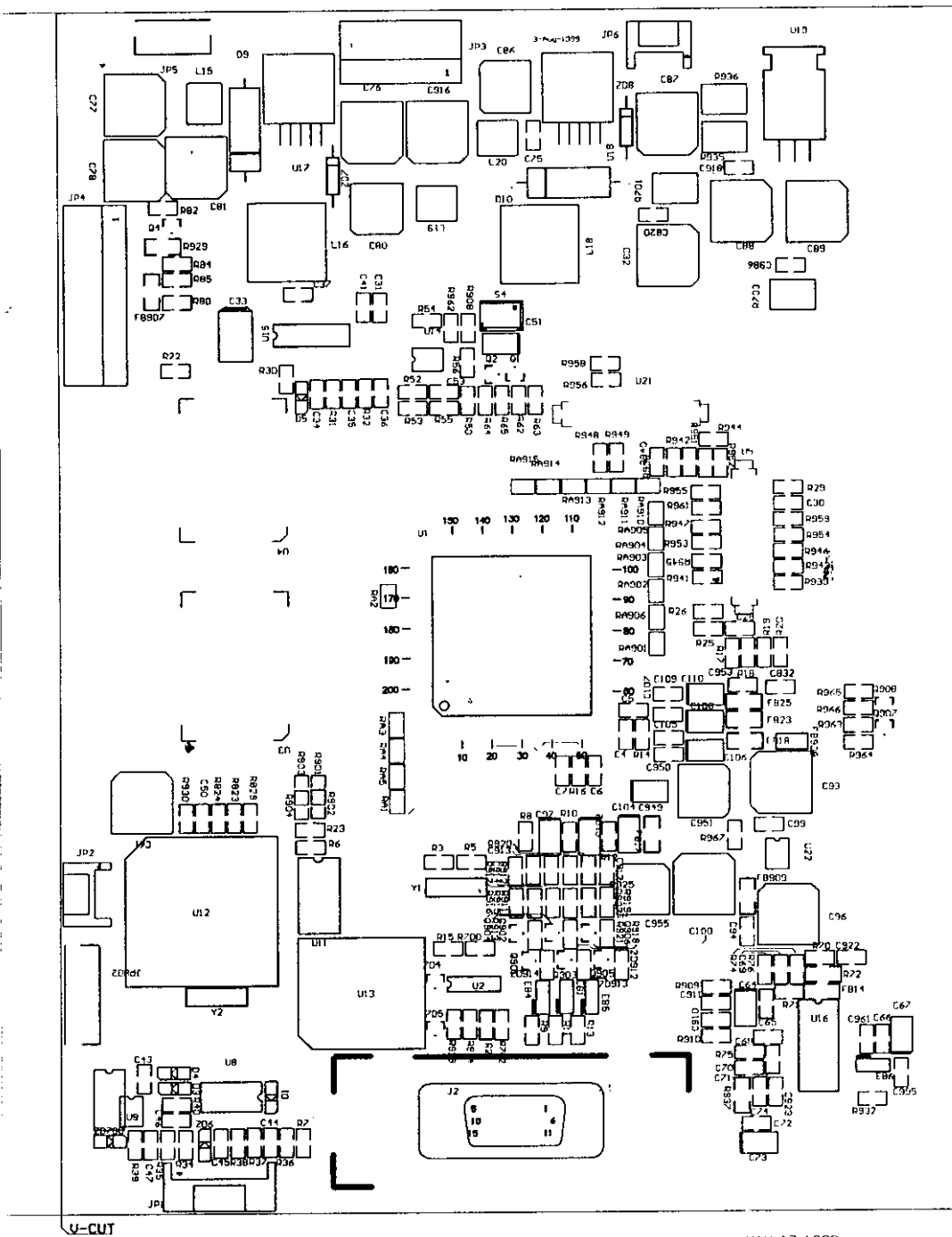
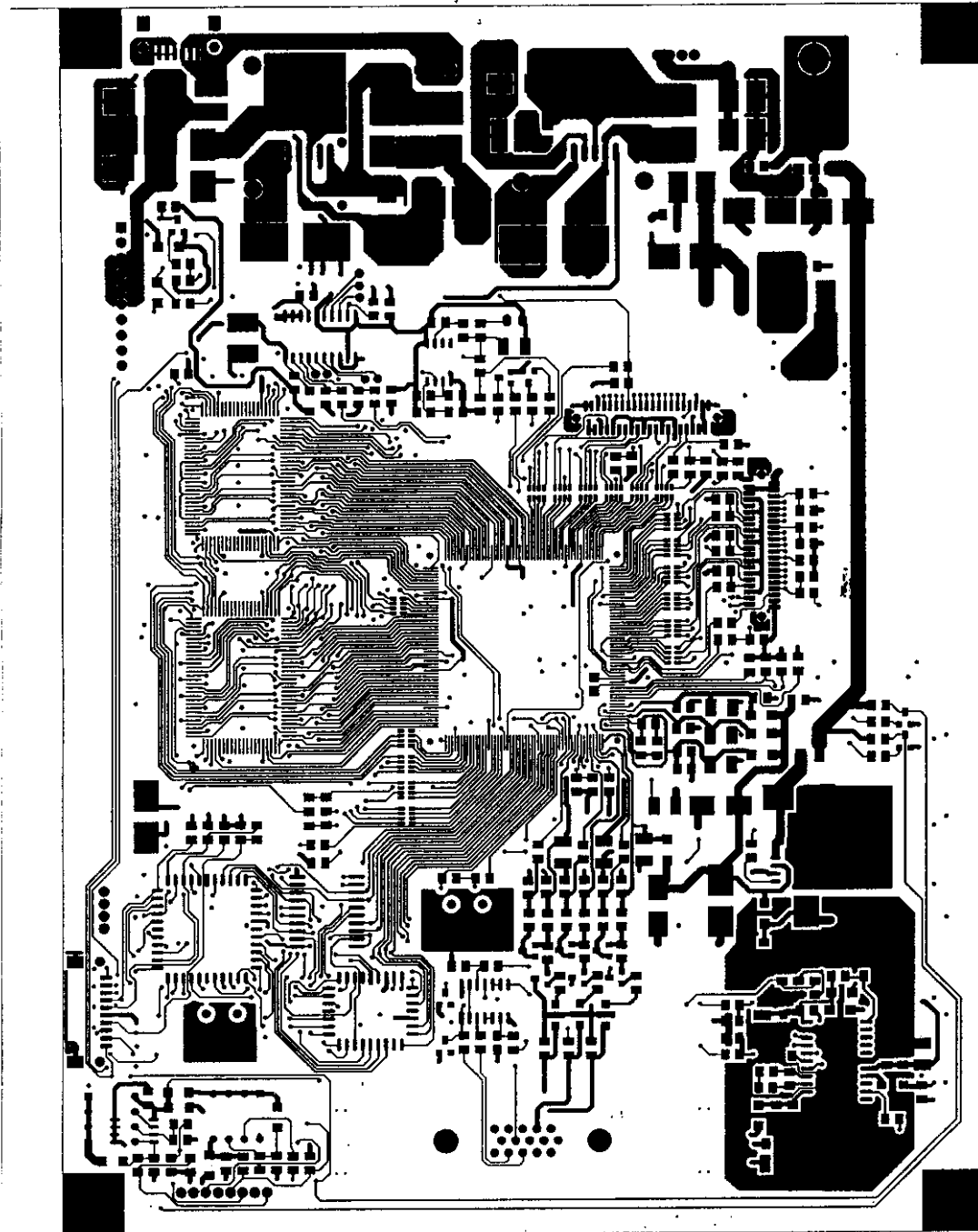




3-Aug-1999

MAY 17, 1999
R-5 MAIN BOARD REV:1.1

FCC ID: HSUTRLR-5

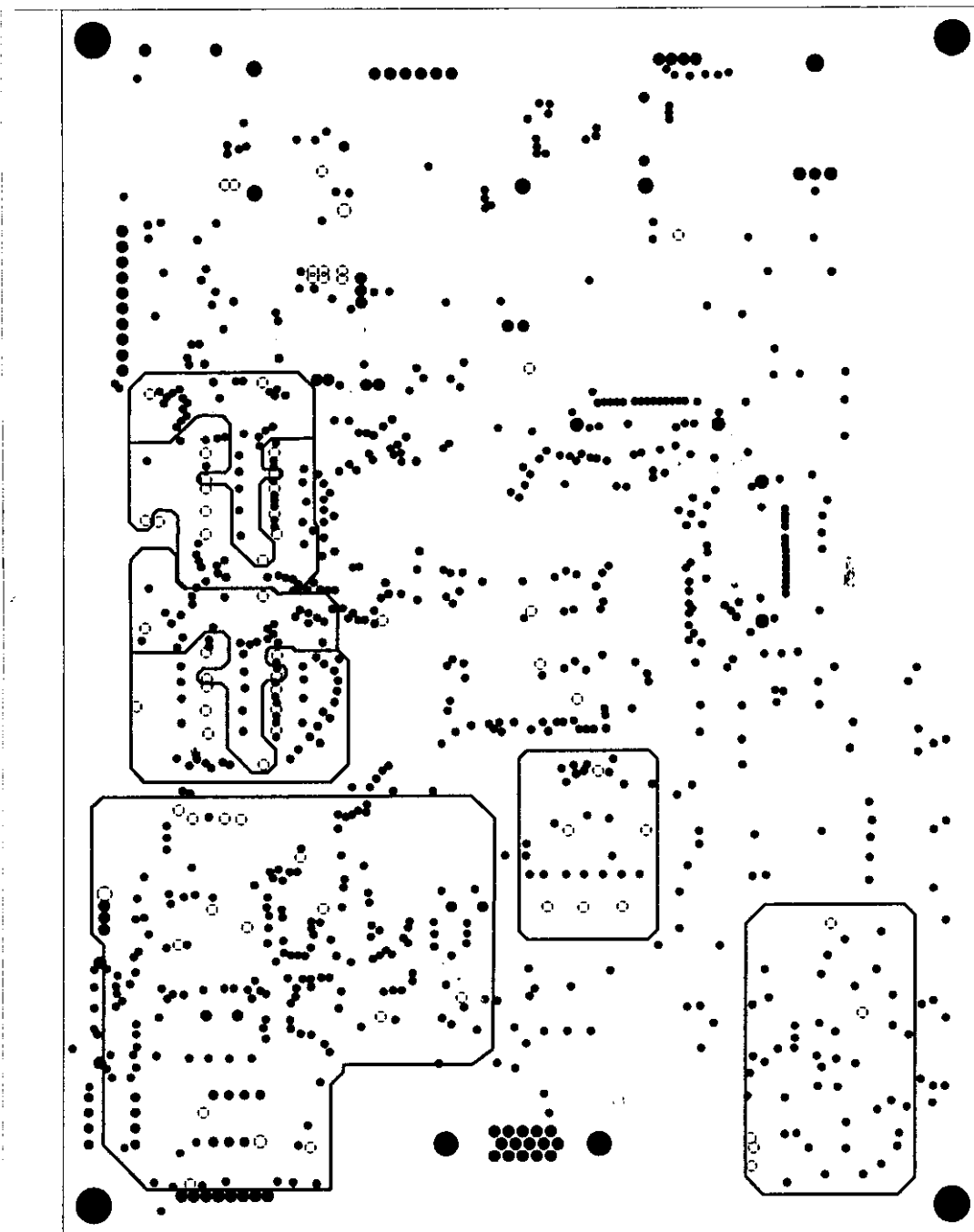


U-CUT

Top Layer

1

3-Aug-1993

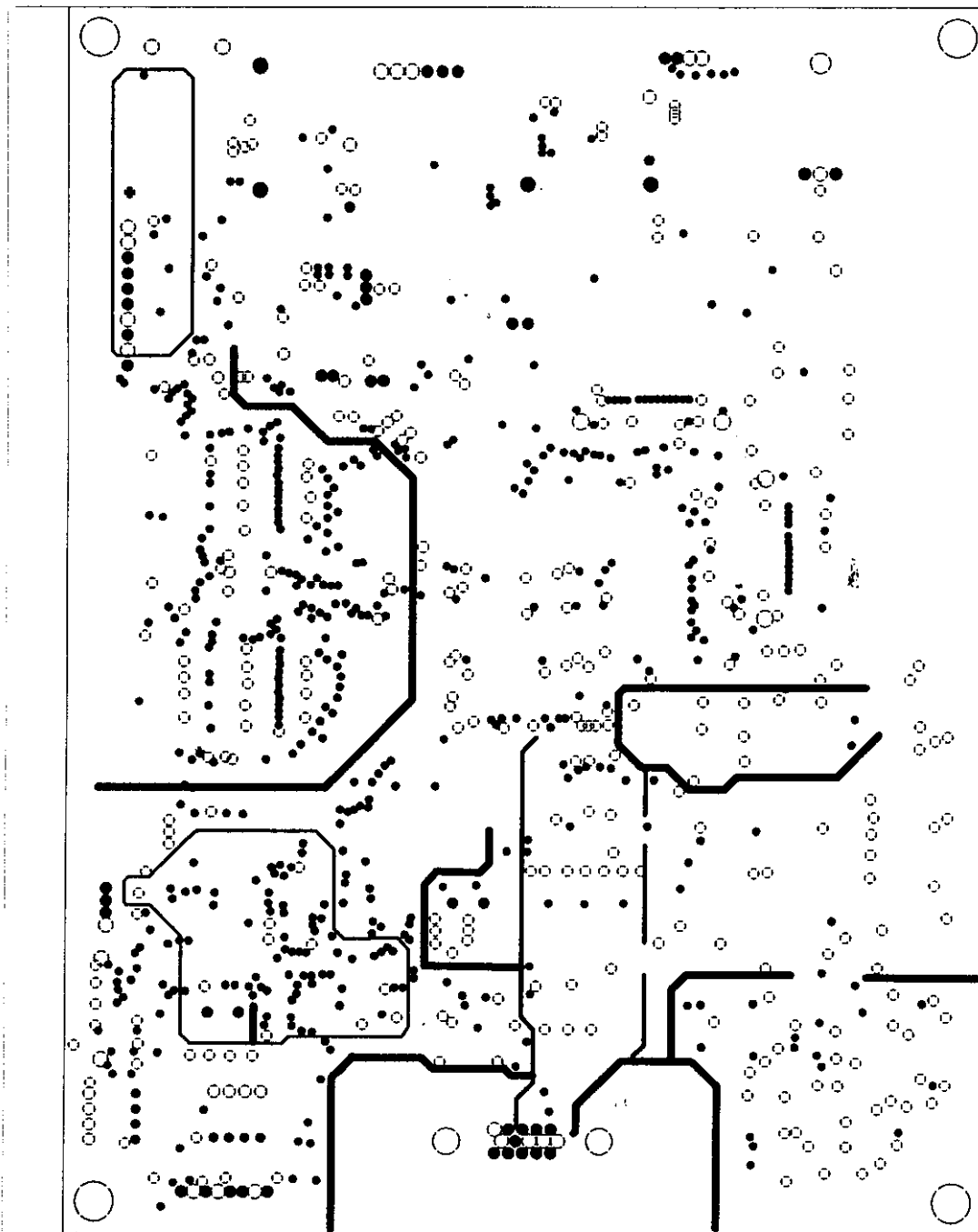


V-CUT

Internal Plane 2

3

3-Aug-1999

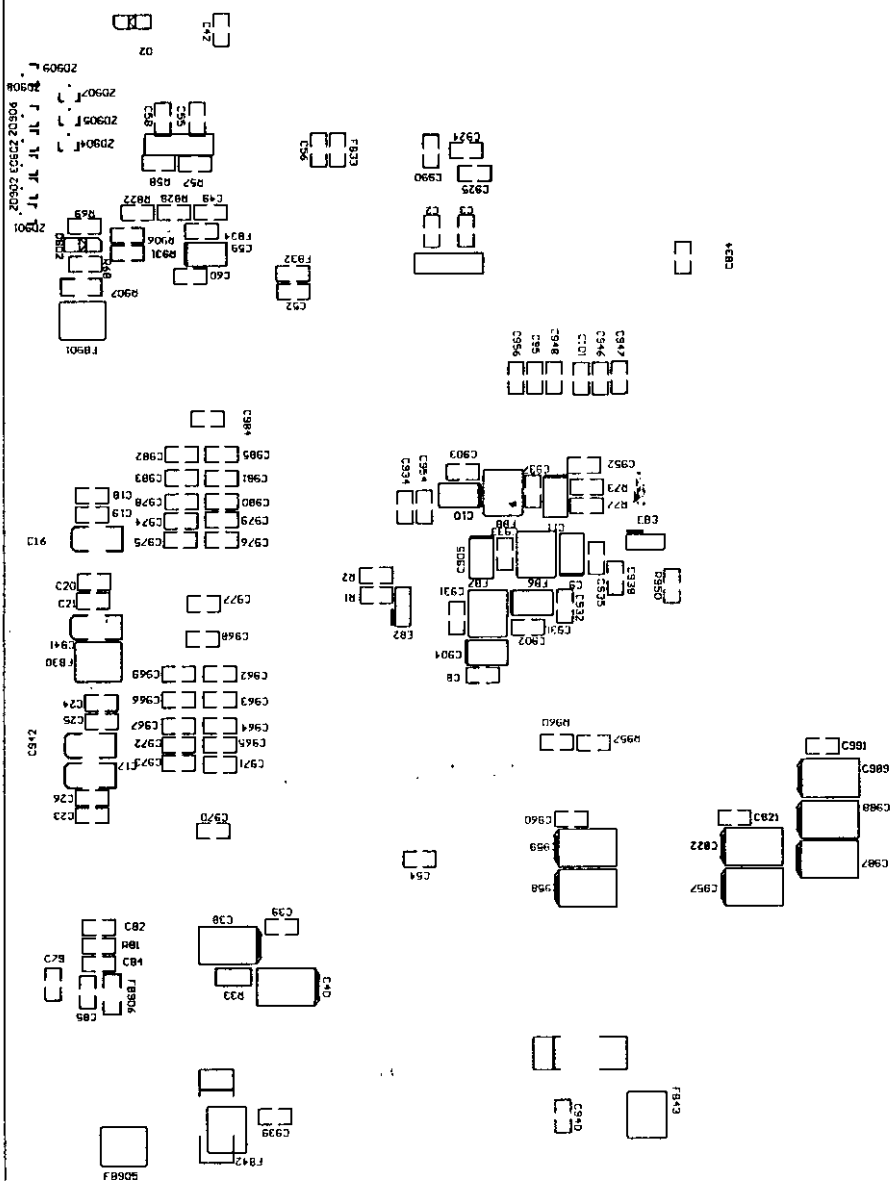


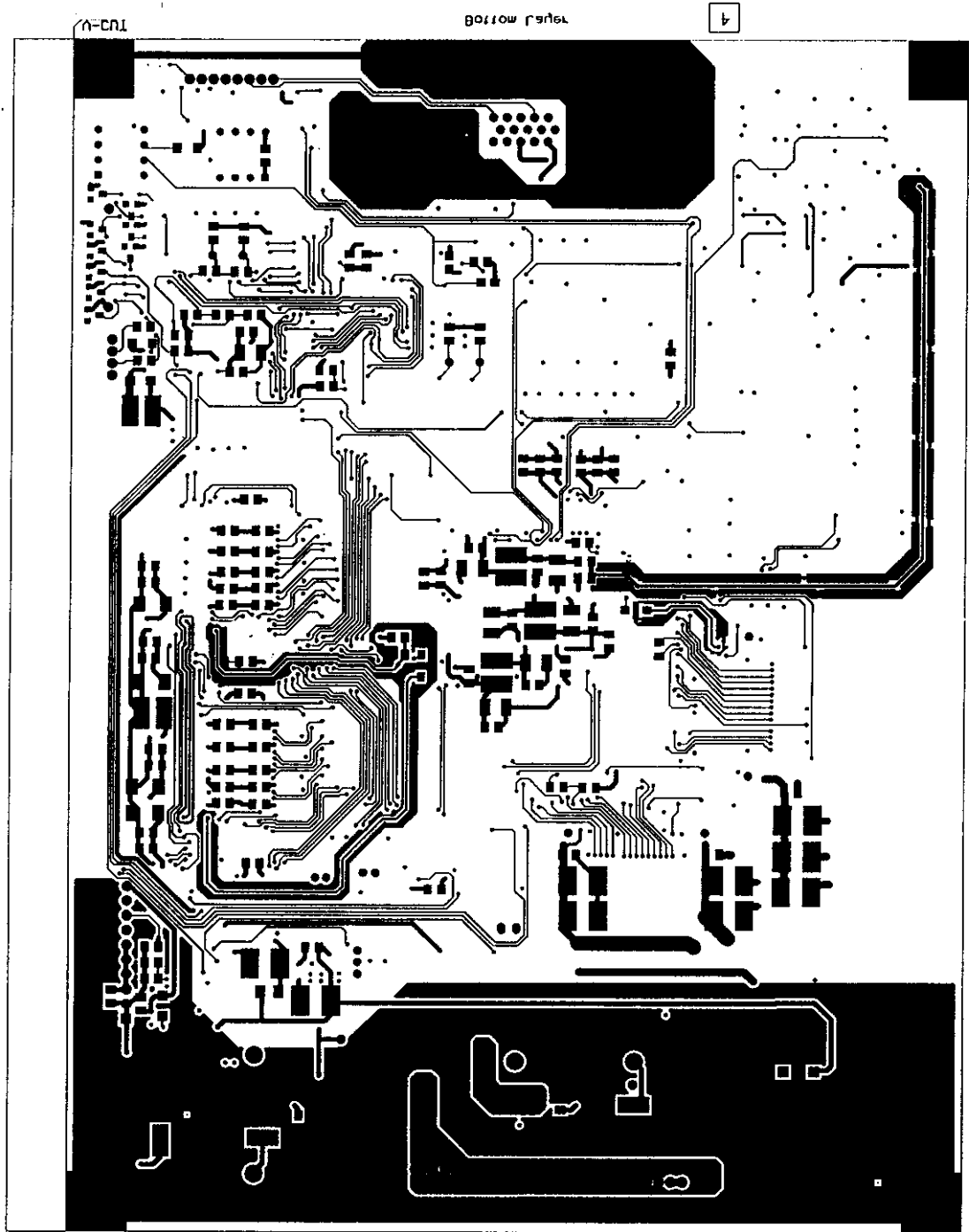
V-CUT

Internal Plane 1

2

3-Aug-1999





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VERSION 0.1 REVERSION 0.7

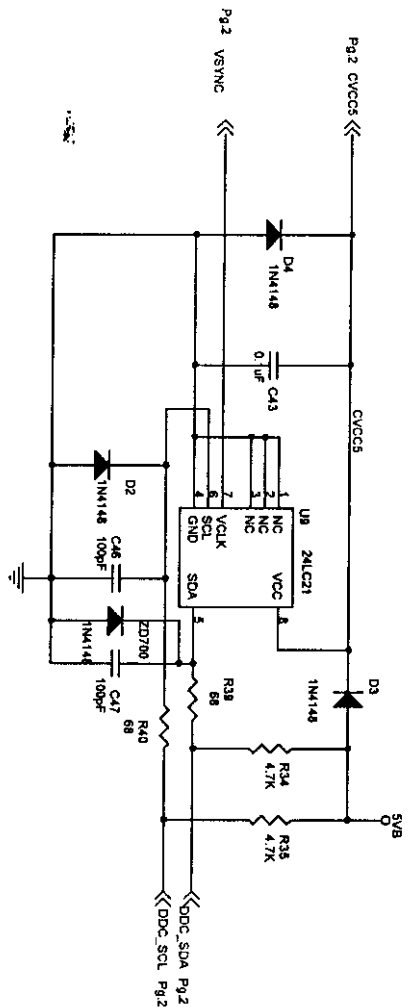
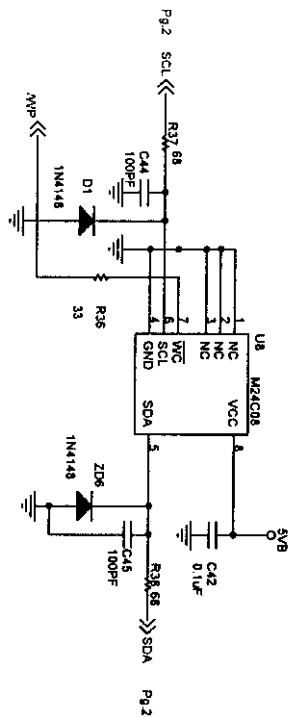
History:

4/1/99 - 1st Created.
DESIGN BY BYRON
4/5/99 - Added RGB Input Buffer,
Corrected Schematic errors, added MCU
ground filter and add support for 256K
& 512K x 32 memory address 8 & 9
select. Tony Chen

Royal Information Electronics Co., Ltd.

R-5 Cover Page

Date	Friday, July 16, 1999	Sheet	2	of	8
Size	Document Number				
Custom(Doos)					



R-5 EEPROM

Size Document Number

Change(Doel)

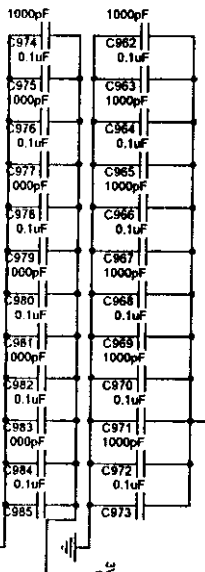
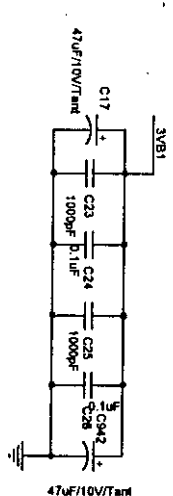
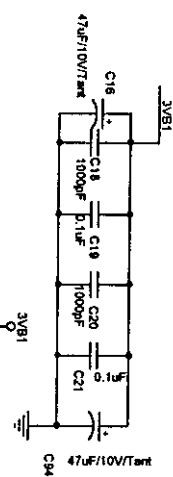
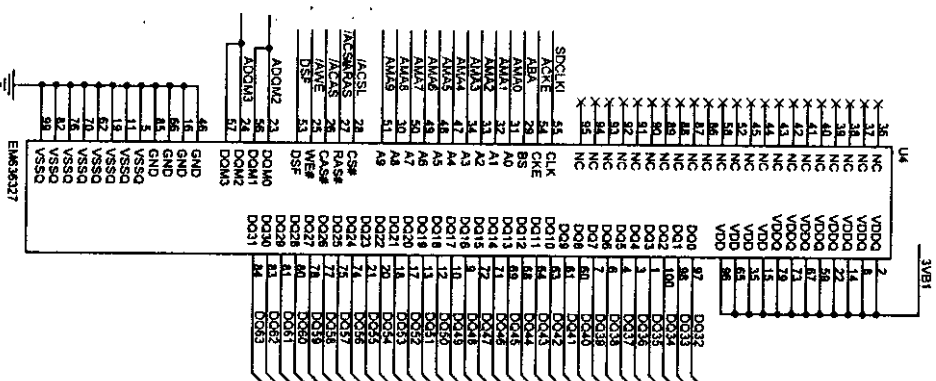
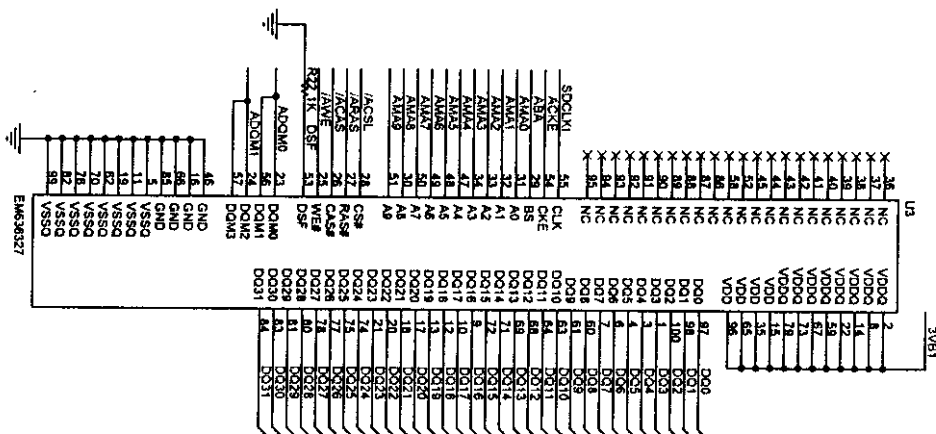
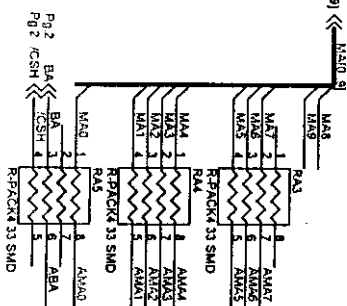
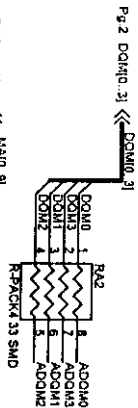
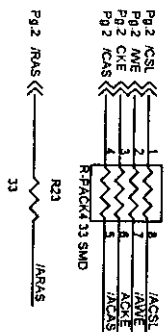
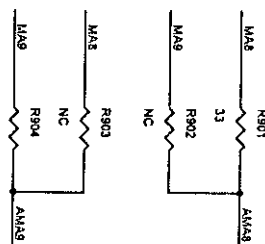
Friday, July 23, 1993

Print

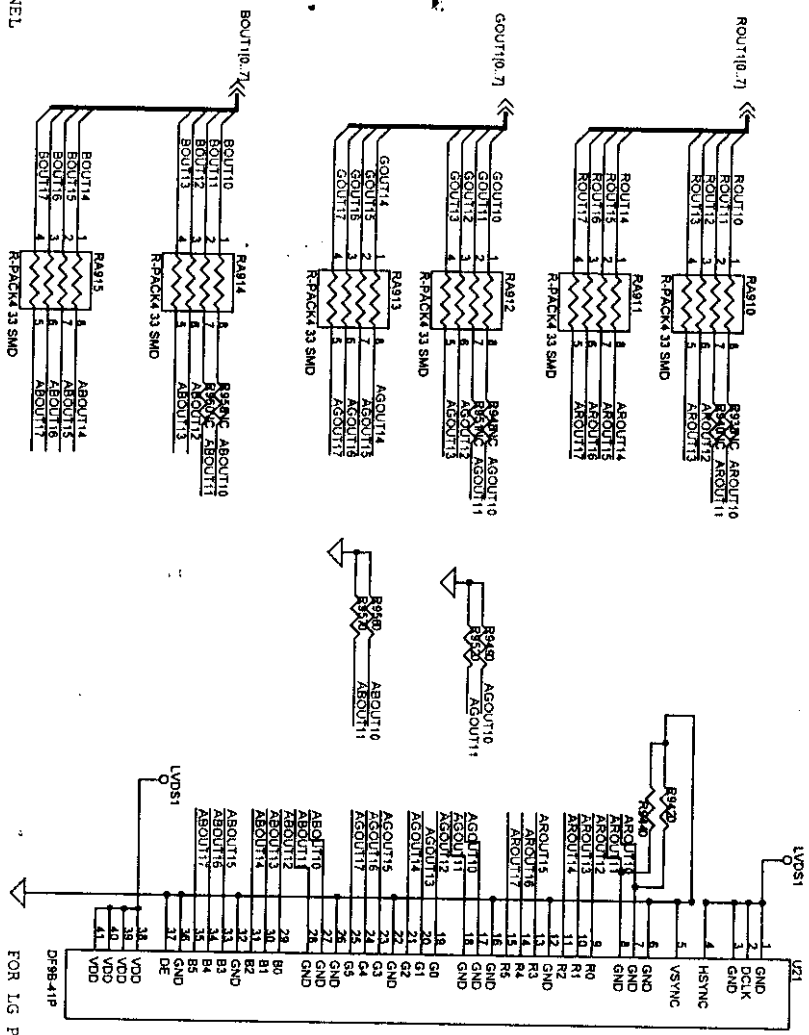
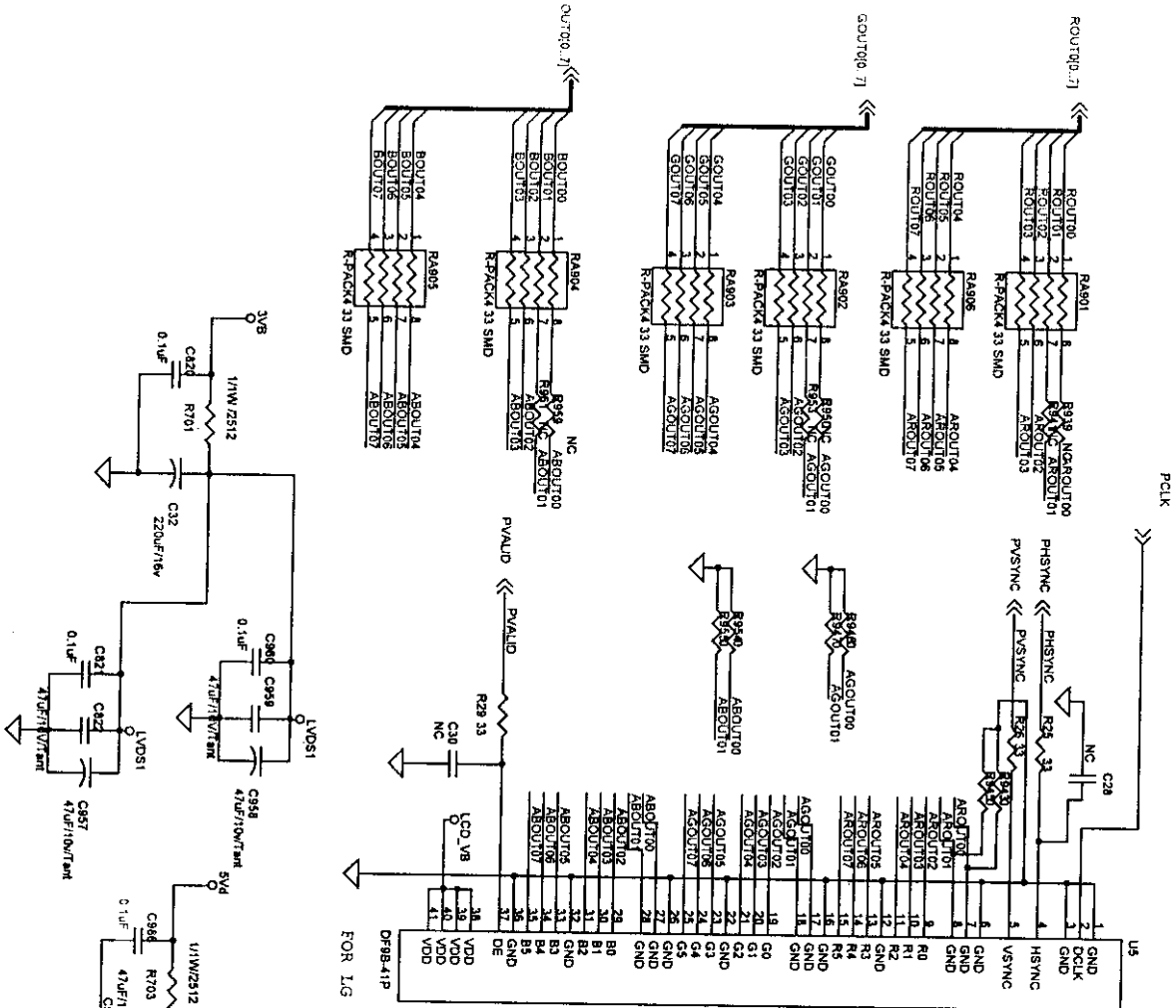
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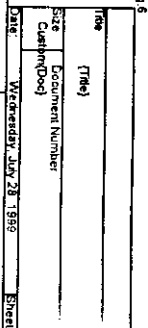
of

8

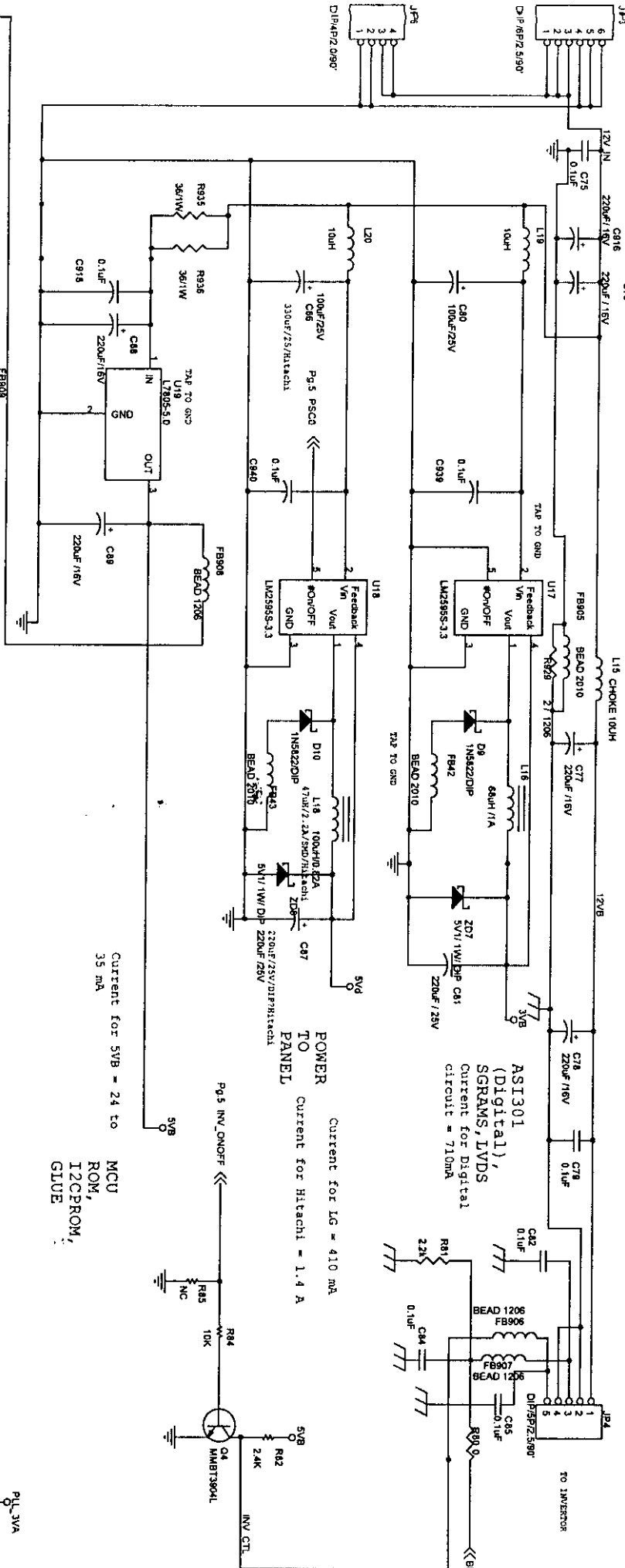




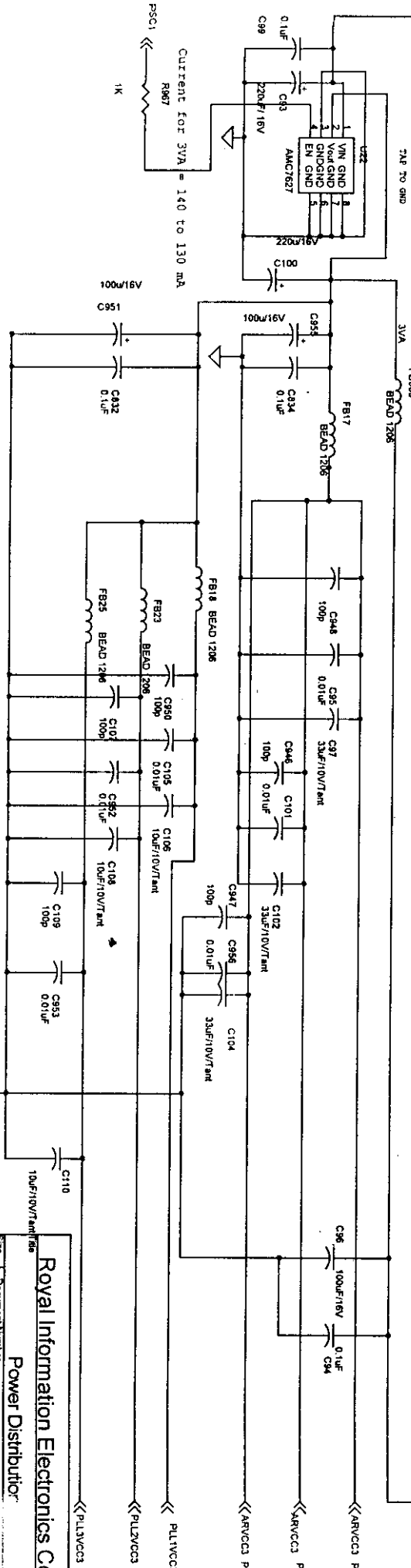




Current for LG=460mA
to 1.08A



Current for LG = 410 mA
POWER TO PANEL
Current for Hitachi = 1.4 A
Current for 5VB = 24 to 35 mA
MCU, ROM, I2CPROM, GLUE



FB & Cap must be placed closed to ASI301

Royal Information Electronics Co. L

Power Distributor

Document Number

Customer Order

Date: Friday, July 23, 1999

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