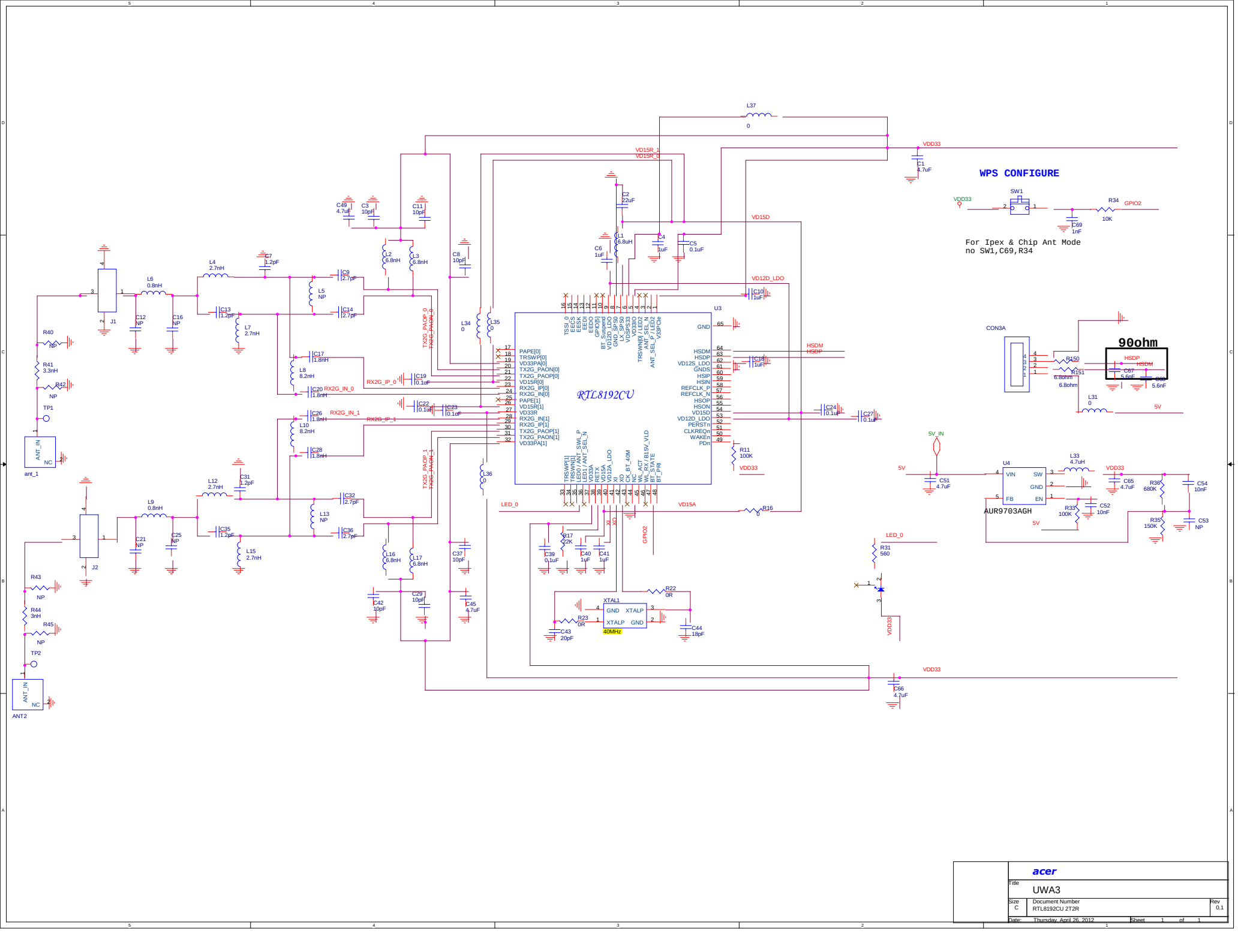
WPS CONFIGURE
For Ipex & Chip Ant Mode
no SW1,C69,R34
90ohm
RTL8192CU
U3
AUR9703AGH
U4
CON3A
ANT_IN
ant_1
ANT2
J1
J2
XTAL1
40MHz
VDD33
VD15D
VD12D_LDO
VD15A
5V
5V_IN
LED_0
HSDM
HSDP
TX2G_PAOP_0
TX2G_PAON_0
TX2G_PAOP_1
TX2G_PAON_1
RX2G_IP_0
RX2G_IN_0
RX2G_IN_1
RX2G_IP_1
XI
XO
GPIO2
Title
UWA3
Size
C
Document Number
RTL8192CU 2T2R
Rev
0.1
Date:
Thursday, April 26, 2012
Sheet
1
of
1