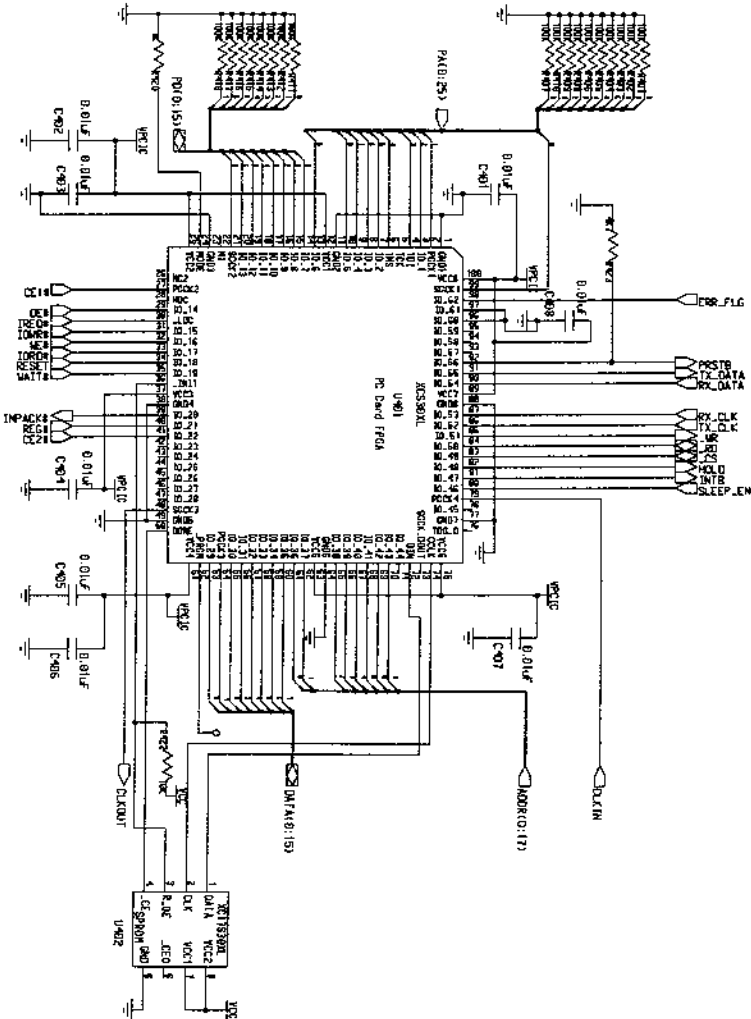
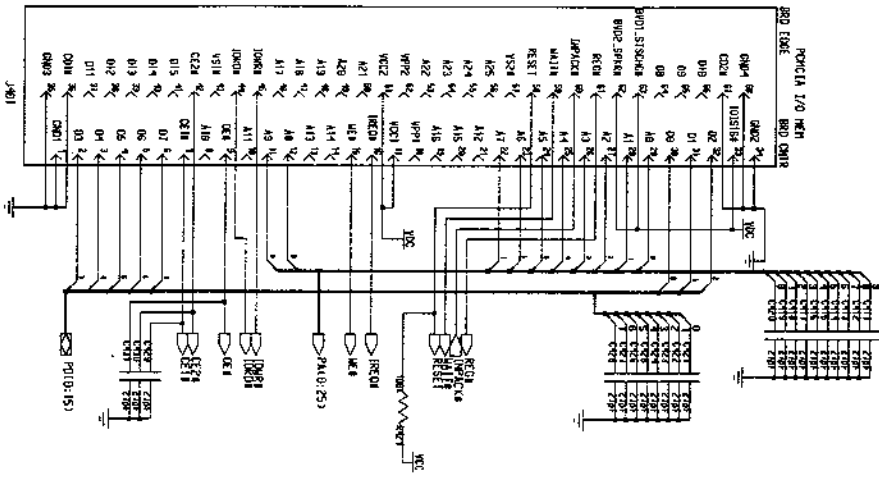


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REV.	DESIGN AUTH.	DATE
001	DESIGN AUTH.	04/01/2000
002	DESCRIPTION	04/01/2000

REV.	DATE	DESCRIPTION
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