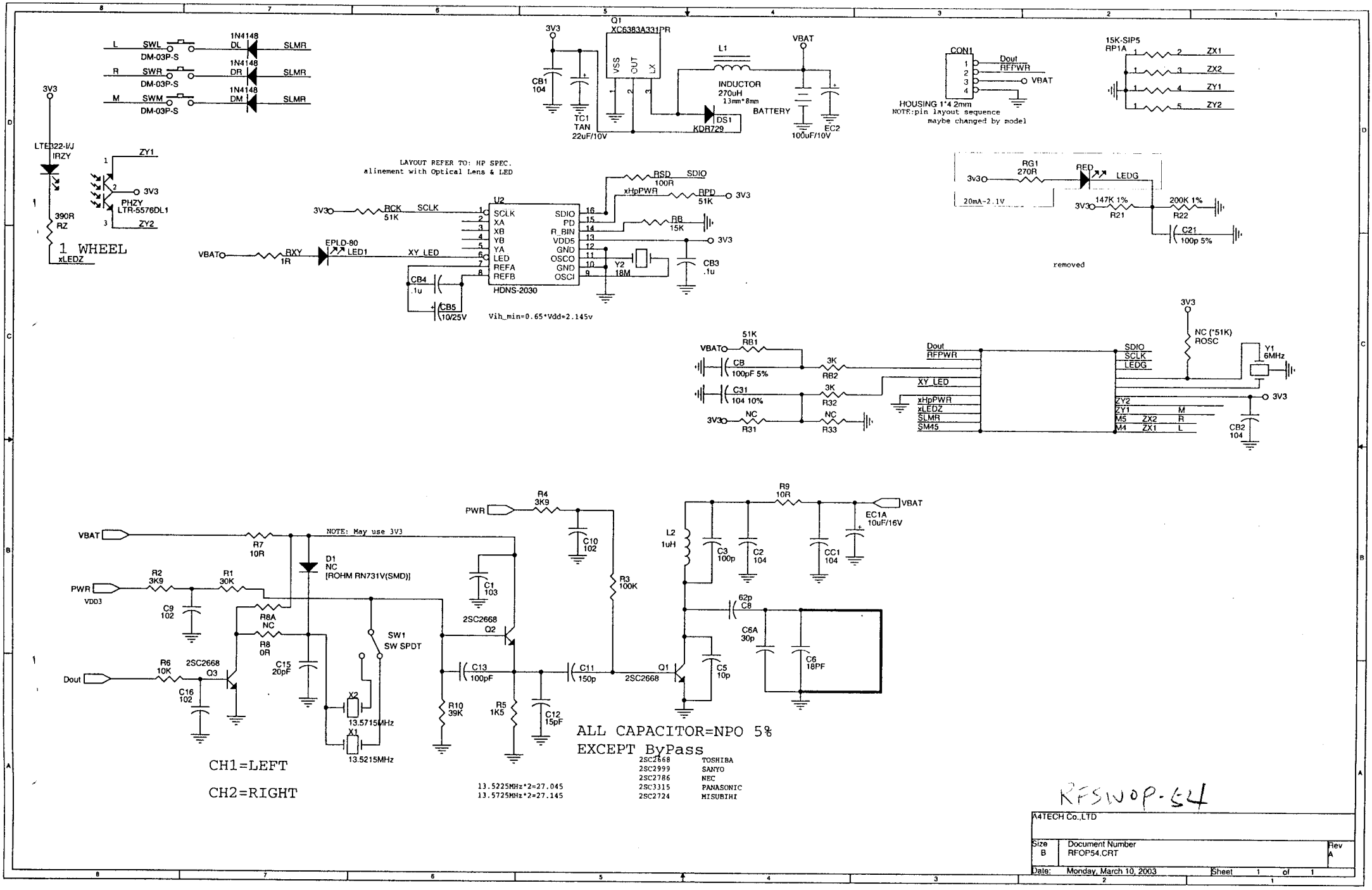
L SWL DM-03P-S DL 1N4148 SLMR
R SWR DM-03P-S DR 1N4148 SLMR
M SWM DM-03P-S DM 1N4148 SLMR
3V3
LTE322-I/J
IRZY
390R
RZ
1 WHEEL
xLEDZ
ZY1
3V3
PHZY
LTR-5576DL1
ZY2
LAYOUT REFER TO: HP SPEC.
alinement with Optical Lens & LED
3V3
RCK 51K
SCLK
VBAT
RXY 1R
EPLD-80
LED1
XY_LED
U2
SCLK
XA
XB
YB
YA
LED
REFA
REFB
SDIO
PD
R_BIN
VDD5
GND
OSCO
GND
OSCI
HDNS-2030
CB4 .1u
CB5 10/25V
Vih_min=0.65*Vdd=2.145v
RSD 100R
SDIO
xHpPWR
RPD 51K
3V3
RB 15K
3V3
Y2 18M
CB3 .1u
3V3
CB1 104
TC1 TAN 22uF/10V
Q1 XC6383A331PR
VSS
OUT
LX
L1
INDUCTOR 270uH 13mm*8mm
DS1 KDR729
VBAT
BATTERY
EC2 100uF/10V
CON1
Dout
RFPWR
VBAT
HOUSING 1*4 2mm
NOTE:pin layout sequence maybe changed by model
15K-SIP5
RP1A
ZX1
ZX2
ZY1
ZY2
3v3
RG1 270R
RED
LEDG
20mA-2.1V
3V3
147K 1%
R21
200K 1%
R22
C21 100p 5%
removed
VBAT
51K RB1
CB 100pF 5%
3K RB2
C31 104 10%
3K R32
3V3
NC R31
NC R33
Dout
RFPWR
XY_LED
xHpPWR
xLEDZ
SLMR
SM45
SDIO
SCLK
LEDG
3V3
NC (*51K) ROSC
Y1 6MHz
3V3
ZY2
ZY1 M
M5 ZX2 R
M4 ZX1 L
CB2 104
VBAT
R7 10R
NOTE: May use 3V3
D1 NC [ROHM RN731V(SMD)]
PWR
VDD3
R2 3K9
R1 30K
C9 102
R8A NC
R8 0R
C15 20pF
SW1 SW SPDT
Dout
R6 10K
2SC2668 Q3
C16 102
X2 13.5715MHz
X1 13.5215MHz
PWR
R4 3K9
C10 102
C1 103
2SC2668 Q2
R3 100K
C13 100pF
R10 39K
R5 1K5
C12 15pF
C11 150p
2SC2668 Q1
L2 1uH
C3 100p
C2 104
R9 10R
CC1 104
EC1A 10uF/16V
VBAT
62p C8
C6A 30p
C6 18PF
C5 10p
CH1=LEFT
CH2=RIGHT
13.5225MHz*2=27.045
13.5725MHz*2=27.145
ALL CAPACITOR=NPO 5%
EXCEPT ByPass
2SC2668 TOSHIBA
2SC2999 SANYO
2SC2786 NEC
2SC3315 PANASONIC
2SC2724 MISUBIHI
RFSWOP-54
A4TECH Co.,LTD
Size B
Document Number RFOP54.CRT
Rev A
Date: Monday, March 10, 2003
Sheet 1 of 1