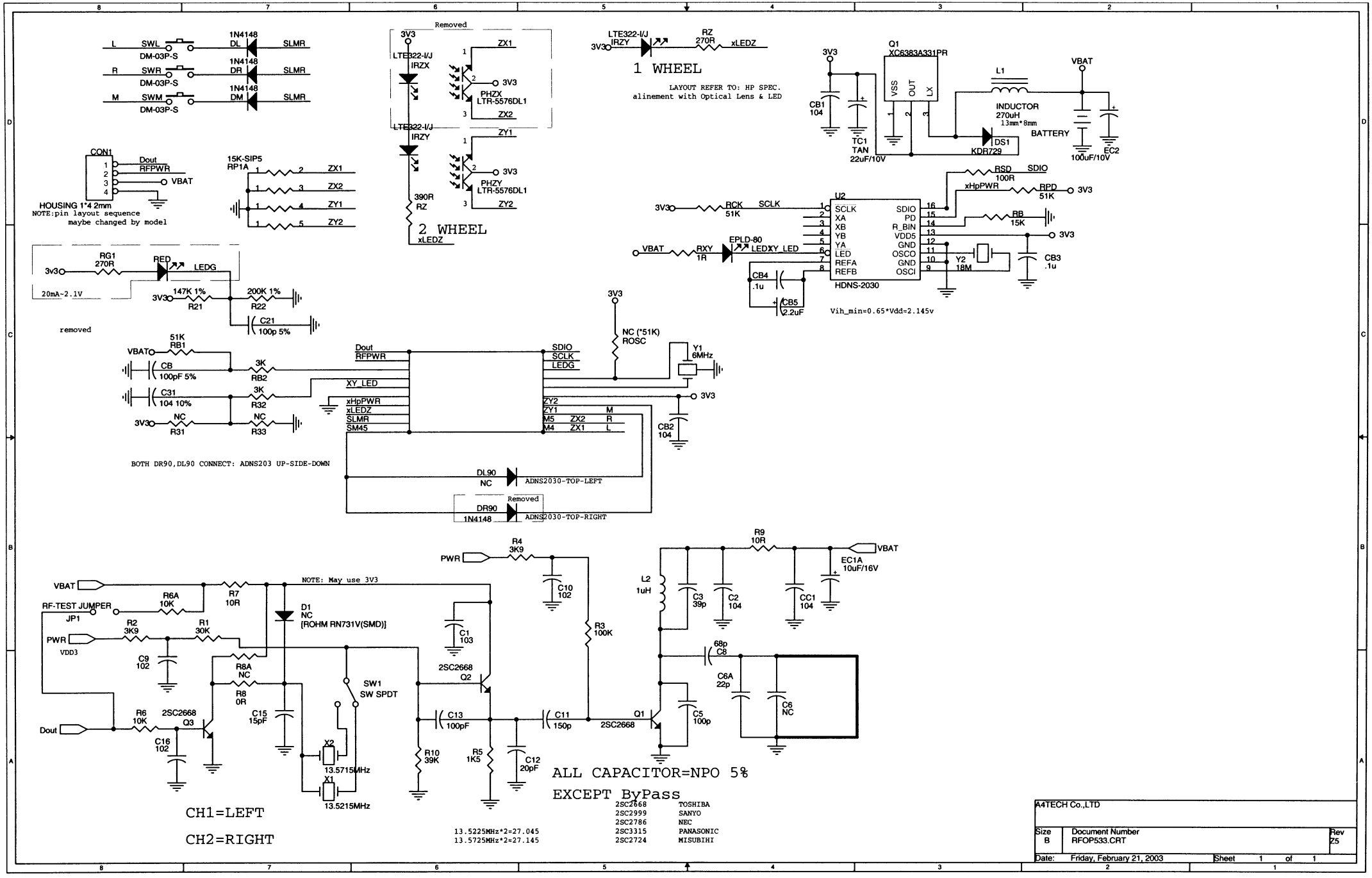
L SWL DM-03P-S 1N4148 DL SLMR
R SWR DM-03P-S 1N4148 DR SLMR
M SWM DM-03P-S 1N4148 DM SLMR
Removed
3V3 LTE322-I/J IRZX ZX1 PHZX LTR-5576DL1 ZX2
LTE322-I/J IRZY ZY1 PHZY LTR-5576DL1 ZY2
390R RZ
2 WHEEL
xLEDZ
LTE322-I/J IRZY RZ 270R xLEDZ
1 WHEEL
LAYOUT REFER TO: HP SPEC.
alinement with Optical Lens & LED
Q1 XC6383A331PR
VSS OUT LX
CB1 104
TC1 TAN 22uF/10V
L1
INDUCTOR 270uH 13mm*8mm
DS1 KDR729
VBAT
BATTERY
EC2 100uF/10V
CON1
Dout
RFPWR
VBAT
HOUSING 1*4 2mm
NOTE:pin layout sequence maybe changed by model
15K-SIP5 RP1A
ZX1 ZX2 ZY1 ZY2
RSD 100R SDIO
xHpPWR RPD 51K
RCK 51K SCLK
U2
SCLK XA XB YB YA LED REFA REFB
SDIO PD R_BIN VDD5 GND OSCO GND OSCI
HDNS-2030
RB 15K
EPLD-80 RXY 1R LEDXY_LED
CB4 .1u
CB5 2.2uF
Y2 18M
CB3 .1u
Vih_min=0.65*Vdd=2.145v
RG1 270R RED LEDG
20mA~2.1V
147K 1% R21
200K 1% R22
C21 100p 5%
removed
51K RB1
CB 100pF 5%
3K RB2
C31 104 10%
3K R32
NC R31
NC R33
Dout RFPWR
XY_LED
xHpPWR
xLEDZ
SLMR
SM45
SDIO SCLK LEDG
NC (*51K) ROSC
Y1 6MHz
CB2 104
ZY2 ZY1 M5 ZX2 M4 ZX1 M R L
BOTH DR90,DL90 CONNECT: ADNS203 UP-SIDE-DOWN
DL90 NC ADNS2030-TOP-LEFT
Removed
DR90 1N4148 ADNS2030-TOP-RIGHT
PWR R4 3K9
C10 102
R3 100K
R9 10R
EC1A 10uF/16V
L2 1uH
C3 39p
C2 104
CC1 104
68p C8
C6A 22p
C6 NC
C5 100p
Q1 2SC2668
VBAT
RF-TEST JUMPER JP1
R6A 10K
R7 10R
NOTE: May use 3V3
D1 NC [ROHM RN731V(SMD)]
PWR VDD3
R2 3K9
R1 30K
C9 102
R8A NC
R8 0R
SW1 SW SPDT
2SC2668 Q2
C1 103
C13 100pF
C11 150p
C15 15pF
X2 13.5715MHz
X1 13.5215MHz
R10 39K
R5 1K5
C12 20pF
Dout
R6 10K
2SC2668 Q3
C16 102
ALL CAPACITOR=NPO 5%
EXCEPT ByPass
CH1=LEFT
CH2=RIGHT
13.5225MHz*2=27.045
13.5725MHz*2=27.145
2SC2668 TOSHIBA
2SC2999 SANYO
2SC2786 NEC
2SC3315 PANASONIC
2SC2724 MISUBIHI
A4TECH Co.,LTD
Size B
Document Number RFOP533.CRT
Rev Z5
Date: Friday, February 21, 2003
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