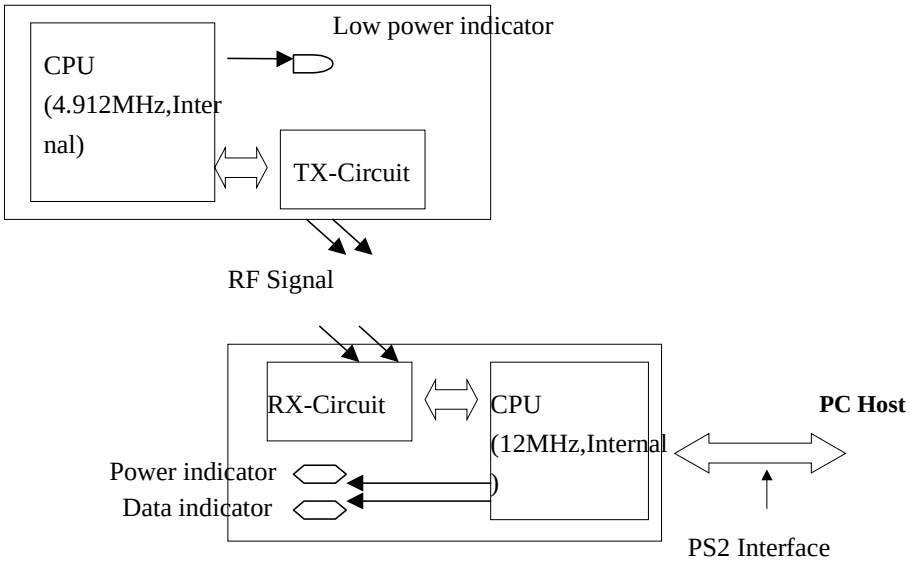
Block diagram

FCC ID : GYUR95SK
Model :SK-8810
RF Wireless Keyboard

SILITEK CORP.

Department: Wireless BU
Date: Oct-15-2001

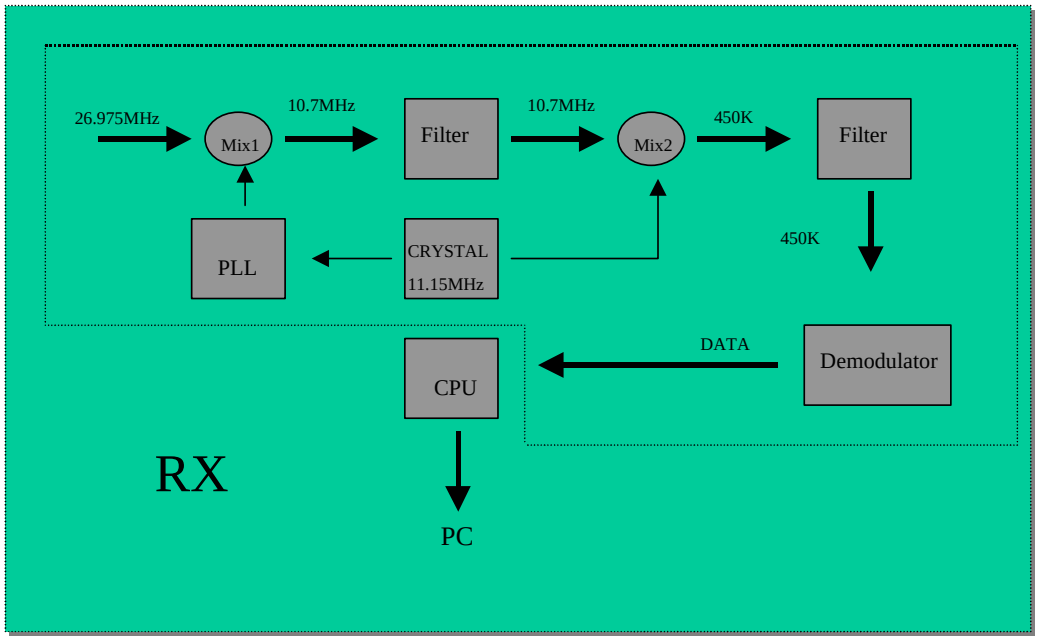
Functional Block diagram:



System description

The receiver part is design for a double conversion architecture. The incoming radio frequency Signal will be filtered and amplified before reaching the first mixer.At this stage the RF signal will be converted down to the first intermediate frequency (10.7MHz) by using a crystal oscillator (LO1).
The transmit part contain two PLL controlled VCOs.The frequency modulation is accomplished by superposing the incoming audio signal on the PLL control voltage. Final frequency is a product of mixing VCO1 with first local oscillator of receiver part (VCO3). The modulated carrier is amplified by externals power amplifier before entering the output filter and antenna connector.


TX
26.975MHz
27.015MHz


26.975MHz
Mix1
10.7MHz
Filter
10.7MHz
Mix2
450K
Filter
450K
PLL
CRYSTAL
11.15MHz
DATA
Demodulator
CPU
RX
PC