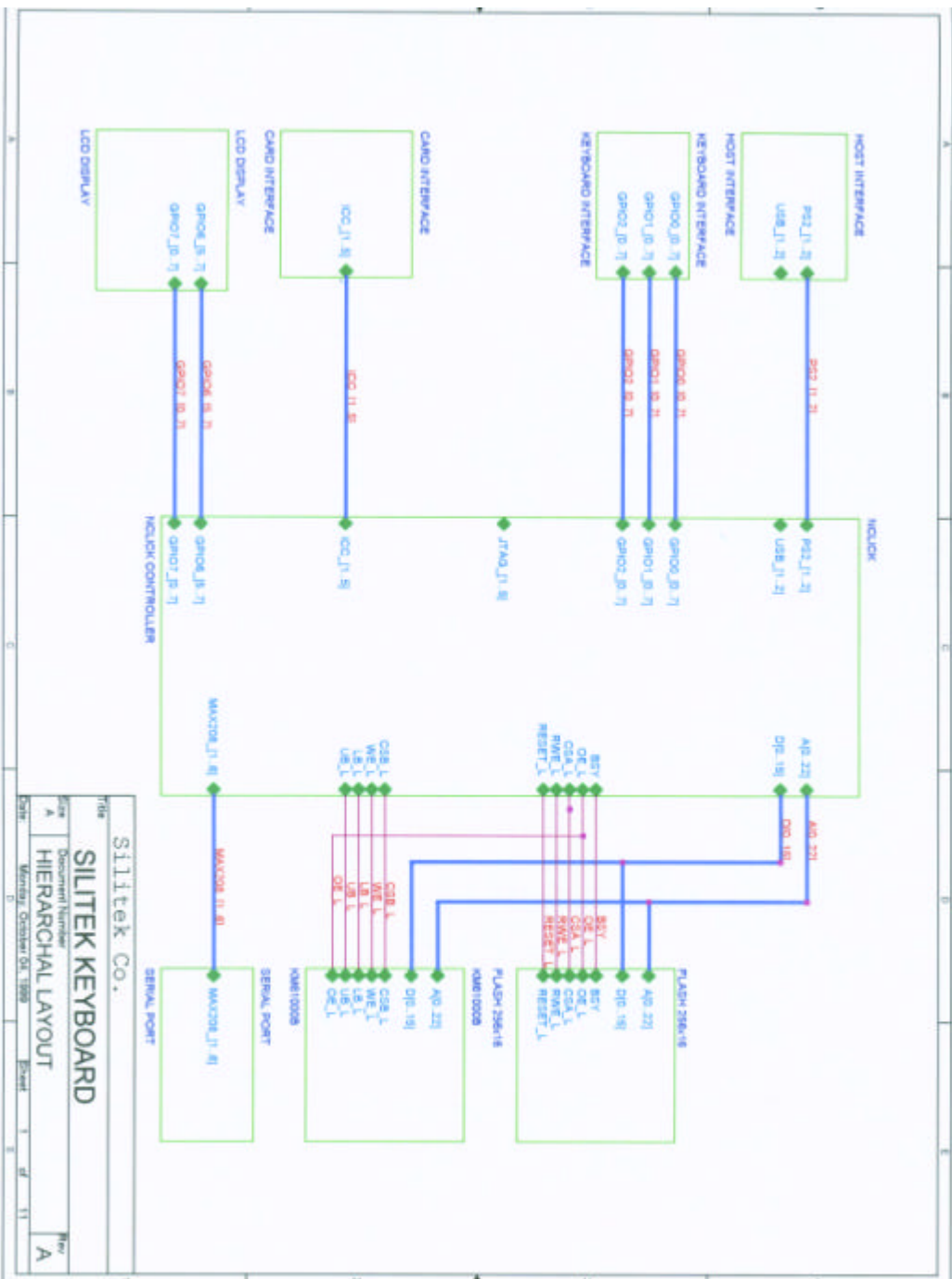
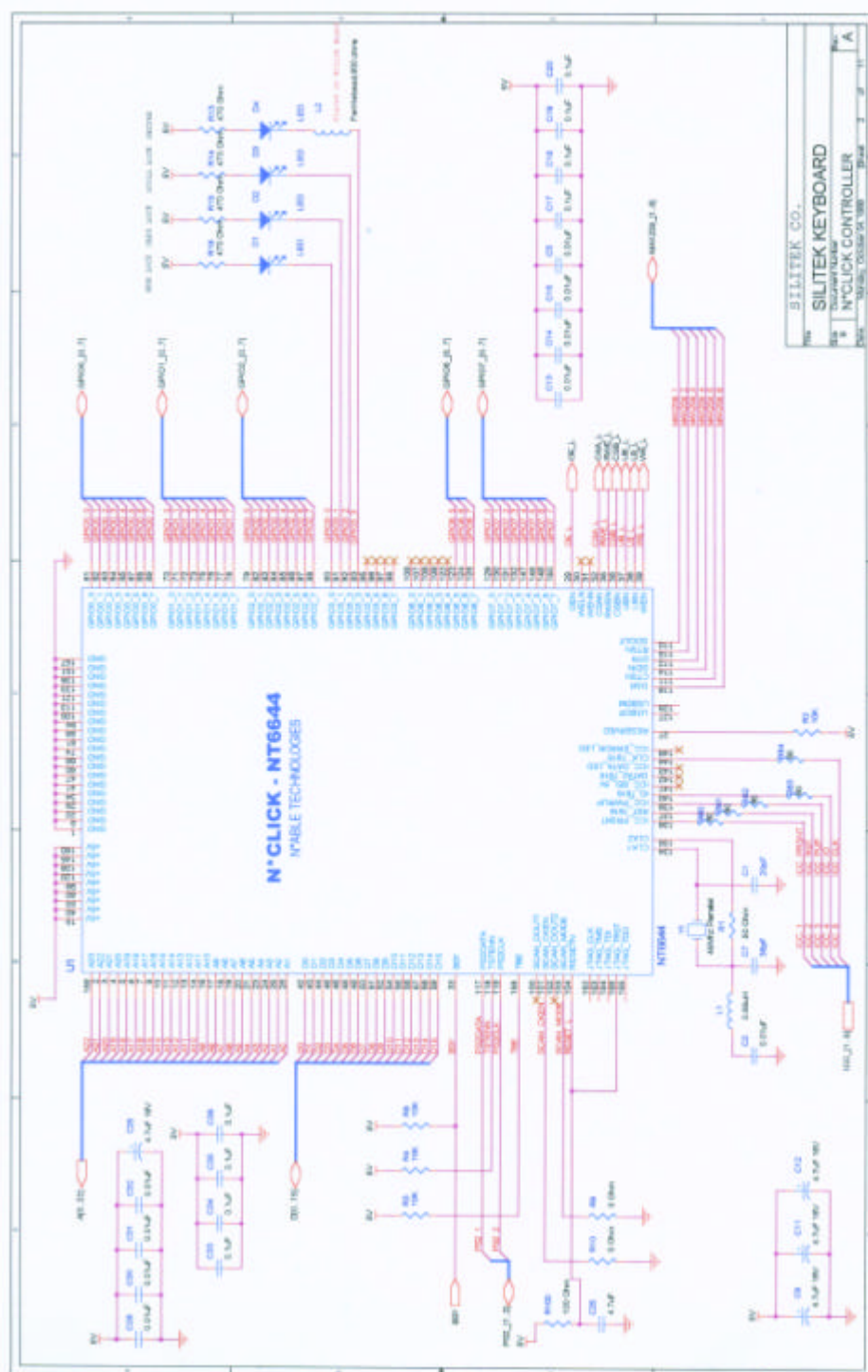


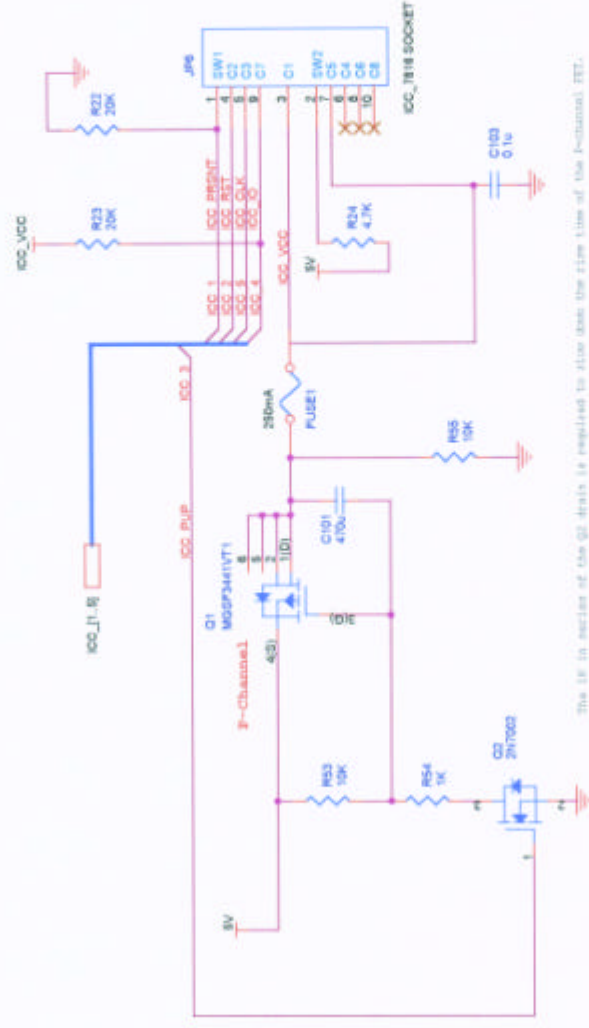






JUMPER CABLE DRAWING

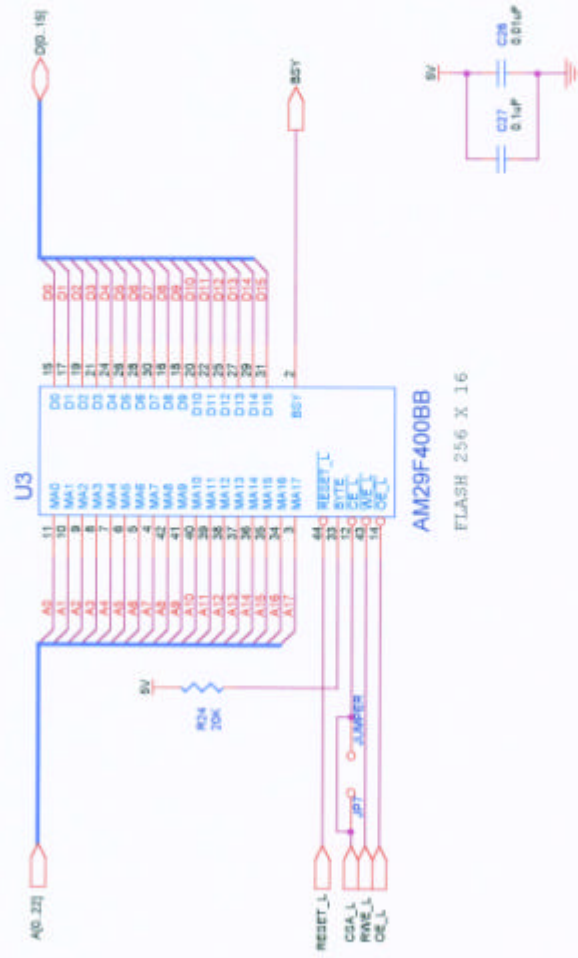
CARD INTERFACE



The IF in section of the Q1 drain is required to slow down the rise time of the P-channel FET.
 C101 is optional, and can be used to slow down the output further. C101 can be in the range of 47pF to 0.01uF.
 Rise time of the P-channel FET should be approximately 12uS with this circuit.

File	Silitek Co.
Size	SILITEK KEYBOARD
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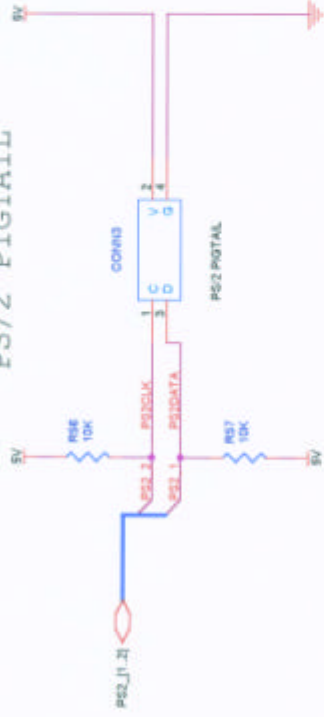
FLASH 256 x 16 MEMORY



File		Silitek Co.	
Size		Silitek Keyboard	
Document Number		FLASH 256x16 MEMORY	
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HOST INTERFACE

PS/2 PIGTAIL



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SILITEK KEYBOARD

Document Number

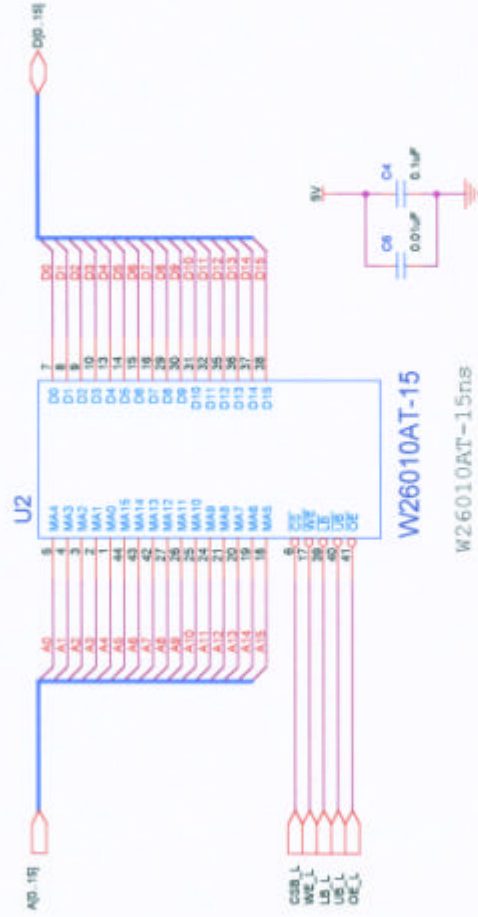
HOST INTERFACE

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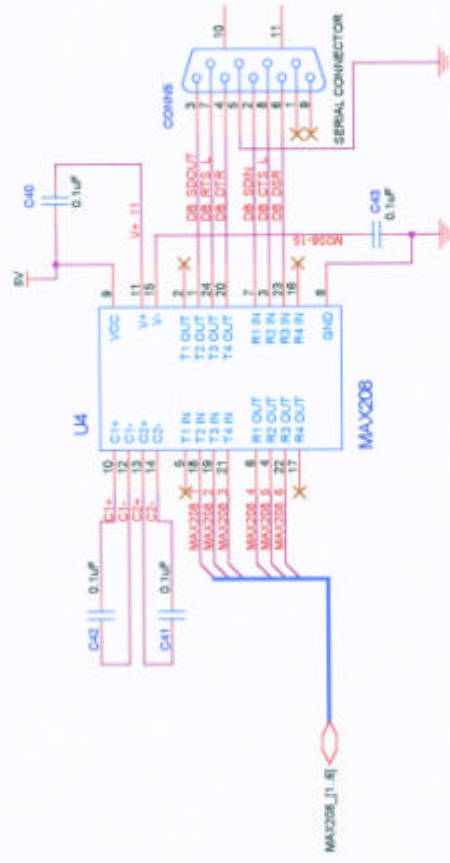
Rev: A

CMOS SRAM MEMORY



Silitek Co.			
SILITEK Serial-PS/2 Keyboard			
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SERIAL PORT



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SILITEK KEYBOARD

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