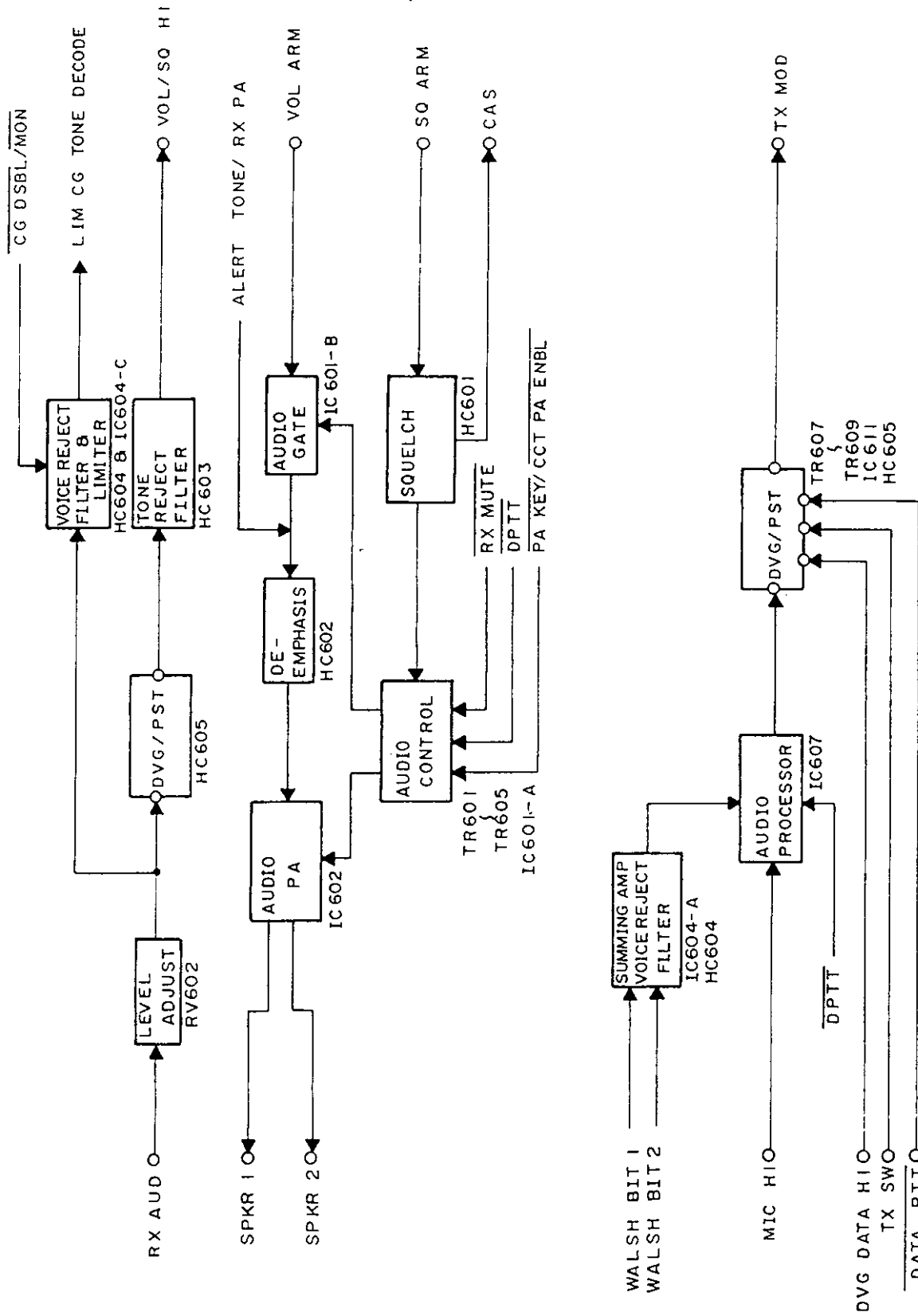
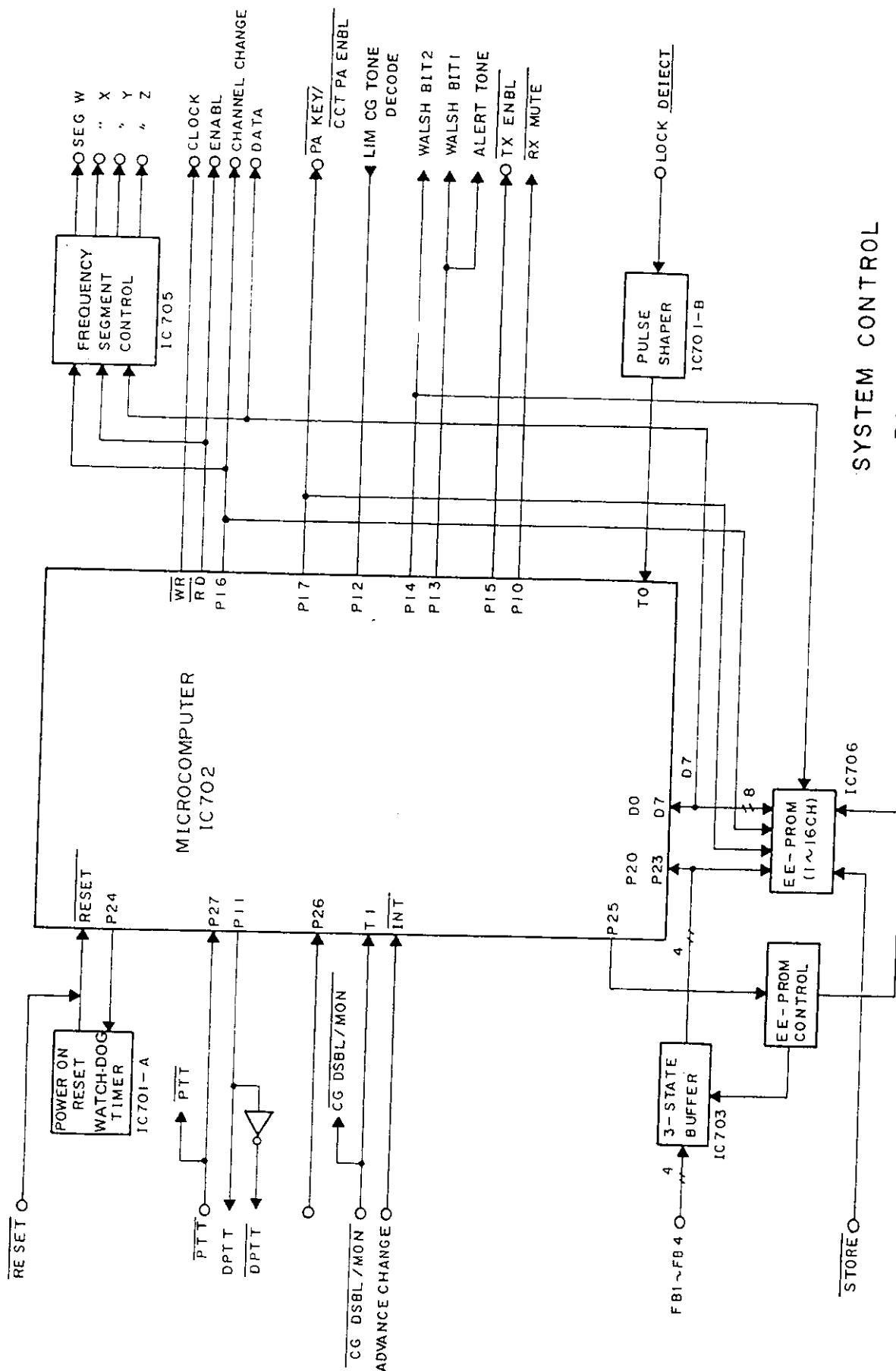




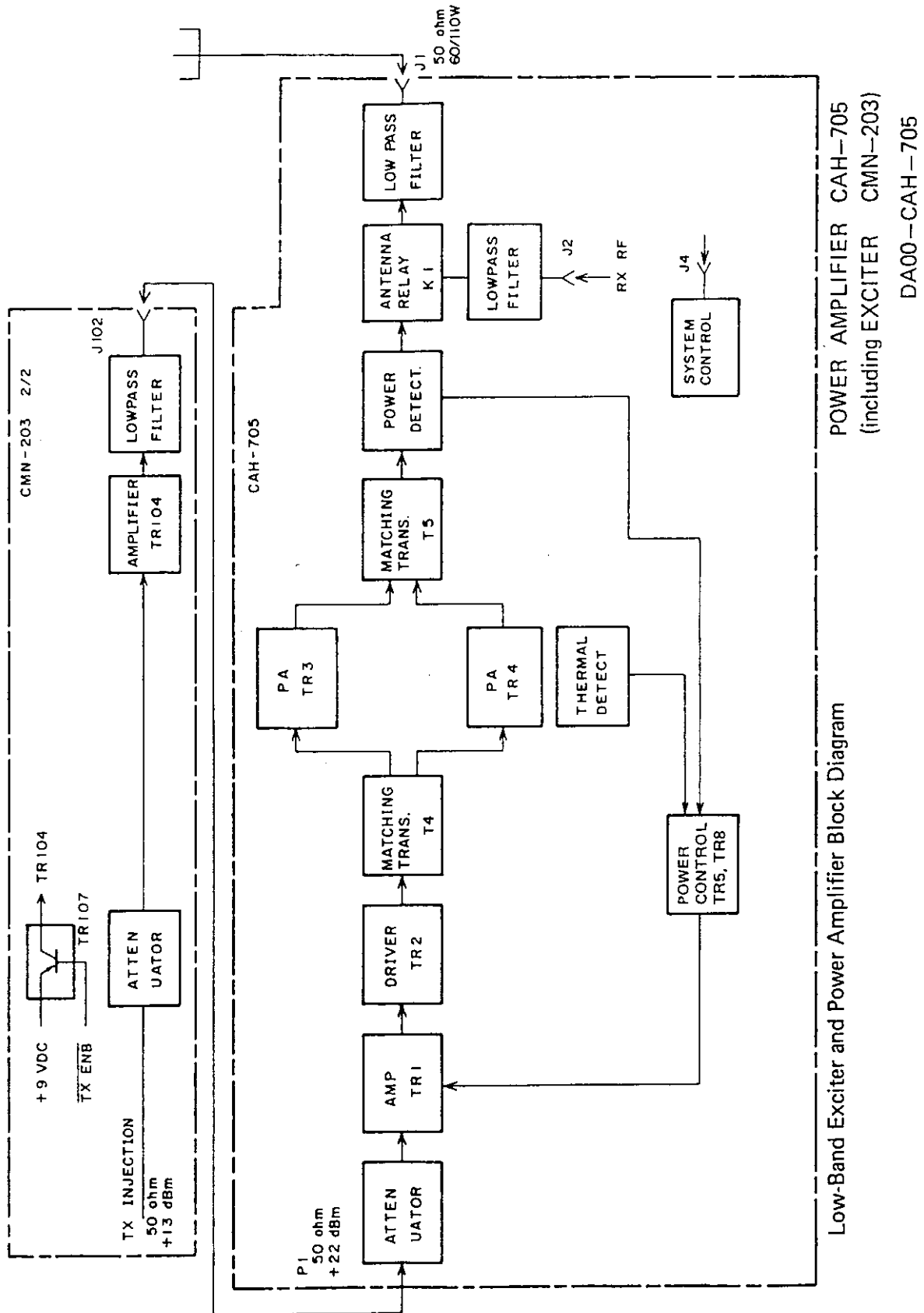
SYSTEM CONTROL
BLOCK DIAGRAM

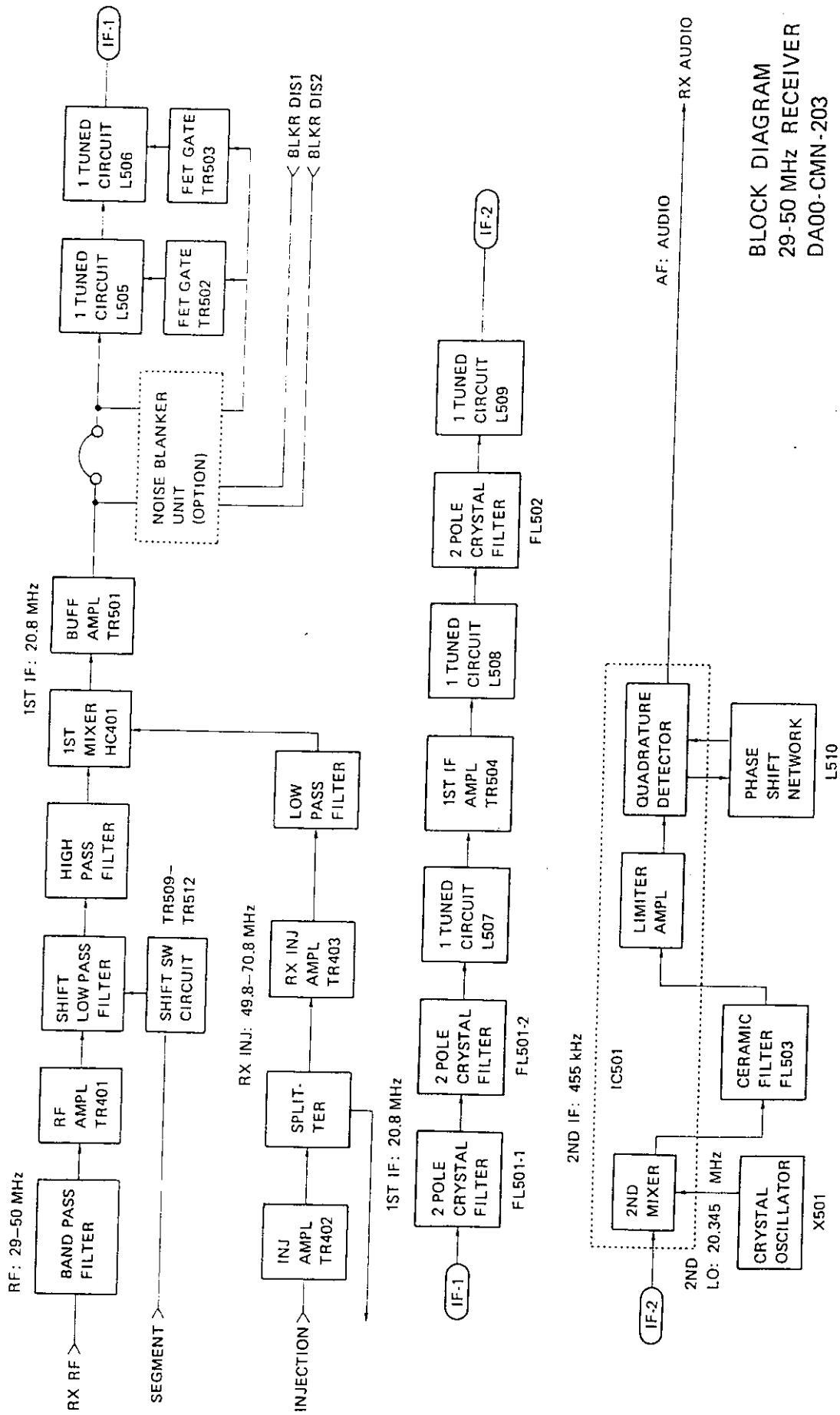
DA 00-CMC-486



SYSTEM CONTROL
BLOCK DIAGRAM

DA00-CMC-486 1/2





BLOCK DIAGRAM
29-50 MHz RECEIVER
DA00-CMN-203