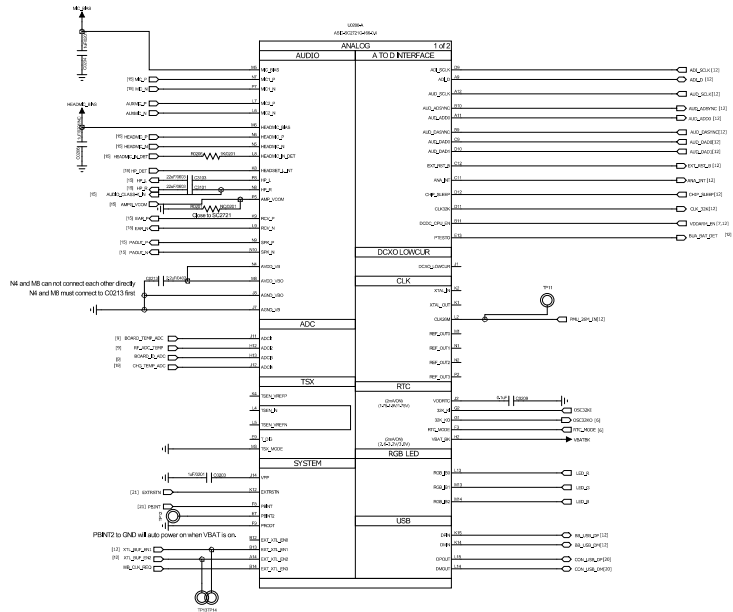
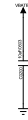


REV. HISTORY			
REV.	DESCRIPTION	DATE	BY

PMU_ANALOG



BKBT

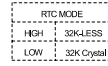


RTC



RTC Mode	Type	R0202	R0205	X0200	C0201	C0202	R0203	R0204
32K Crystal	Option	OR	NF	Crystal	8.2pF	8.2pF	OR	NF
32Kless	Default	NF	OR	NF	NF	NF	NF	10K

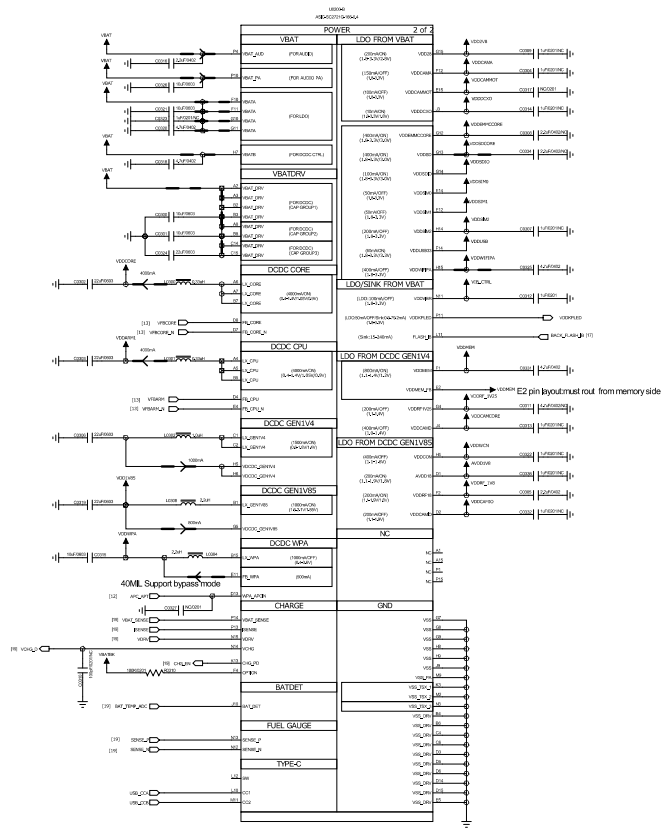
Note: 32Kless only use in TSX mode. 32K crystal is only use in TCXO mode.



REV: 001			
LTB	ESDNO	APPROVED	DATE

PMU_POWER

INPUT	Description
VBAT_AUD	VBAT power input for audio
VBAT_PA	VBAT power input for audio speaker PA
VBAT_LDO	VBAT power input for LDO
VBATB	VBAT power input for VBAT_LED, VBATBK, Flash LED
VBATC	VBAT power input for LDO
VBAT_DRY	VBAT power input for DCDC



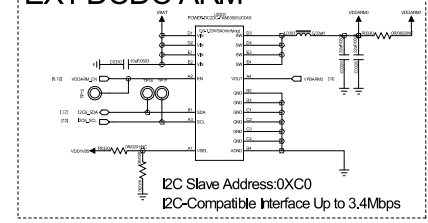
SC2721 DCDC output

Input Power	Power Name	Output Voltage (V)	Output Current	Default Voltage	Default
VBAT	DCDCCPU	0.4-1.4/1.05/0.9	4000mA	0.9V	ON
	DCDCCORE	0.4-1.4/1.05/0.9	4000mA	0.9V	ON
	DCDCGEN1V4	0.6-1.8	1500mA	1.4V	ON
	DCDCGEN1V85	1.6-2.1	1000mA	1.85V	ON
	DCDCWPA	0.4-3.6	1000mA		OFF

SC2721 LDO output

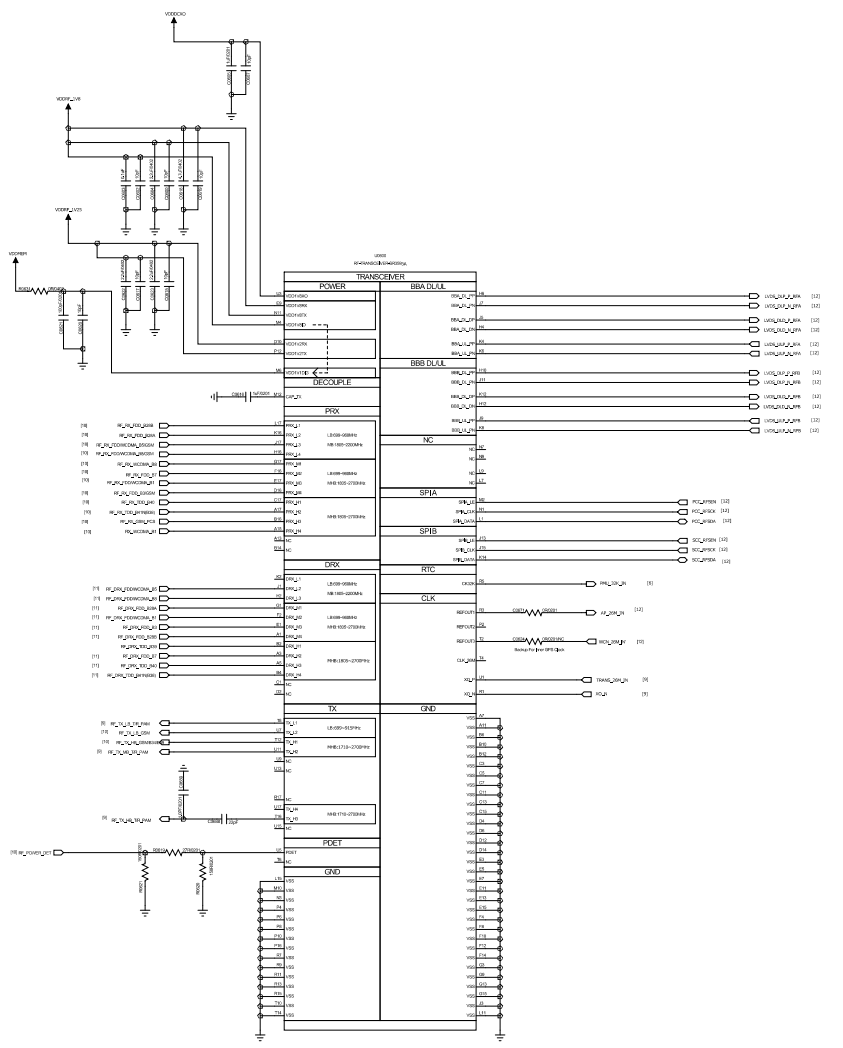
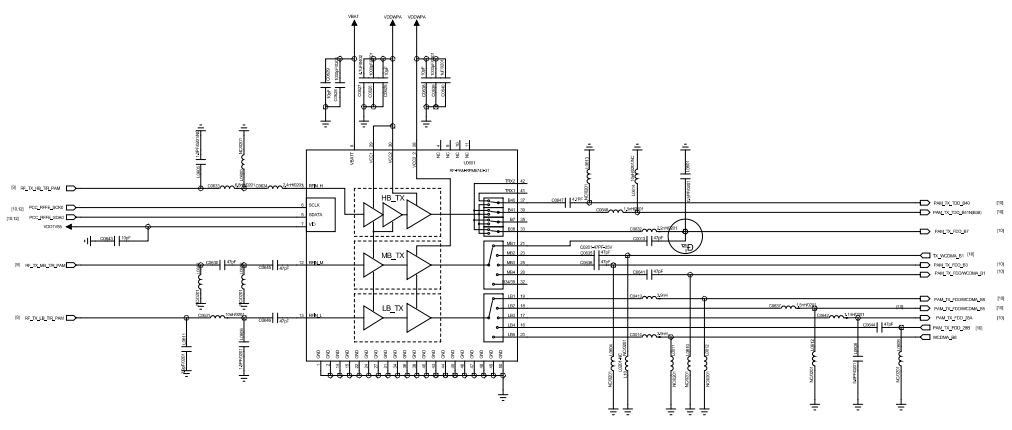
Input Power	Power Name	Output Voltage (V)	Output Current	Default Voltage	Default
LDO from VBAT	VDDCAMA	1.8-3.3	150mA	2.8V	OFF
	VDDCMMOT	1.8-3.3	100mA	2.8V	OFF
	VDDSIM0	1.8-3.3	50mA	1.8V	OFF
	VDDSIM1	1.8-3.3	50mA	1.8V	OFF
	VDDSIM2	1.8-3.3	200mA	1.8V	OFF
	VDD28	1.8-3.3	200mA	2.8V	ON
	VDDMMCCORE	1.8-3.3	400mA	3.0V	ON
	VDDSDCORE	1.8-3.3	400mA	3.0V	ON
	VDDSDIO	1.8-3.3	100mA	3.0V	ON
	VBATBK	2.6-3.2	2mA	3.0V	ON
	VDDVB	1.8-3.3	100mA	3.3V	OFF
	VDDKPLED	1.8-3.3	LDO 50mA/Sink 0.9-79.2mA		
	VDDWFIPA	1.8-3.3	400mA	3.3V	OFF
	VDDDCXO	1.8-3.3	10mA	1.8V	ON
	VDDUSB33	1.8-3.3	60mA	3.3V	ON
LDO from DCDC GEN1V4	VDDCAMD	1.0-1.4	400mA		OFF
	VDDRF1V25	1.1-1.4	200mA		OFF
	VDD_MEM	1.1-1.4	800mA	1.2V	ON
LDO from DCDC GEN1V85	VDDCAMO	1.1-1.9	200mA		OFF
	AVDD18	1.1-1.9	200mA	1.8V	ON
	VDDRF18	1.1-1.9	200mA	1.8V	ON
	VDD_CON	1.1-1.6	400mA		OFF

EXT DCDC ARM

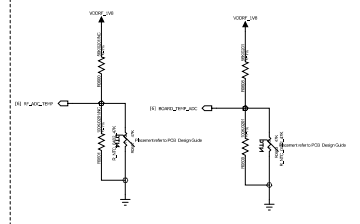


REV. RECORD			
REV.	REASON	APPROVED	DATE

LTE/WCDMA PAM



NTC

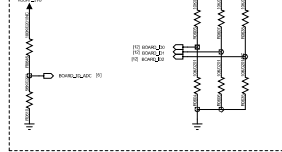


BoardID0	
Clock Mode	TSX
TCXO +32K	1

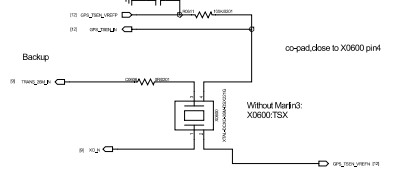
BoardID1	
LPDDR3	0
LPDDR4X	1

BoardID2 Backup	
-----------------	--

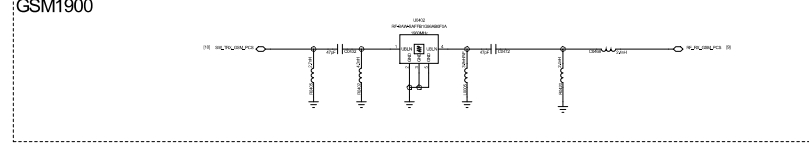
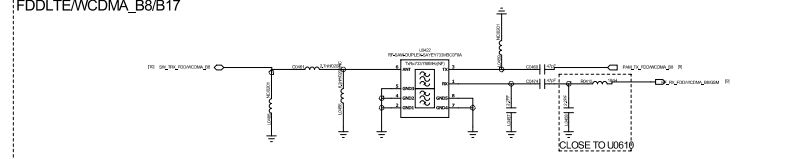
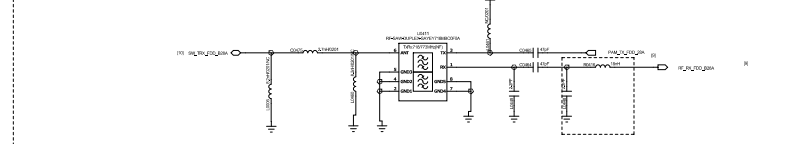
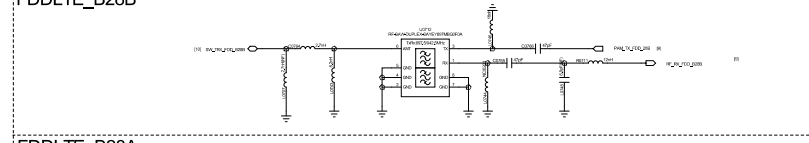
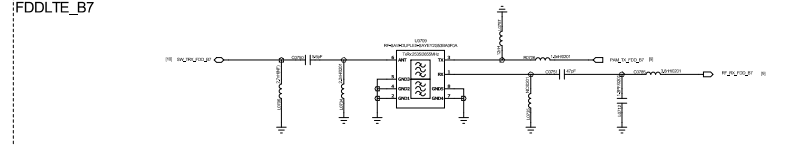
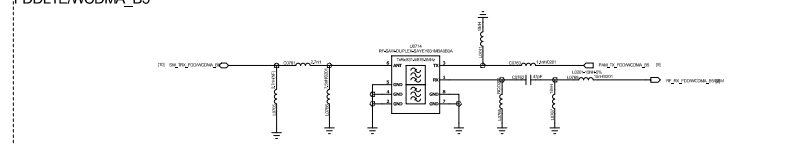
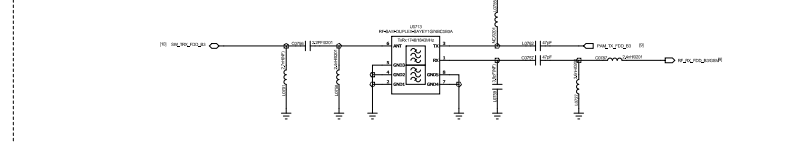
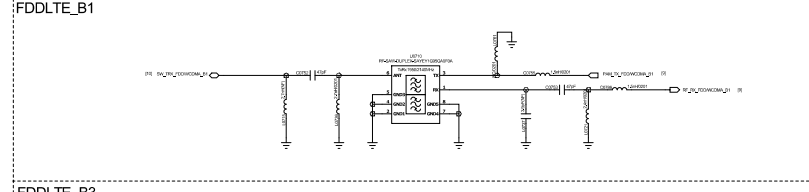
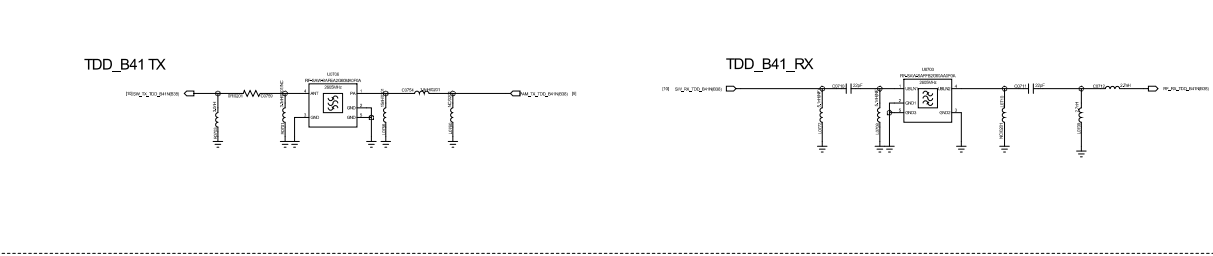
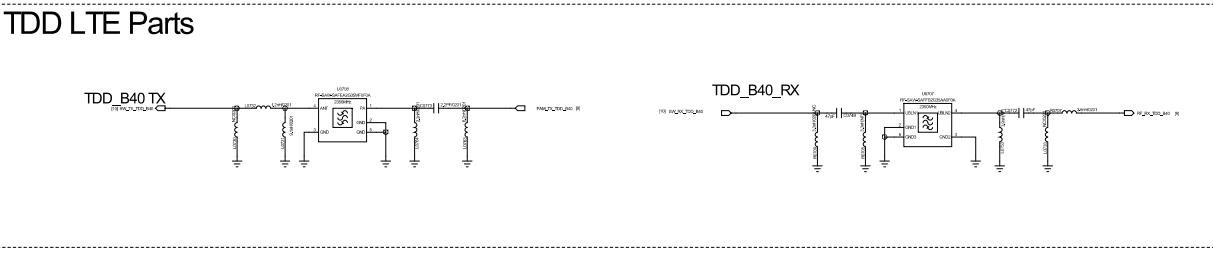
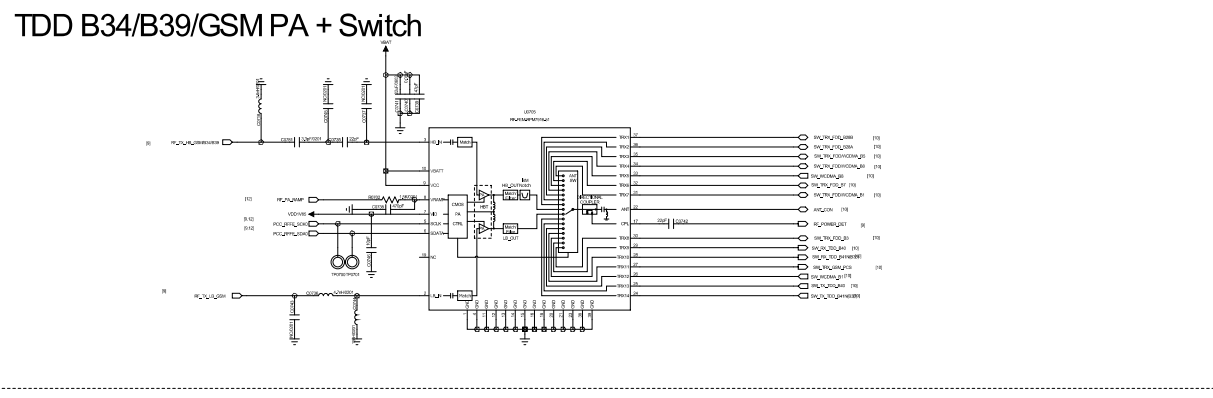
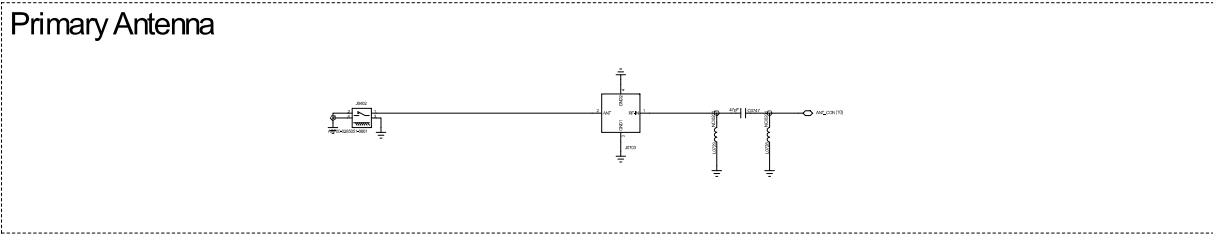
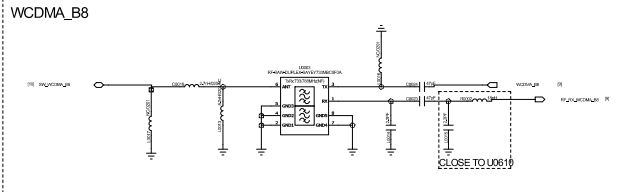
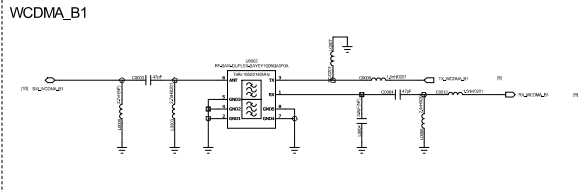
Band info



DCXO/TSX 26M

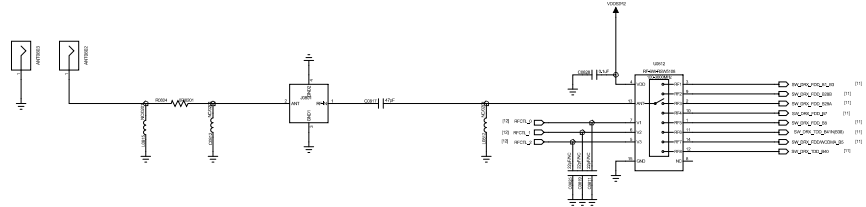


REV	DATE	APPROVED	DATE

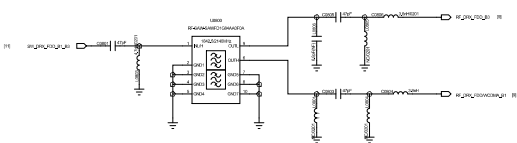


REV	DATE	APPROVED	DATE

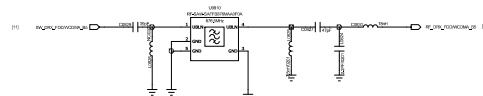
Diversity LTE Path



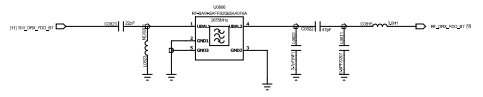
FDD_B1-B3_Diversity RX path



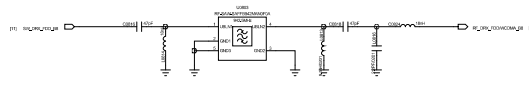
FDD_B5_Diversity RX path



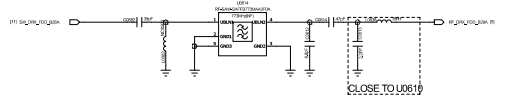
FDD_B7_Diversity Rx path



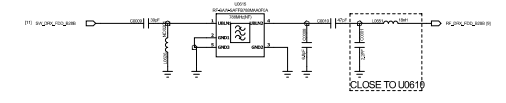
FDD_B8_Diversity RX path



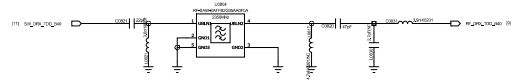
FDD_B28A_Diversity RX path



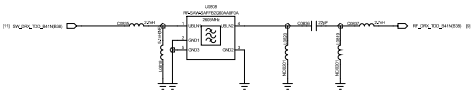
FDD_B28B_Diversity RX path



TDD_B40_Diversity RX path

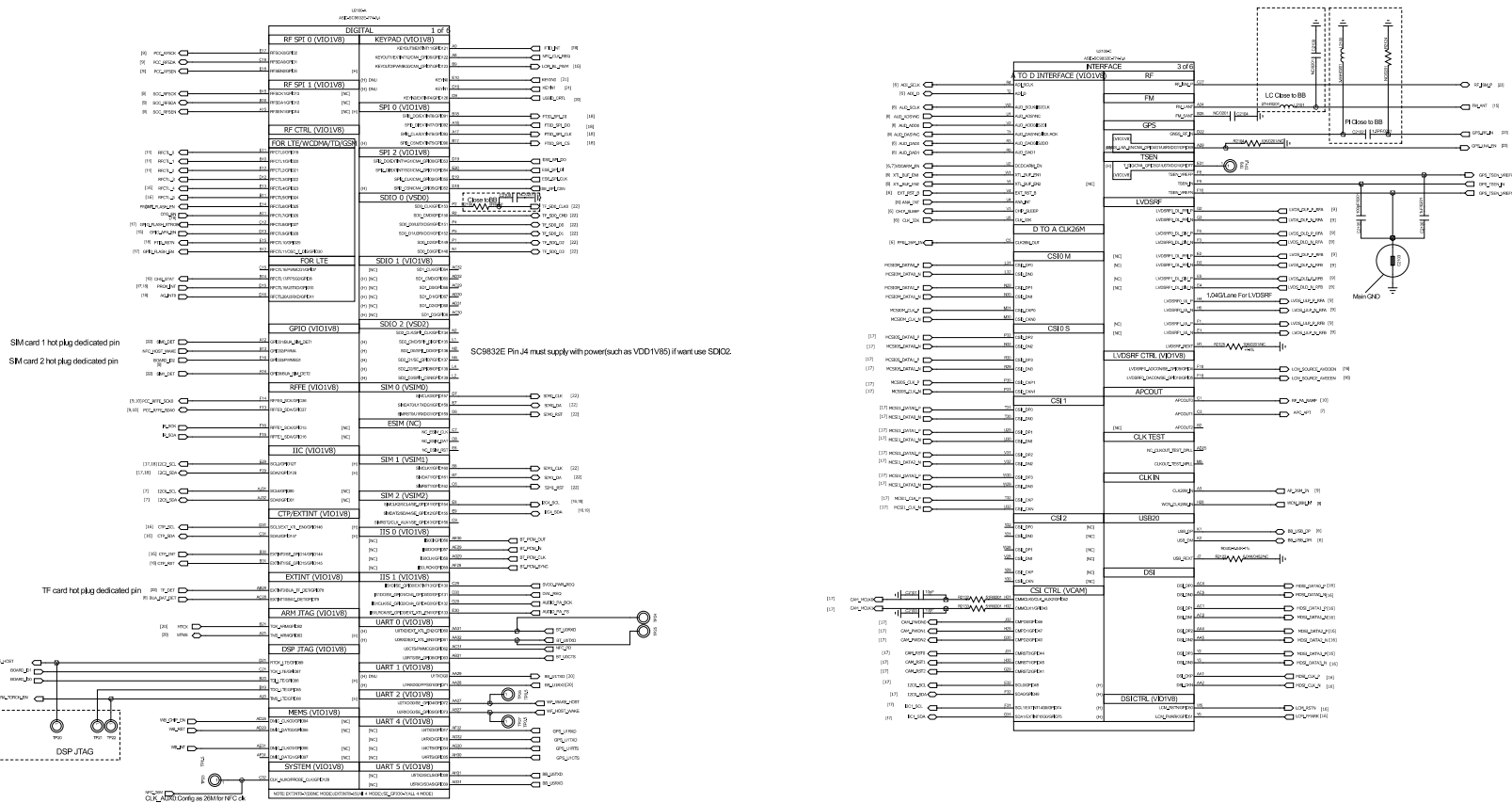


TDD_B41_Diversity RX path



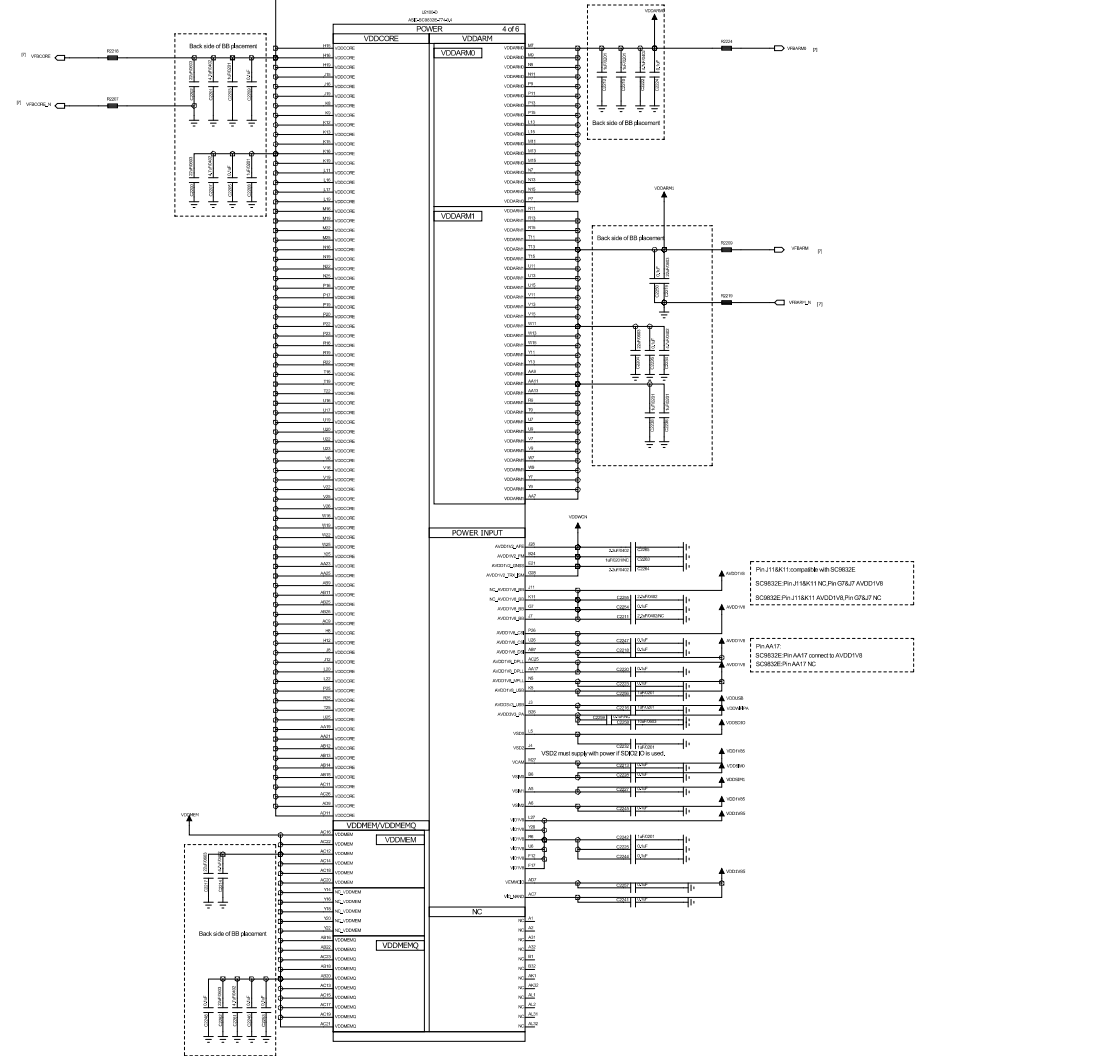
REV	DATE	APPROVED	DATE

DIGITAL

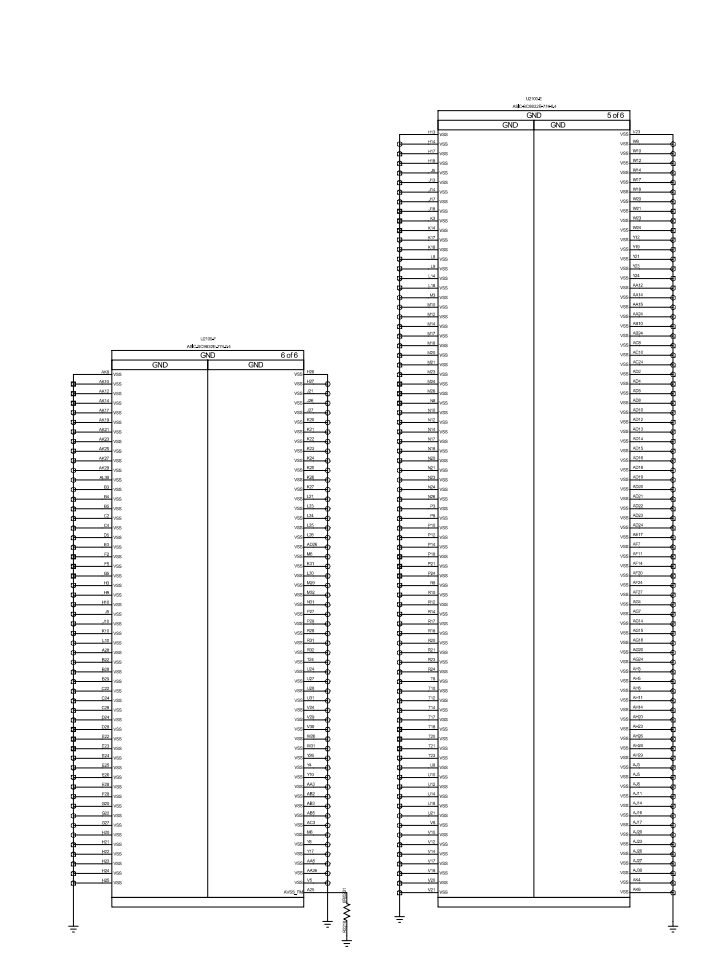


REV: 00000000			
VER	EDWG	APPROVED	DATE

POWER

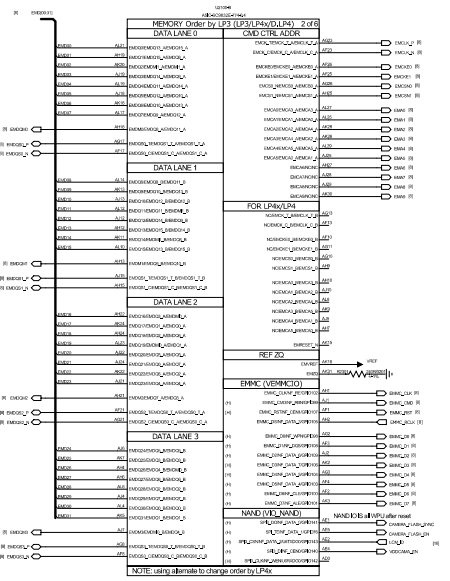


GND



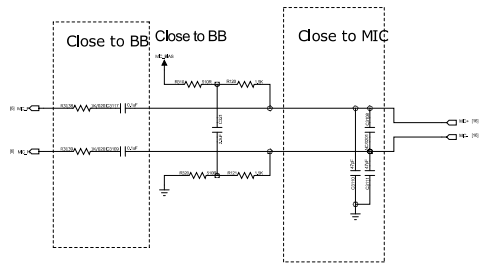
MEMORANDUM			
DATE	BY	APPROVED	DATE

MEMORY

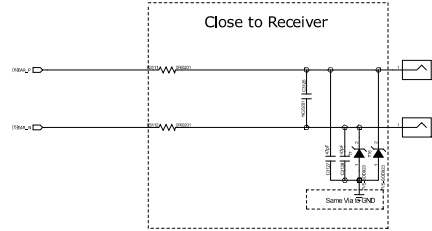


REGISTRATION			
DATE	DESIGN	APPROVED	DATE

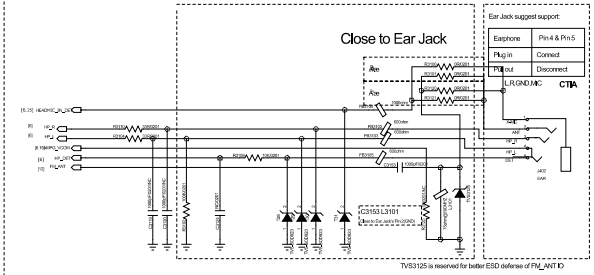
MAIN MIC



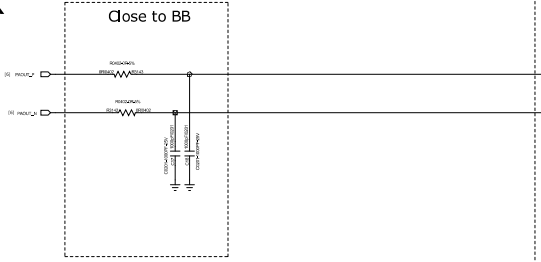
RECEIVER



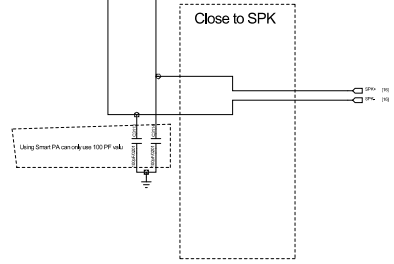
3.5mm Ear Jack



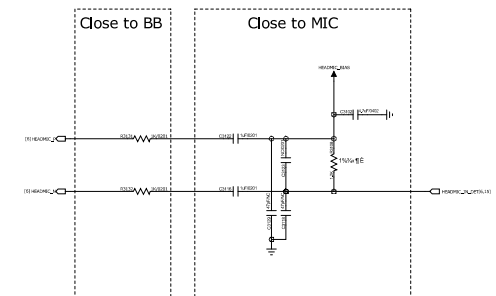
interior SPK



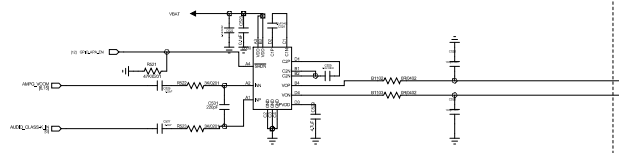
SPEAKER



HEAD MIC

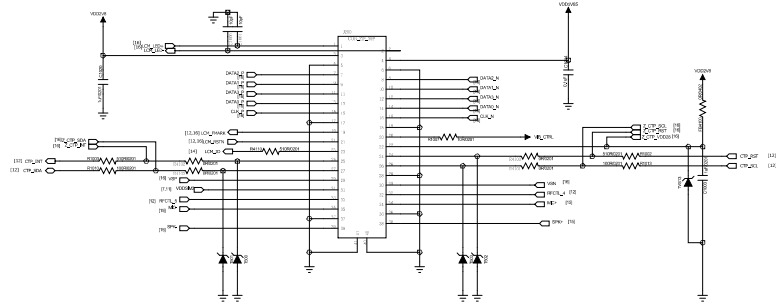


CLASS K PA

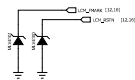
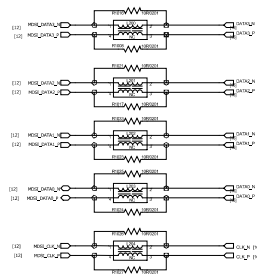


REV	REVNO	APPROVAL	DATE

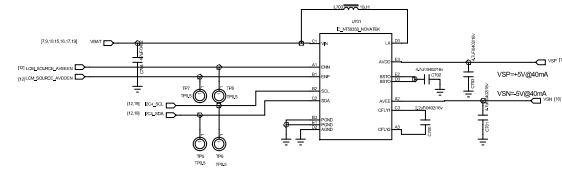
5.5" FHD LCM(4lane MIPI)



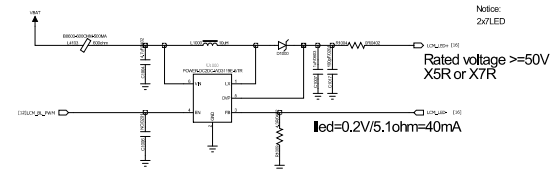
EMI



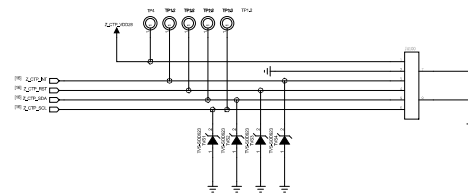
DCDC 3POWER



Back Light Driver

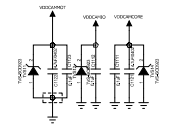
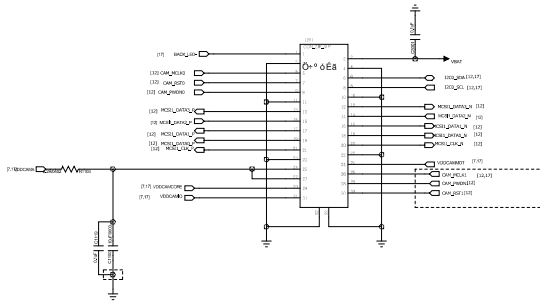


CTP

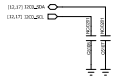


REVISIONS			
DATE	DESCRIPTION	APPROVED	DATE

Rear Dual Camera 0:16M

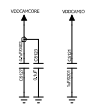
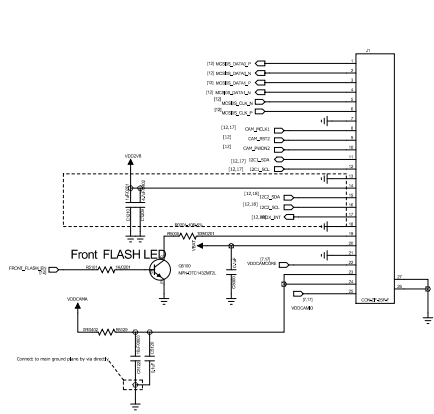


For diverse debug use, please close to camera

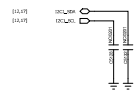


Rear Dual Camera 1:5M

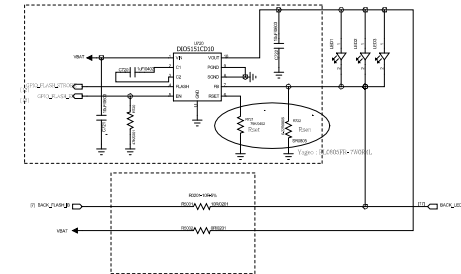
Front Camera:8M



Connect to rear ground for use flash



Back FLASH LED

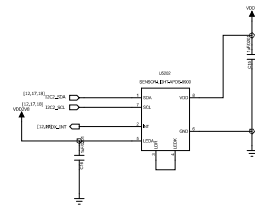


ICM-20600			
DATE	DESIGN	APPROVAL	DATE

Accelerometer&GYRO: ICM-20600

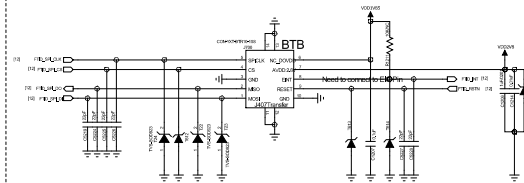
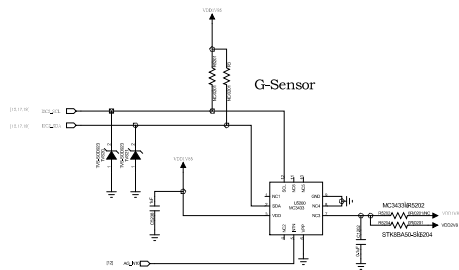
ALS & PROX

InfraRed Sensor



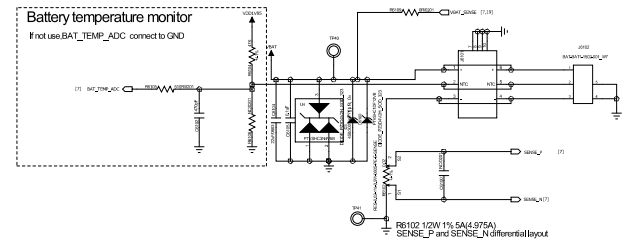
G Sensor

FingerPrint ID

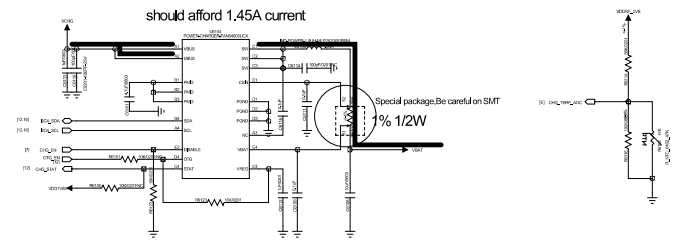


REVISION			
ITS	EQNS	APPROVED	DATE

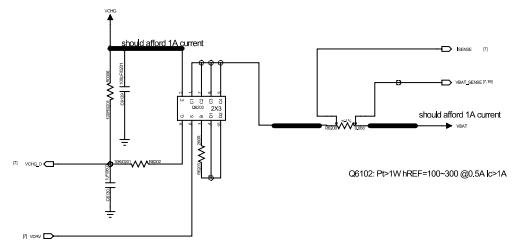
BATTERY



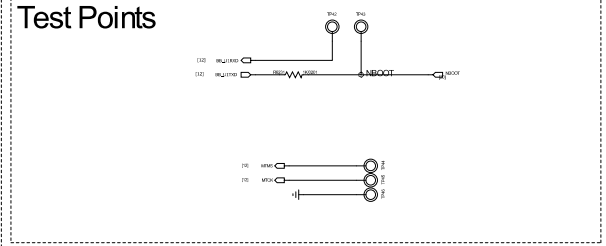
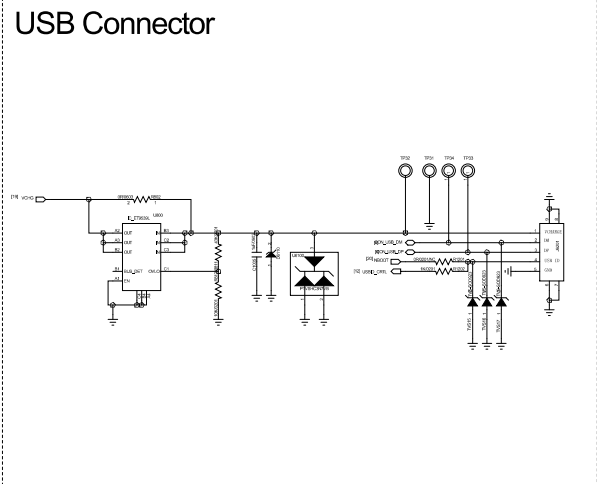
Switching charger



CHARGE

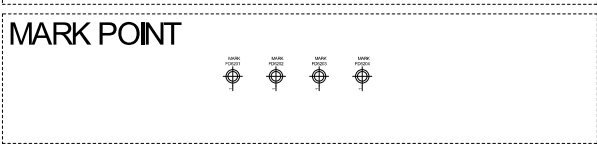


REVISIONS			
REV.	DATE	APPROVED	DATE



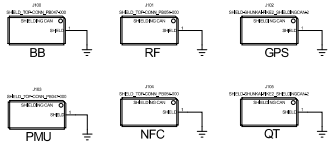
SharkL3 strapping pin

Ball name	Signal name in boot code	Value	Description	Default value
UTXD	USB_ULOAD_EN	0	USB development mode	1 (e1) (epd)
ADL_DAD1	ARM_BOOT_MD0	000	Reserved	111 (e1) (epd)
		001	TLC NAND Toggle	
ADL_DAD0	ARM_BOOT_MD1	010	M.CS/C.NAND Flash (CFE) 2	000 (e1) (epd)
		011	SD boot (in the out)	
		100	M.CS/C.NAND Flash Toggle	
ADL_SCLK	ARM_BOOT_MD2	001	TLC NAND Flash Toggle	000 (e1) (epd)
		100	TLC NAND Flash Toggle	
		110	Reserved	
		111	MMC boot	



REVISIONS			
REV	REASON	APPROVED	DATE

SHIELDING

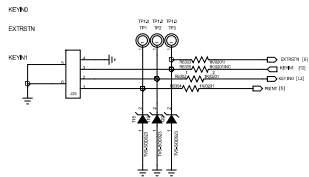


Support two keys 7S reset ,also support single key(PBNT) 7S reset with SC2721
 Two keys Reset: EXTRSTN+PBNT

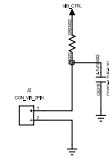
KEYPAD LED(Reserved)

SOS+Volume KEY

Volume- Key,Location:Down
 Volume+ Key,Location:Up
 SOS Key

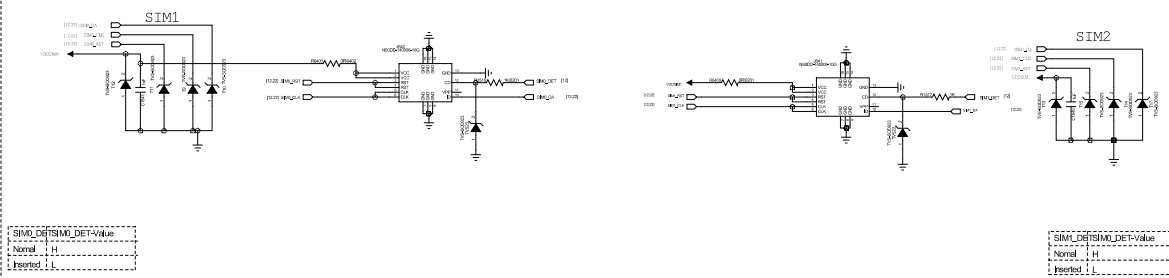


VIB

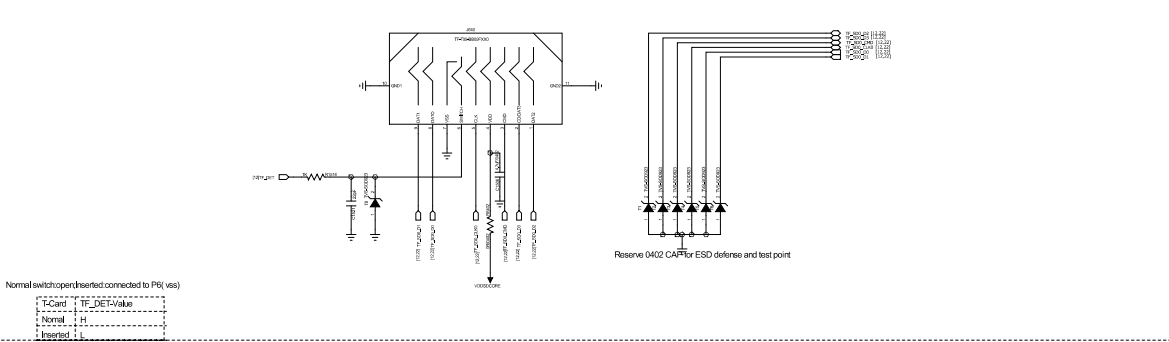


REV	ECNO	APPX/EC	DATE

SIM1 & SIM2

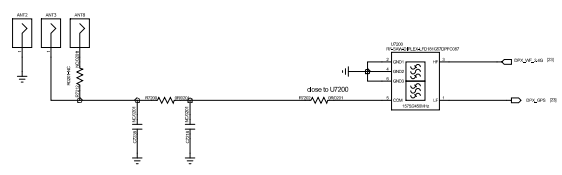


T_Flash

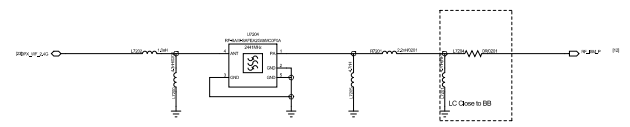


REVISIONS			
REV	COMD	APPD/DC	DATE

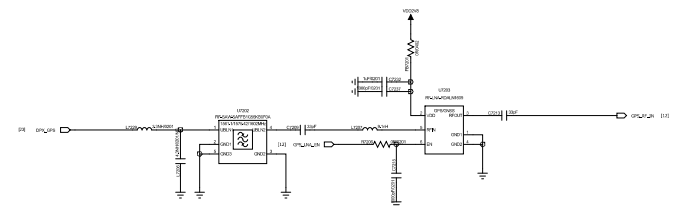
WiFi&BT&GPS ANT



WiFi/BT SAW



GPS SAW LNA



REV	DESCRIPTION	DATE	BY

