

This USGv2 is designed to be a standalone device with RF interface to CATV cable network and Ethernet interface to CPE equipments such as PC or LAN devices.

1. CPU, U101, BCM3383Z is to support DOCSIS3.0 function, which have a 1600DMIPS VIPER processor core, scalable clock rates to 800MHz. It needs an external 54MHz crystal of reference. The DOCSIS3.0 core provides an integrated downstream demodulator supporting rates up to 1024 QAM, an upstream modulator supporting rates up to 256QAM, a DOCSIS3.0 MAC with 16 upstream queues, a transmission convergence module which supports mapping for both SCDMA and ATDMA, and a 1GHZ QAM tuner. A complete set of interfaces including an integrated 10/100/1000 Ethernet MAC/PHY.
 2. Ethernet, U1001, BCM53124S is to support - 4 Gigabit Ethernet LAN interfaces (with advanced routing capability)
 3. SLIC, U701, Si32392 is to support Dual Ringing SLIC device is a dual-channel device optimized to provide battery feed, ringing, and supervision on voice loops found in short-loop VoIP applications.
 4. Wireless function 2.4G: BCM4331 support 802.11b/g/n 3x3 internal antenna.
 5. Wireless function 5G: BCM4331 support 802.11a/n 3x3 internal antenna.
 6. Internal power supply to transfer 120Vac to 12Vdc
 7. Battery, 2S2P 4400mha which can support 8 hours standby time and 5 hours talk time when AC power fail
 8. DECT 6.0, U2301, SC14441B is to support CAT-iq 2.0/DECT 6.0 hardware.
 9. MoCA2.0, U1401, EN2710 is to support MoCA2.0 capability to permit interconnection between the USGv2 and other MoCA enabled devices
7. Application Scenario



Universal Services Gateway

