

Notes: Unless Otherwise Stated

Scheme Spec:

FLASH: MLC, 3.3V
DRAM: DDR2/3 1.8V /1.5V
Key: NEXT, PREV, Vol+, Vol-, UP, DOWN, ENTER, UBOOT
Power: DCIN, 5V, 2A; BAT, 3.7V, 3600mAH
USB0: OTG
USB1: HOST
USB2: HOST
WIFI: SDIO WIFI
Card: TFcard*2
Other: Headphone, MIC, G-Sensor, camera

Power Supply:

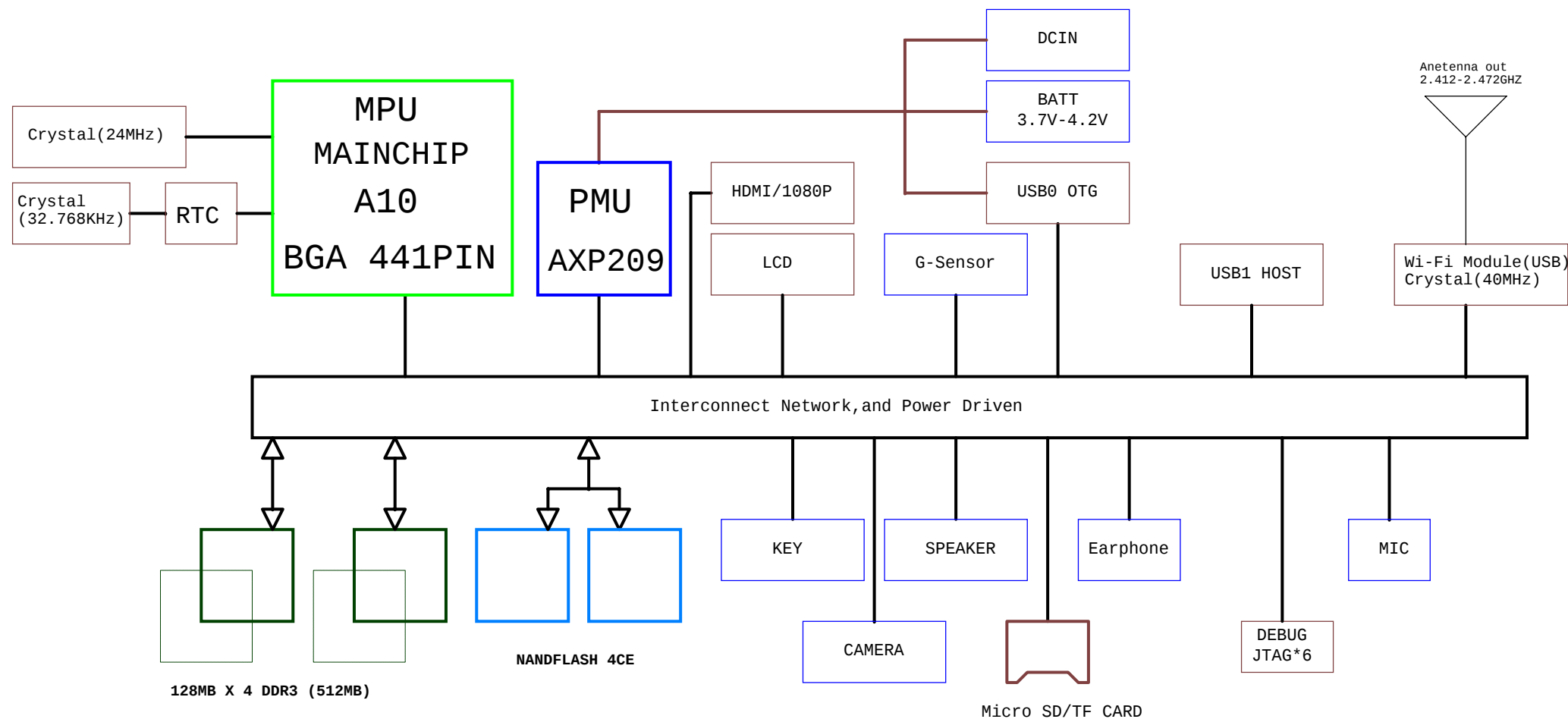
AXP209 DCDC2	1.25V	1600mA	CPU
AXP209 DCDC3	1.2V	1200mA	CORE
AXP209 LD01	1.3V	30mA	RTC
AXP209 LD02	3V	200mA	AVCC
AXP209 LD03	2.8V	400mA	CSI0-IO
AXP209 LD04	3.3V	200mA	CSI1-IO
SY8008B DCDC	1.8V	1000mA	CSI-CORE
SY8008B DCDC	1.5V/1.8V	1000mA	DRAM
SY8008B DCDC	3.3V	1000mA	VCC/LCD/NAND//WIFI
SY7208	5V	2000mA	HDMI/USB
AP1231B28ZRM	2.8V	300mA	CSI0-AF-VCC
AP1231B12ZRM	1.2V	300mA	WIFI

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Rev	Description	Date	Drawn	Checked	Approved
PAD_MAINCHIP_STD_V1.15		2011-07-12	Leo		
PAD_MAINCHIP_STD_V1.15		2011-07-18	Leo		
PAD_MAINCHIP_STD_V1.16		2011-07-25	Leo		

BLOCK

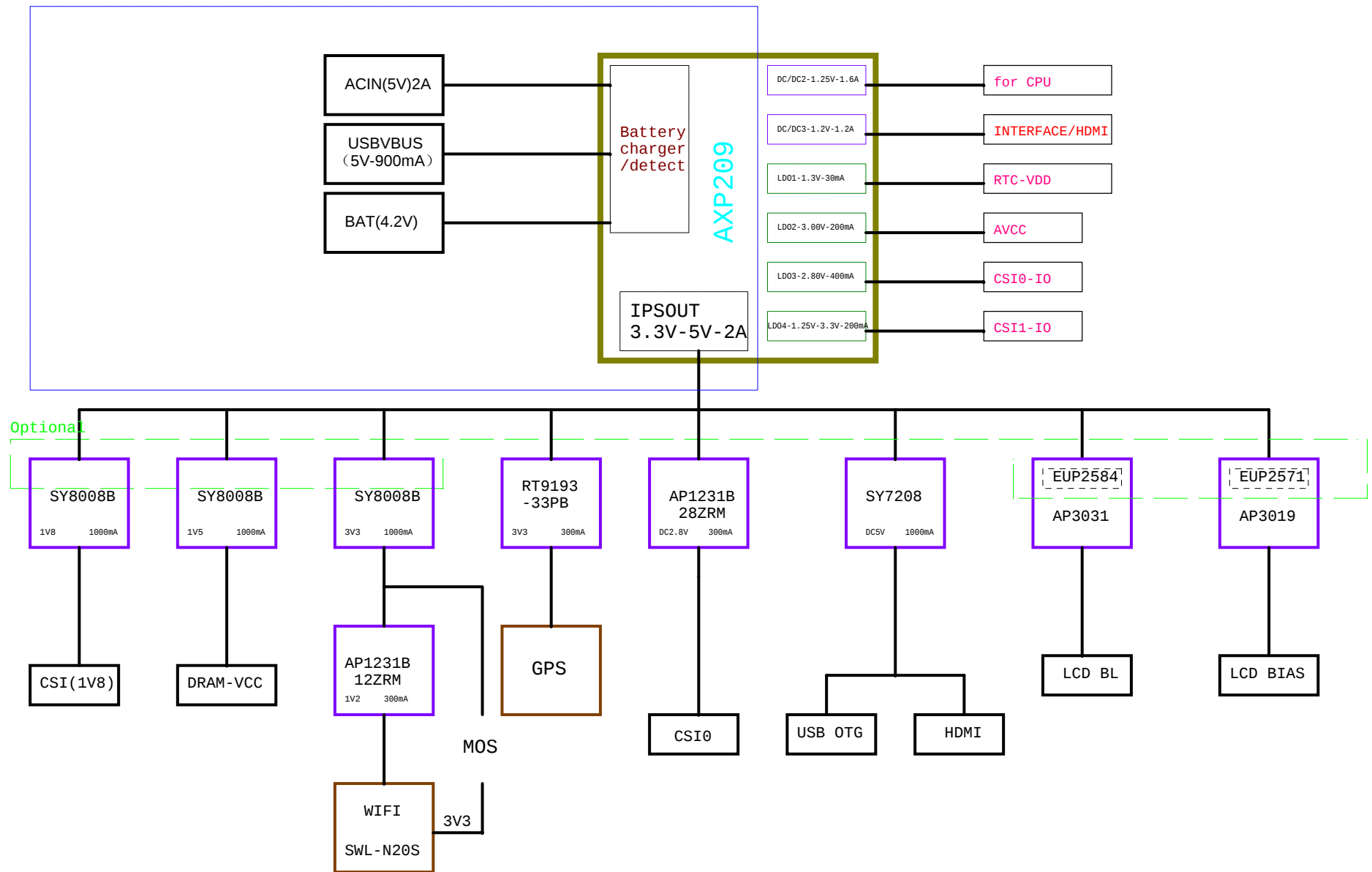


MAINCHIP_PAD_DDR3		
BLOCK		
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PIO ASSIGNMENT

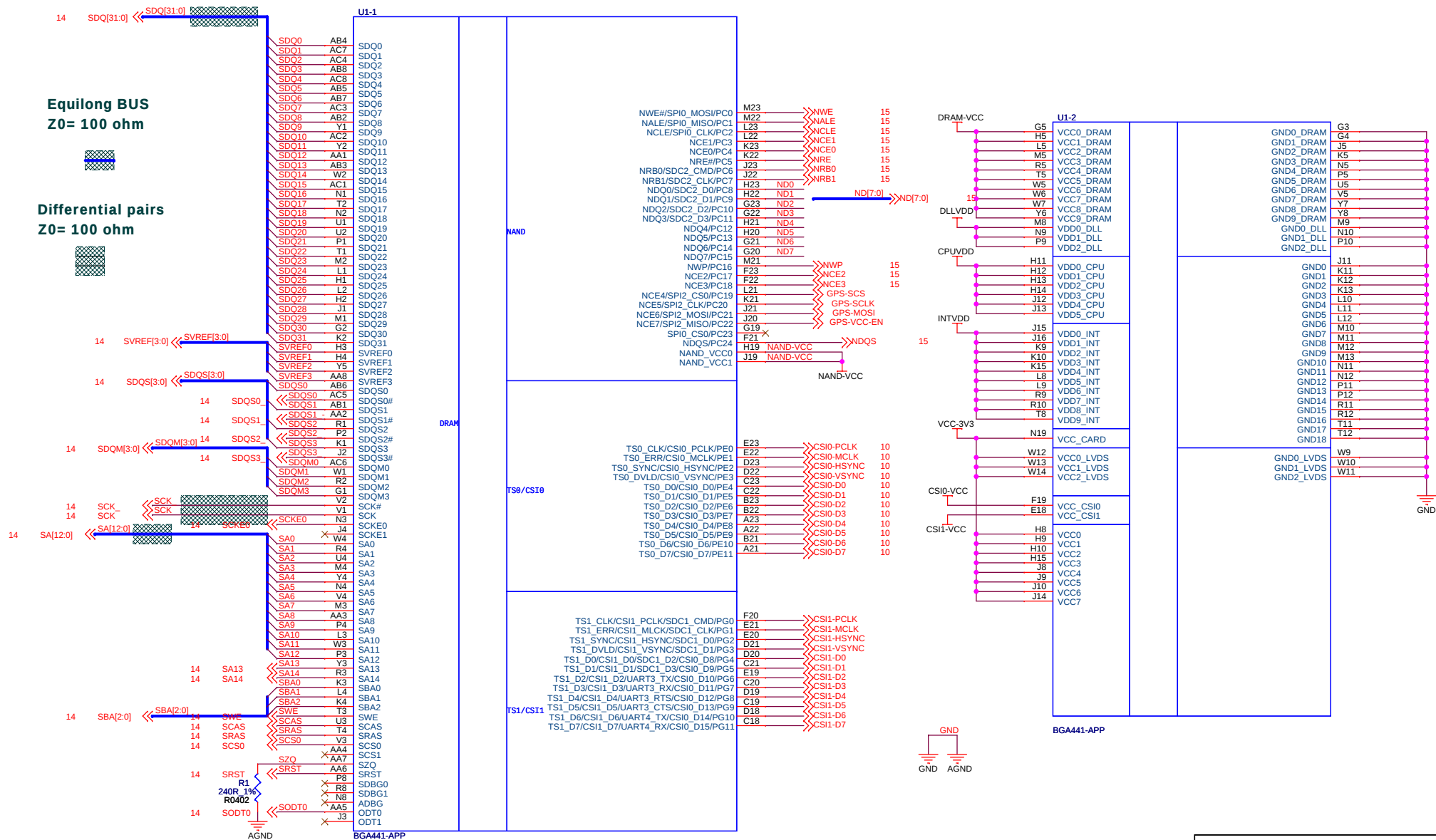
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PA(18)	PA0	ERXD3	EMAC	PC(25)	PC0	NWE#	NAND	PD(28)	PD18	LCD0_D18	LCD	PH(28)	PH0	EINT0	GS-INT1	PI(22)		PI15	GPIO_OUT	GPS-RX-EN	
	PA1	ERXD2			PC1	NALE			PD19	LCD0_D19			PH1	GPIO_IN	SD0-DET#		PI16	UART2_RTS	BT-UART-RTS		
	PA2	ERXD1			PC2	NCLE			PD20	LCD0_D20			PH2	GPIO_IN	SD1-DET#		PI17	UART2_CTS	BT-UART-CTS		
	PA3	ERXD0			PC3	NCE1			PD21	LCD0_D21			PH3	GPIO_OUT	USB2-DRV		PI18	UART2_TX	BT-UART-TX		
	PA4	ETXD3			PC4	NCE0			PD22	LCD0_D22			PH4	GPIO_IN	USB0-IDDET		PI19	UART2_RX	BT-UART-RX		
	PA5	ETXD2			PC5	NRE#			PD23	LCD0_D23			PH5	GPIO_IN	USB0-VBUSDET		PI20	GPIO_OUT	BT-GPIO0		
	PA6	ETXD1			PC6	NRB0			PD24	LCD0_CLK			PH6	GPIO_OUT	USB1-DRV		PI21	GPIO_OUT	BT-GPIO1		
	PA7	ETXD0			PC7	NRB1			PD25	LCD0_DE			PH7	GPIO_OUT	LCD-BL-EN						
	PA8	ERXCK			PC8	NDQ0			PD26	LCD0_HSYNC			PH8	GPIO_OUT	LCD-PWR						
	PA9	ERXERR			PC9	NDQ1			PD27	LCD0_VSYNC			PH9	GPIO_OUT	WIFI-SHDN#						
	PA10	ERXDV			PC10	NDQ2		PE(12)	PE0	CSI0_PCLK	CSI0		PH10	GPIO_OUT	WIFI-HOST WAKEUP						
	PA11	EMDC			PC11	NDQ3			PE1	CSI0_MCLK			PH11	GPIO_OUT	WIFI-VDD-EN						
	PA12	EMDIO			PC12	NDQ4			PE2	CSI0_HSYNC			PH12	GPIO_OUT	WIFI-VCC-EN						
	PA13	ETXEN			PC13	NDQ5			PE3	CSI0_VSYNC			PH13	GPIO_OUT	CSI0-RESET#						
	PA14	ETXCK			PC14	NDQ6			PE4	CSI0_D0			PH14	GPIO_OUT	CSI1-RESET#						
	PA15	ECRS			PC15	NDQ7			PE5	CSI0_D1			PH15	GPIO_OUT	PA-SHDN#						
	PA16	ECOL			PC16	NwP			PE6	CSI0_D2			PH16	GPIO_OUT	CSI0-1V8-EN						
PA17	GPIO_OUT	E-RST	PC17		NCE2	PE7			CSI0_D3	PH17			GPIO_OUT	CSI1-1V8-EN							
PB(24)	PB0	TWI0_SCK	PMU		PC18	NCE3		GPS-SCS GPS-SCLK GPS-MOSI GPS-VCC-EN	PF(6)	PF0	SDC0_D1		SDC0	PI(22)	PI0		GPS_CLK	GPS			
	PB1	TWI0_SDA			PC19	SPI2_CS				PF1	SDC0_D0				PI1		GPS_SIGN				
	PB2	PWM0			PWM	PC20				SPI2_SCLK	PF2				SDC0_CLK		PI2		GPS_MAG		
	PB3	GPIO_OUT			CP-RST	PC21				SPI2_MOSI	PF3				SDC0_CMD		PI3	PWM1	WIFI		
	PB4	IR0_RX	IR		PC22	GPIO_OUT				PF4	SDC0_D3				PI4	SDC3_CMD					
	PB5	GPIO_OUT	BT-RST		PC23	NC				PF5	SDC0_D2				PI5	SDC3_CLK					
	PB6	I2S_BCLK	BT-PCM-CLK		PC24	NDQS			PG0	CSI1_PCLK	CSI1		PI6		SDC3_D0						
	PB7	I2S_LRCK	BT-PCM-SYNC	PD0	LCD0_D0	LCD		PG1	CSI1_MCLK	PI7			SDC3_D1								
	PB8	I2S_D00	BT-PCM-OUT	PD1	LCD0_D1			PG2	CSI1_HSYNC	PI8			SDC3_D2								
	PB9	GPIO_OUT	USB0-DRV	PD2	LCD0_D2			PG3	CSI1_VSYNC	PI9			SDC3_D3								
	PB10	GPIO_OUT	LCD0-SCK	PD3	LCD0_D3			PG4	CSI1_D0	PI10			SPI0_CS0		GS-INT2	CSI0-AF-EN					
	PB11	GPIO_OUT	LCD0-SDA	PD4	LCD0_D4			PG5	CSI1_D1	PI11			SPI0_CLK								
	PB12	I2S_DI	BT-PCM-IN	PD5	LCD0_D5			PG6	CSI1_D2	PI12			SPI0_MOSI		TV-EN						
	PB13	GPIO_OUT	TP-WAKEUP	PD6	LCD0_D6			PG7	CSI1_D3	PI13			SPI0_MISO		CP-INT						
	PB14	JTAG_MS0	MT-C	PD7	LCD0_D7			PG8	CSI1_D4	PI14			GPIO_OUT		GPS-OSC-EN						
	PB15	JTAG_CK0	GY-INT	PD8	LCD0_D8			PG(12)	PG9	CSI1_D5											
	PB16	JTAG_D00	JTAG	PD9	LCD0_D9				PG10	CSI1_D6											
	PB17	JTAG_DI0		PD10	LCD0_D10				PG11	CSI1_D7											
	PB18	TWI1_SCK	TWI1	PD11	LCD0_D11																
	PB19	TWI1_SDA		PD12	LCD0_D12																
	PB20	TWI2_SCK	TWI2	PD13	LCD0_D13																
	PB21	TWI2_SDA		PD14	LCD0_D14																
	PB22	UART0_TX	UART (DEBUG)	PD15	LCD0_D15																
	PB23	UART0_RX		PD16	LCD0_D16																
				PD17	LCD0_D17																

POWER TREE



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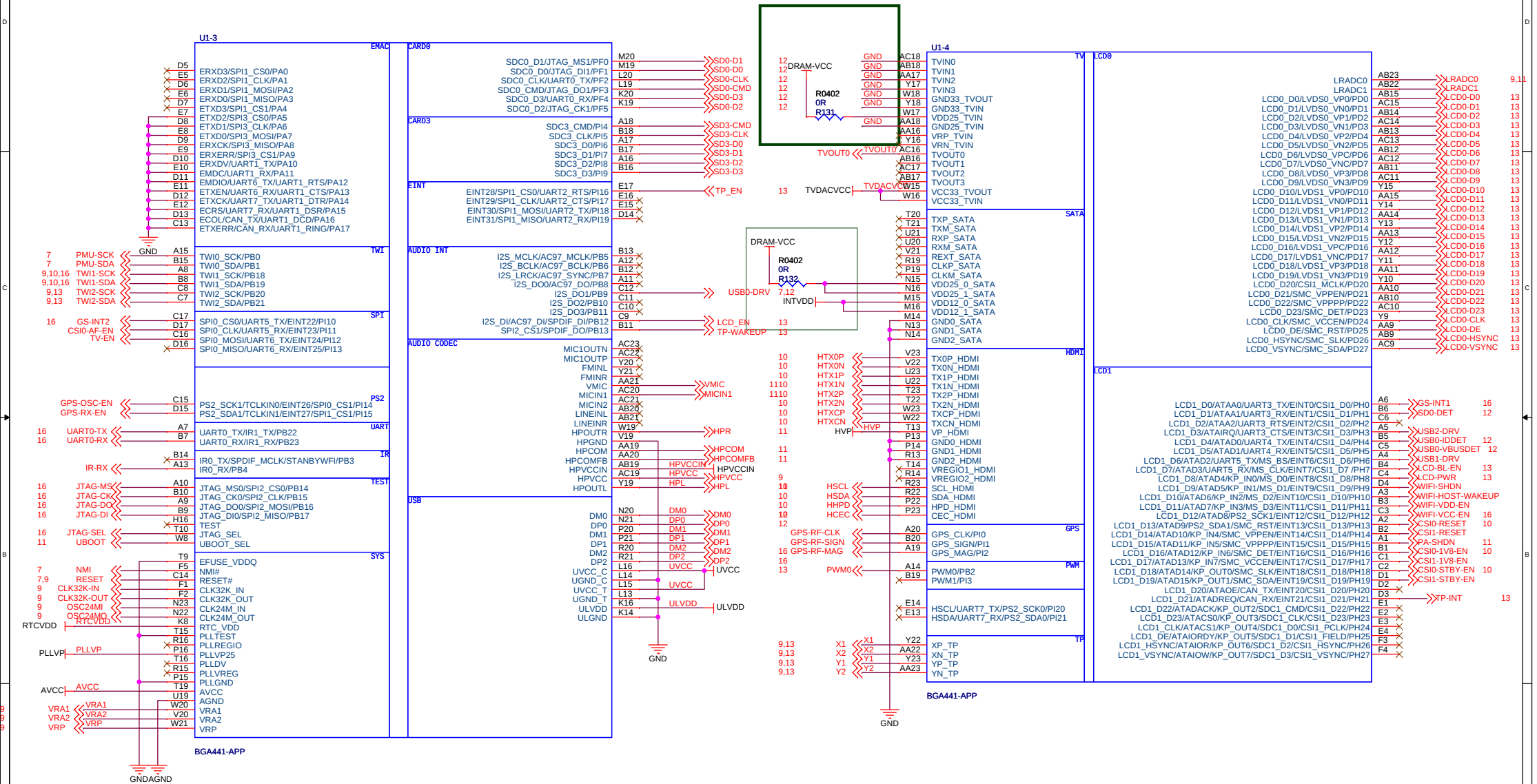
CPU1



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CPU1			
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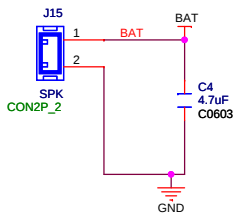
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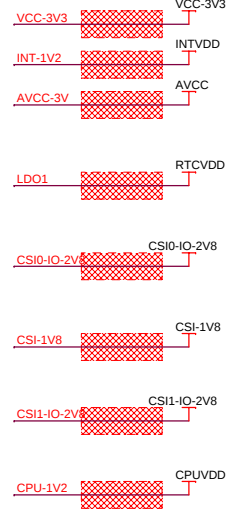
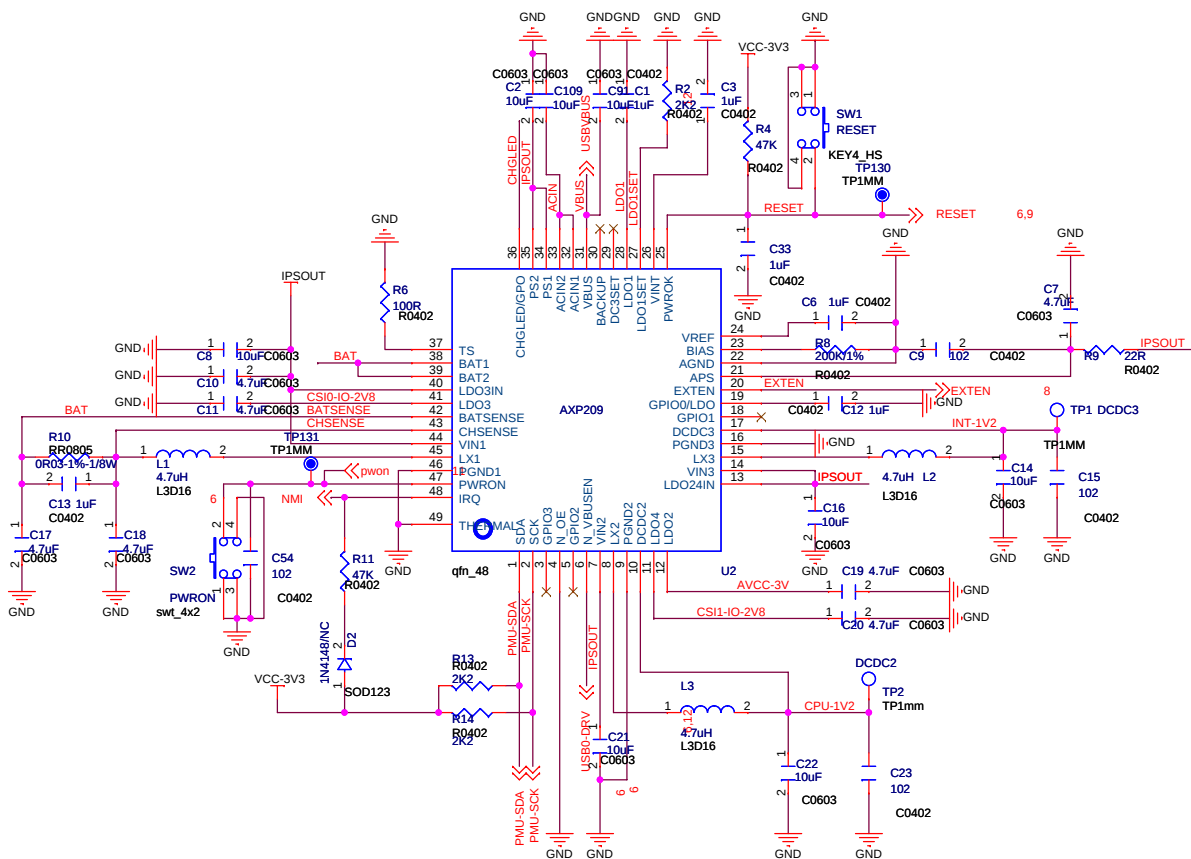
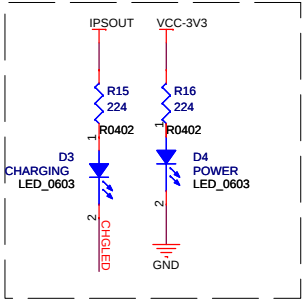
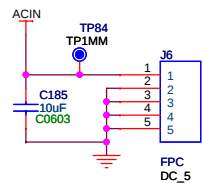
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POWER-PMU



POWER INPUT



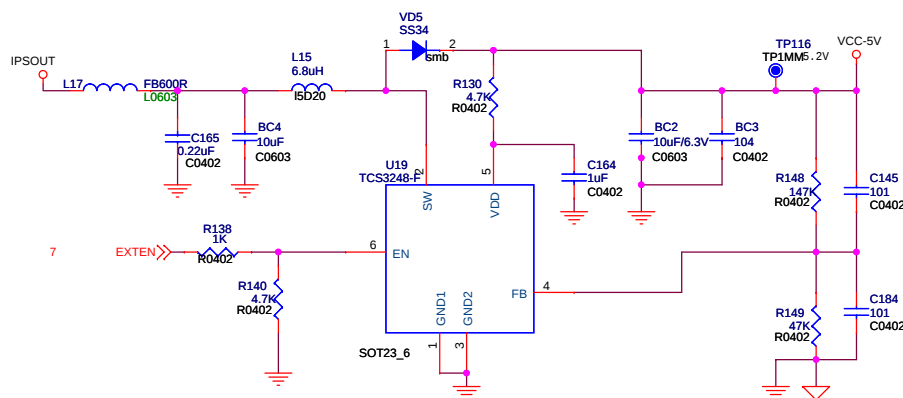
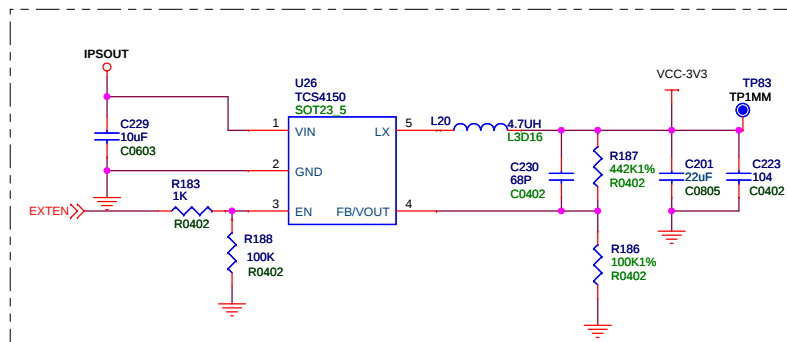
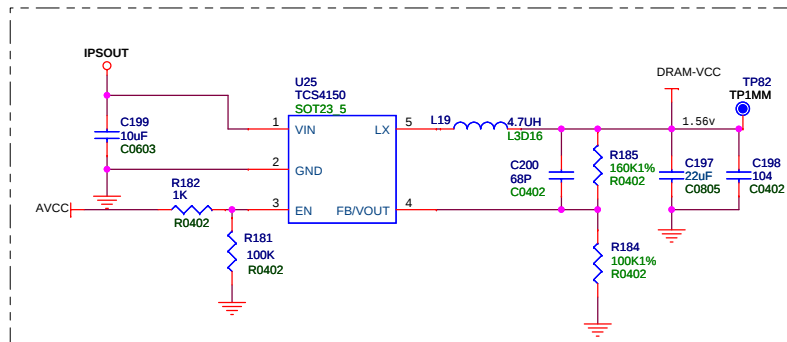
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POWER-DC/DC

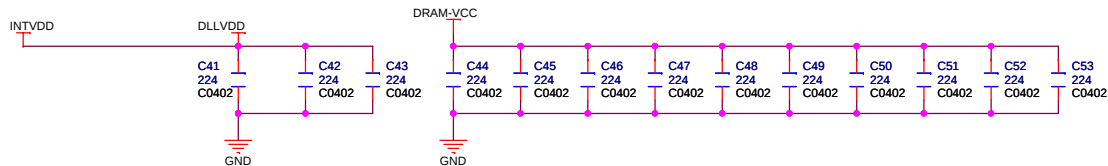


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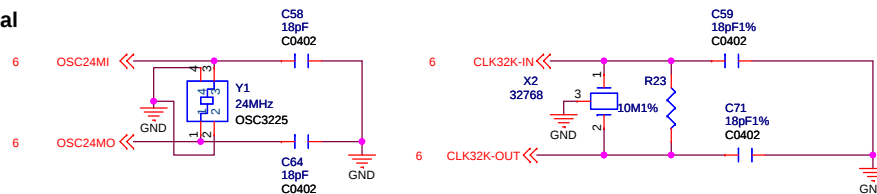
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BESIDE CPU

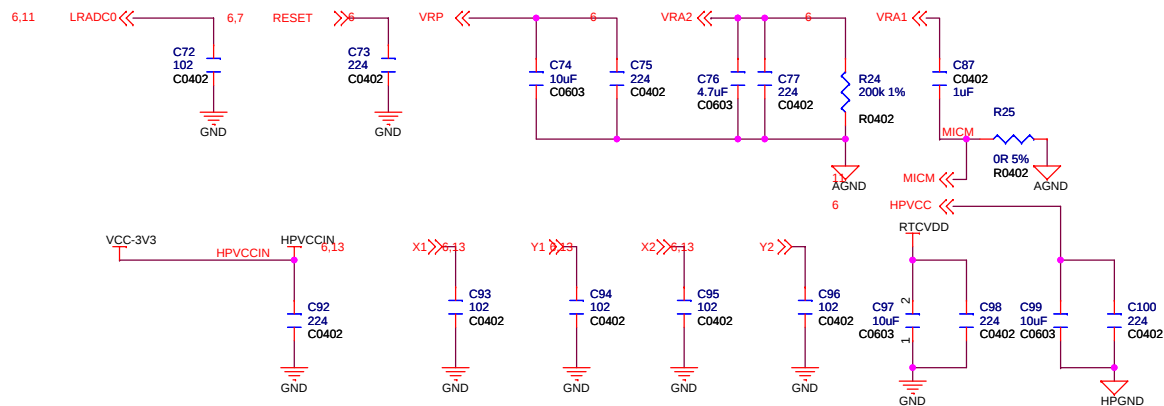
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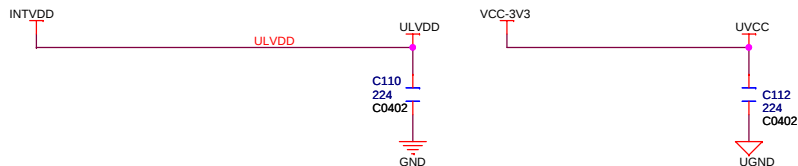
Crystal



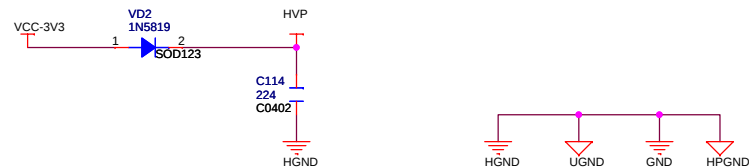
AUDIO&SYS&TP



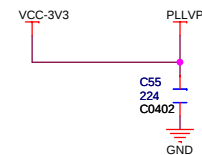
USB



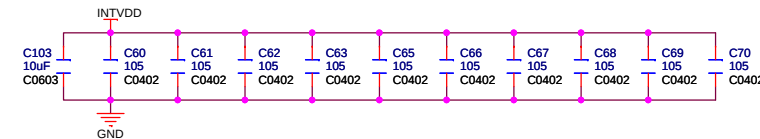
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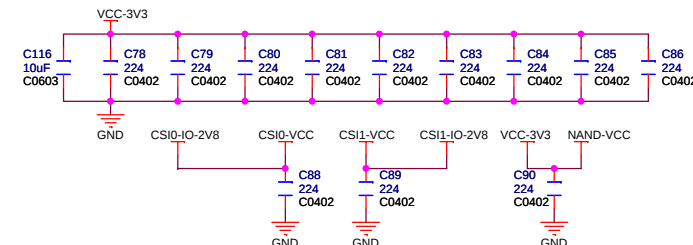
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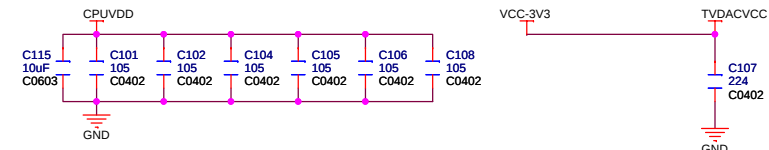
CORE



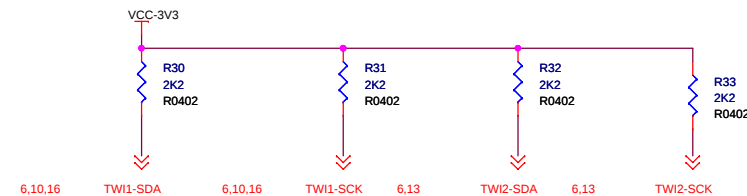
PIO-INTFACE



CPU&TV



TWI-PULLUP

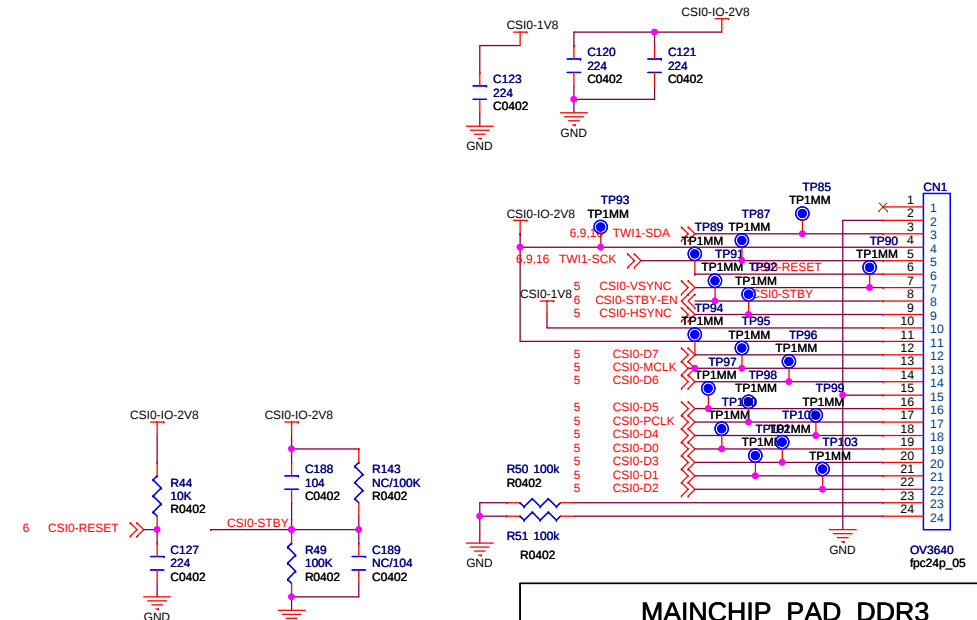


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HDMI

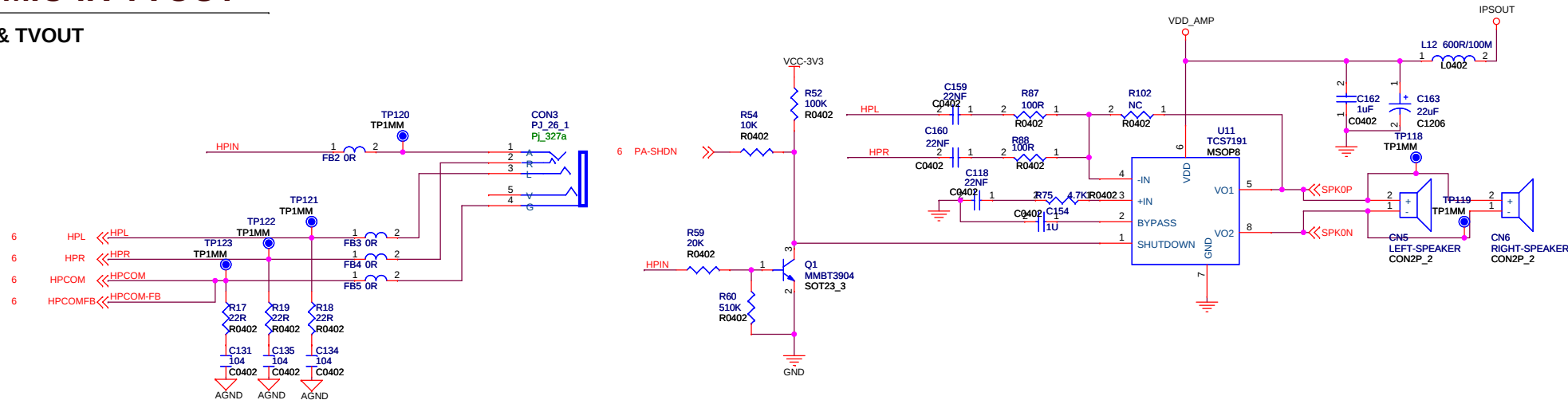
Differential pairs
Z0= 100 ohm



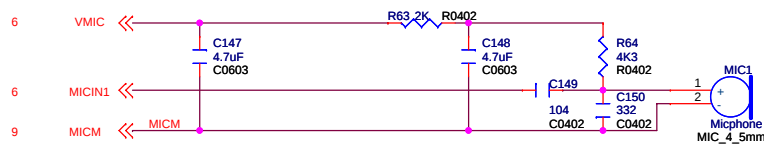
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HP-KEY-MIC-IR-TVOUT

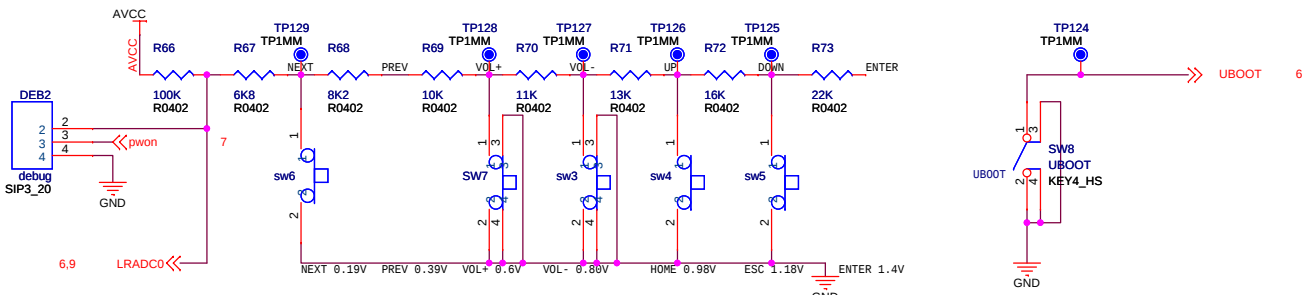
Head Phone & TVOUT



Microphone

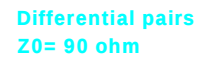


KEY



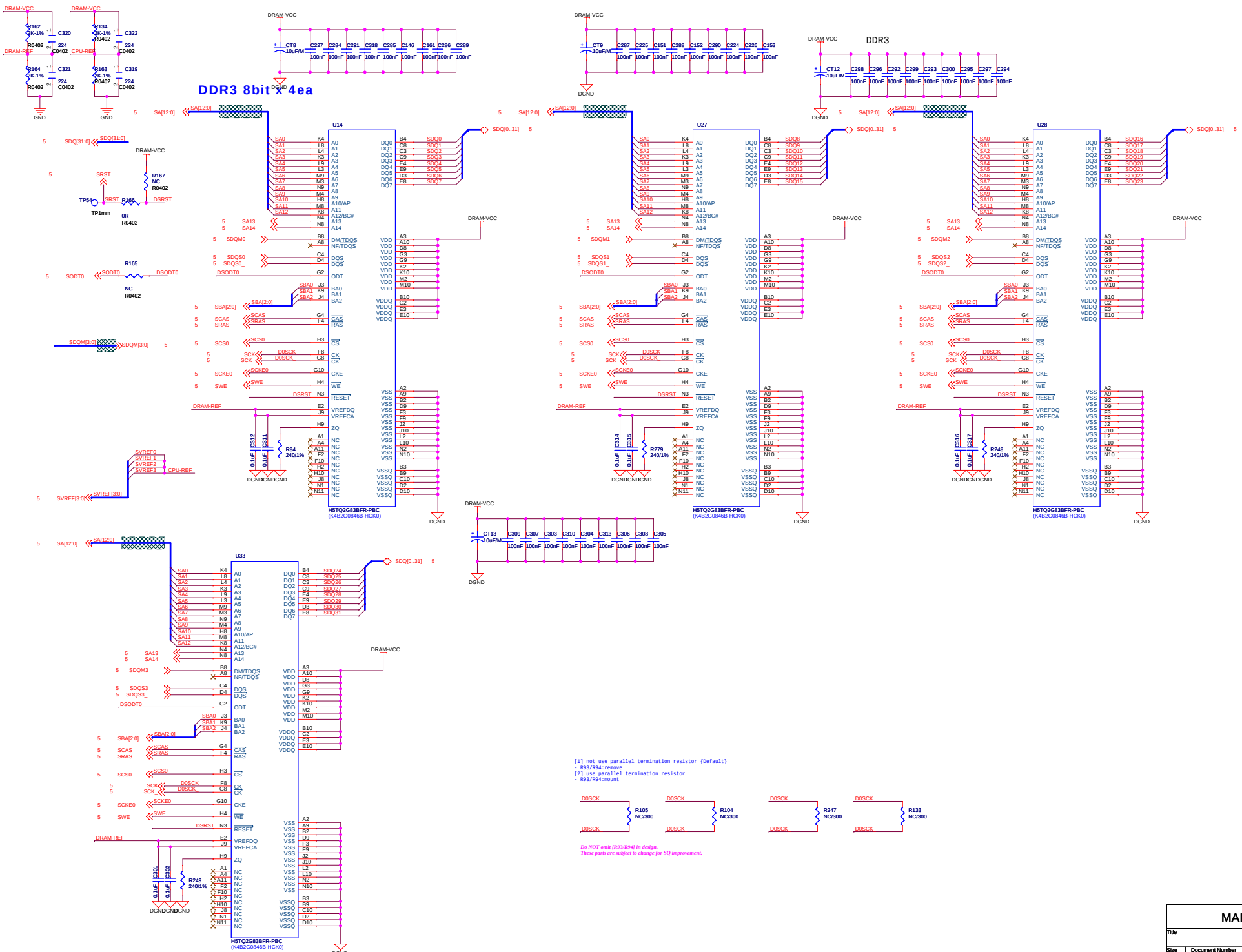
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USB

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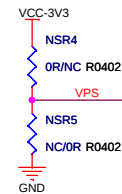
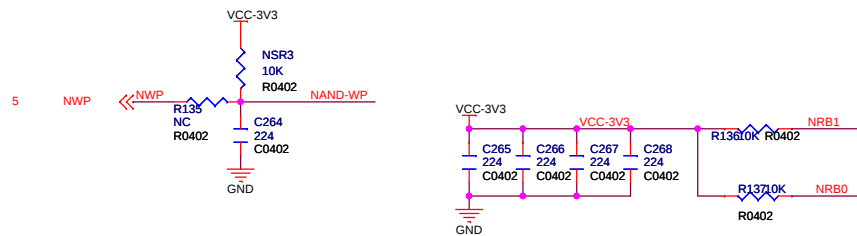
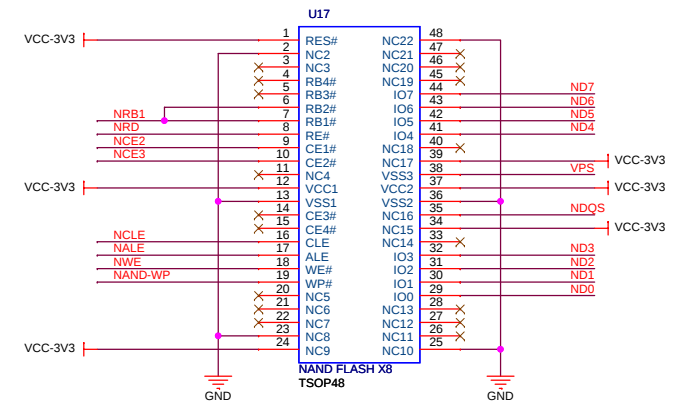
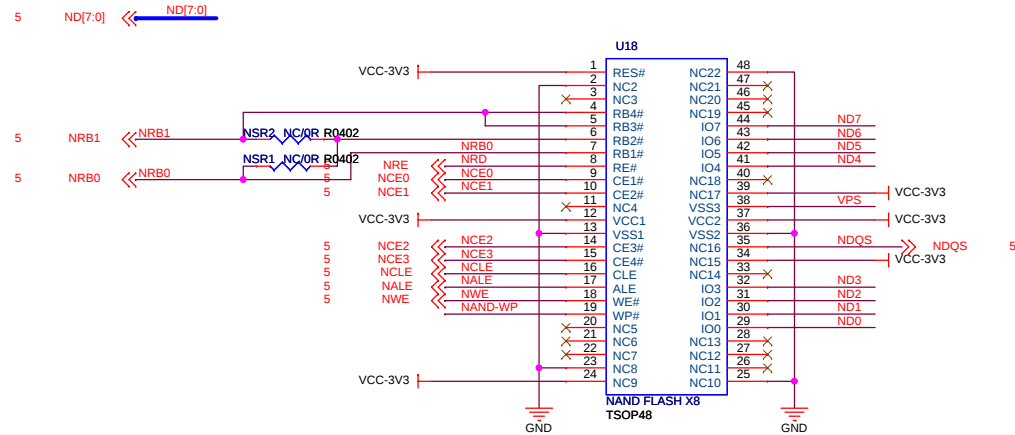
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DDR3



NAND Flash

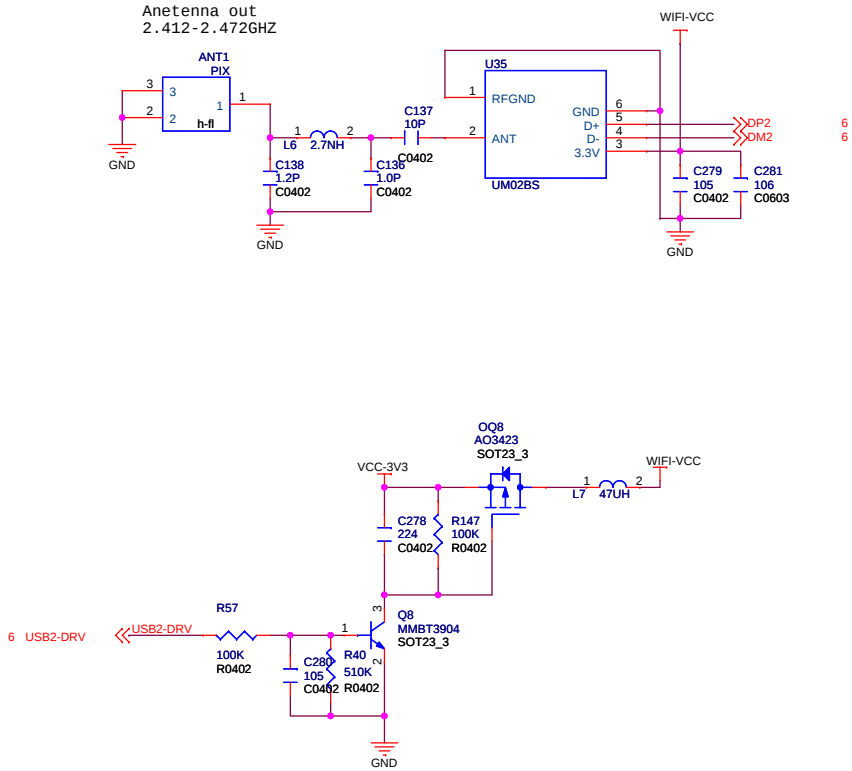
TSOP-48 Nand



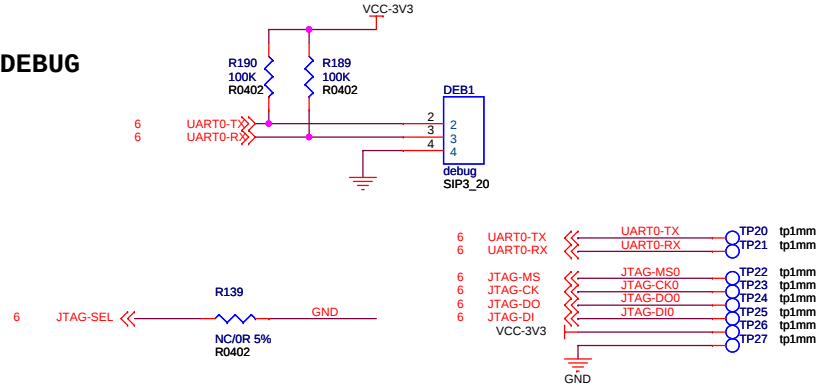
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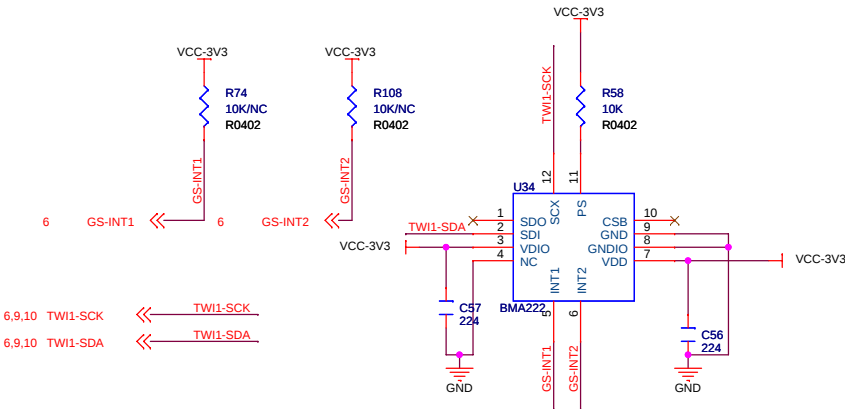
WIFI-GSENSOR



DEBUG



G - SENSOR



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WIFI-GSENSOR			
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