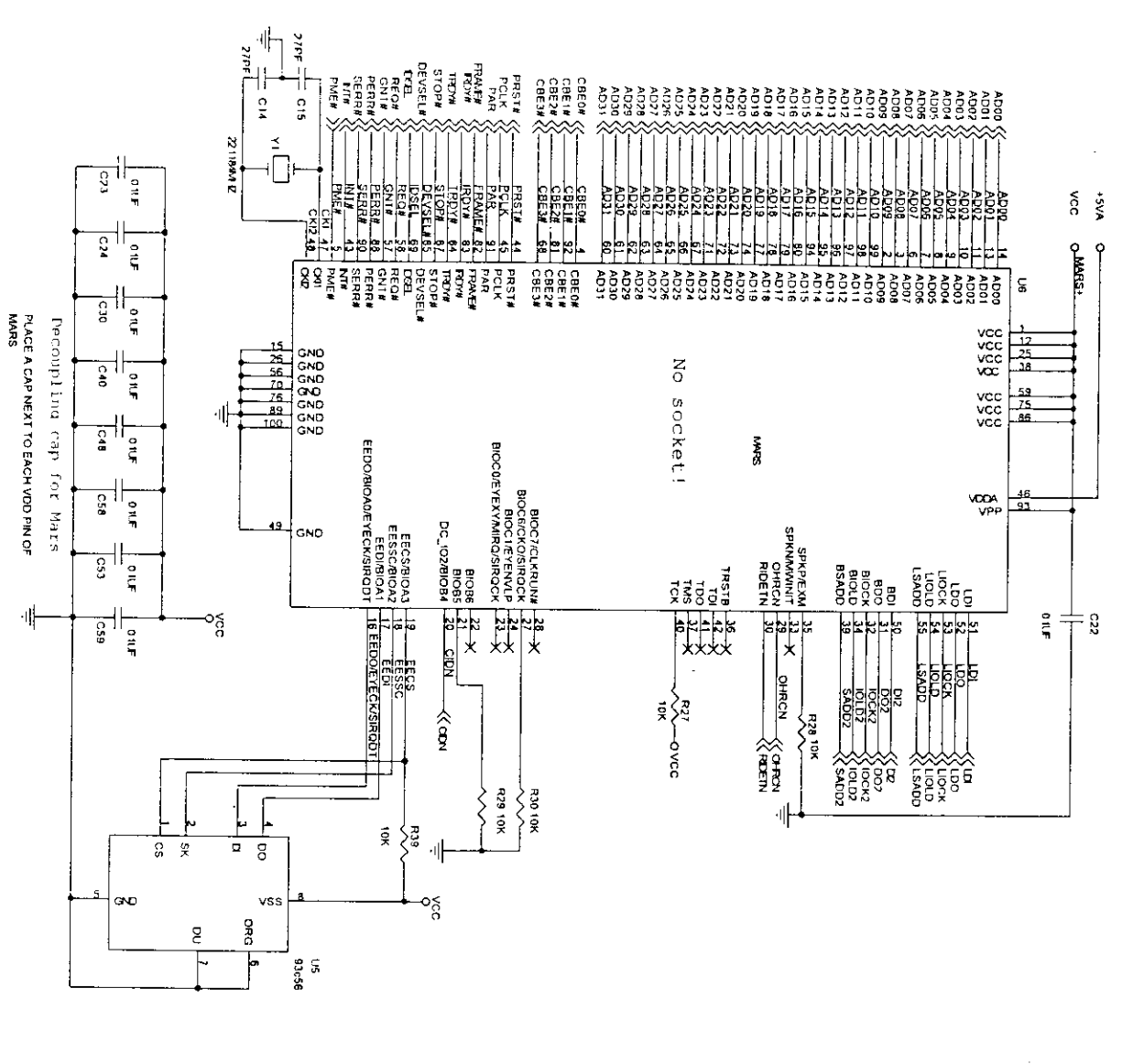




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 Design Engineer DAVID TEN

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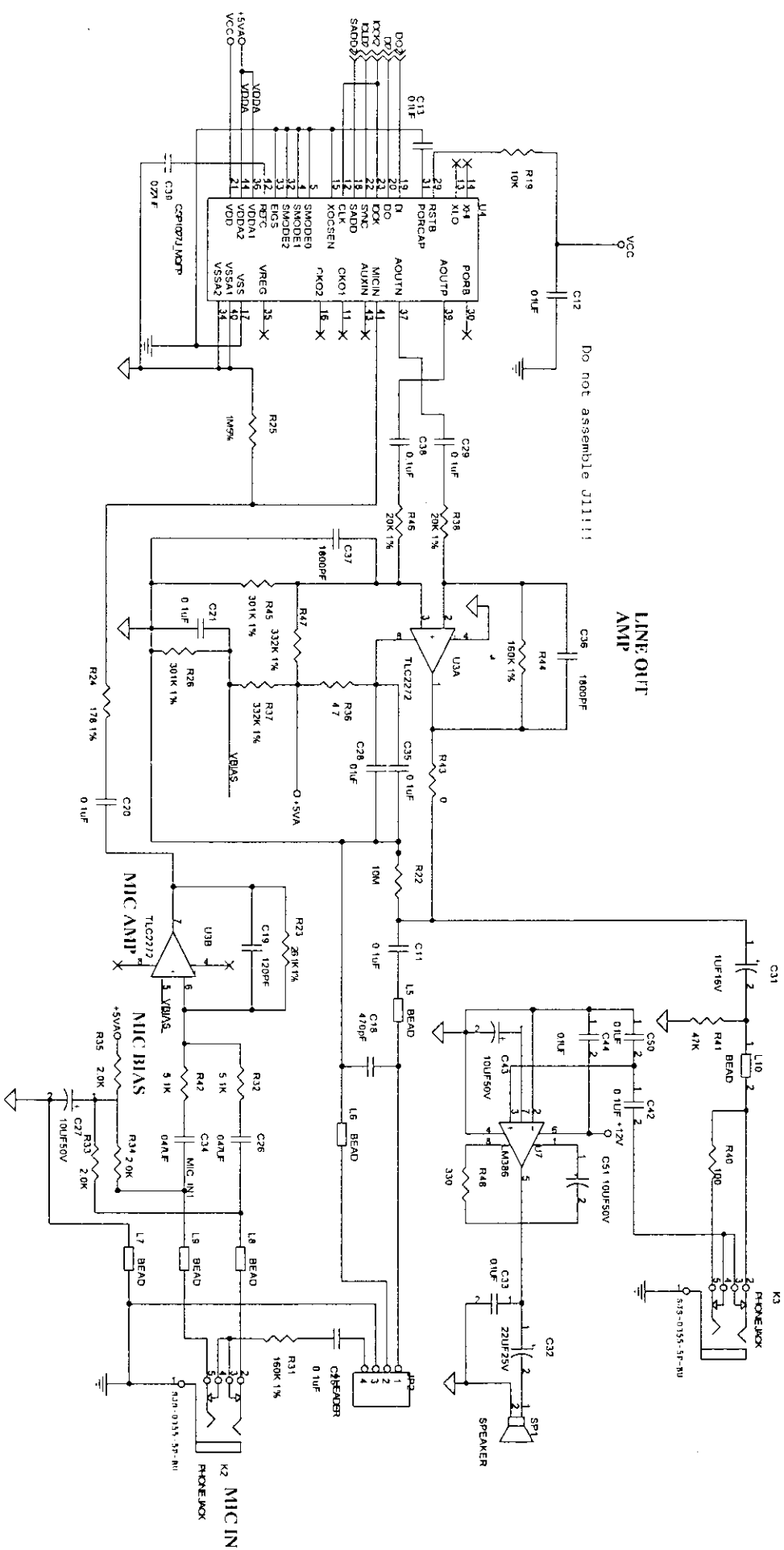
Design Engineer DAVID TEIN

File ML58PVS.M

Size Document Number

Date: Monday, July 27, 1998

Date: Monday, July 27, 1998



LINE OUT AMP

Do not assemble J1!!!!

TAICOM DATA SYSTEMS

Design Engineer DAVID TEN

MSBNSM

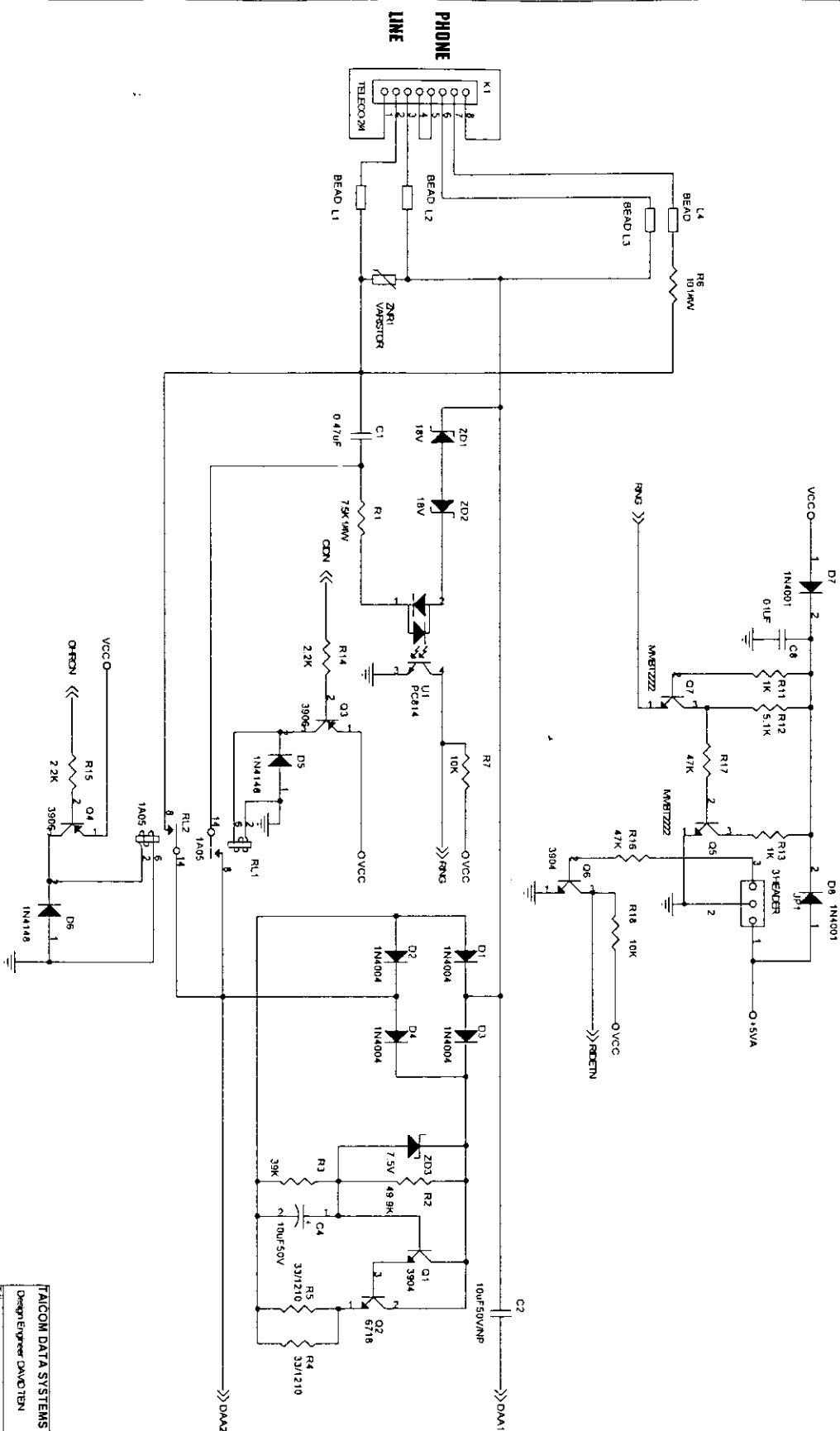
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DAA HIGH VOLTAGE SECTION

WAKE UP ON RING FUNCTION FOR AOPEN MAINBOARD



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Design Engineer: DAVID BEN	
Title	MLSP75-5M4
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B	DAASCH
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The schematic diagram illustrates the AD9044 evaluation board. The central component is the AD9044, a 12-bit, 100-MSPS ADC. It is connected to a +12V supply through a 220μH inductor (L11) and a 10μF/50V capacitor (C53). The output of the ADC is connected to a 0.1μF capacitor (C52) and a 0.1μF capacitor (C51). The board also includes a 12V supply, a 12V supply, and a 12V supply. The board is populated with various components, including resistors, capacitors, and inductors, which are labeled with their respective values and part numbers. The board is designed to be compatible with the AD9044 evaluation board, and it includes a 12V supply, a 12V supply, and a 12V supply.