

Re: FCC ID FDI-04600000-0  
Applicant: Melco Inc  
Correspondence Reference Number: 8672  
731 Confirmation Number: EA94414  
Date of Original E-Mail: 07/09/1999

1) Indicate whether the peripheral portion of the device will be DOC approved or Certified. If it is DOC approved, provide the DOC Certificate and the revised label with DOC label requirements. If it is to be Certified, file a complete application for it. It must be tested as a computer peripheral with minimum test configuration per ANSI C63.4.

**We submitted radiation test data , peripheral list and test setup photo.**

2) Provide the AC line conducted data per Section 15.207.

**We submitted conducted test data.**

3) Due to the integral antenna, the output power must be calculated from a field strength measurement. Measure the maximum peak field strength of the fundamental at 3 meters. Set the RBW=VBW= 1 MHz. Using the formula  $P=(E \text{ V/m} * d)^2/30G$ . E is the field strength converted to V/m. d is the test distance d=3. G is the numeric gain of the antenna G=1. Calculate P for 3 frequencies.

**We submitted test data.**

4) Due to the integral antenna, the power density must be calculated from a field strength measurement. Measure the maximum field strength of the fundamental at 3 meters. After zooming in on the peak emission of the fundamental. Set the RBW=VBW=3kHz, Span=1.5MHz, sweep=500 seconds.

Using the formula  $P=(E \text{ V/m} * d)^2/30G$ . E is the peak field strength converted to V/m. d is the test distance d=3. G is the numeric gain of the antenna G=1. Calculate P for 3 frequencies.

**We submitted test data.**

5)What is the theoretical process gain? **PG=10log(11Mcps/1MHz)=10.4dB**  
What is the Chip/symbol ratio? **Chip rate: 11Mcps , Symbol rate: 1MHz**  
What is the spread rate and the data rate? **Spread rate: 11M ,**  
**Data rate: QPSK(2Mbps)/BPSK(1Mbps)**

6) Provide the test configuration of the process gain test.

What is the Chosen BER? **1.exponential (-5)**

What is the S/N for the chosen BER? **10dB**

What is the system loss (Lsys)? This can not be greater than 2 dB. **8.6dB**

Process gain is calculated as  $G_p = L_{sys} + S/N + M_j$

$M_j$  is the jammer to signal ratio at the input of the receiver.

You only have to provide process gain test on 1 channel.

To find  $M_j$ , Provide a chart of the actual Jammer level and signal level at the input of the receiver that produces the chosen BER. This should be done across the passband of the receiver in 50 kHz steps. Disregard the worst case 20%. Use the lowest remaining J/S level as  $M_j$  in the formula  $G_p = L_{sys} + S/N + M_j$ . What is  $G_p$ ?

**We submitted Chart.**

$$\begin{aligned} PG &= L_{sys} + S/N + M_j \\ &= 8.6+10+0.4 \\ &= 19dB \end{aligned}$$