TDMA ARIC

The ARIC card is broken up into 3 pcbs, which are the:

- ■ TDMA ARIC card which is the interface to the ATM switch (36170)
- ■ ARIC Modem module from Stel
- ■ ARIC I/F Converter Module

A block diagram of the TDMA ARIC is shown in Figure 1.9 below. The card occupies two UCS slots in a 36170 peripheral shelf and supports the following functionality:

- •ATM Interface to the 36170 backplane
- •ATM Cell queuing on egress and policing on ingress
- •One QPSK downstream modulator supporting concatenated RS 204:188 and 7/8 convolutional coding, interleaving and two software provisionable symbol rates
- •Two up stream differentially coded QPSK burst demodulators with FEC
- •Integral L-band Tuner supporting software provisionable channel assignment from the network manager
- •Serial ports for configuration and monitoring of upmast base station radios
- •MAC layer supporting dynamic bandwidth allocation
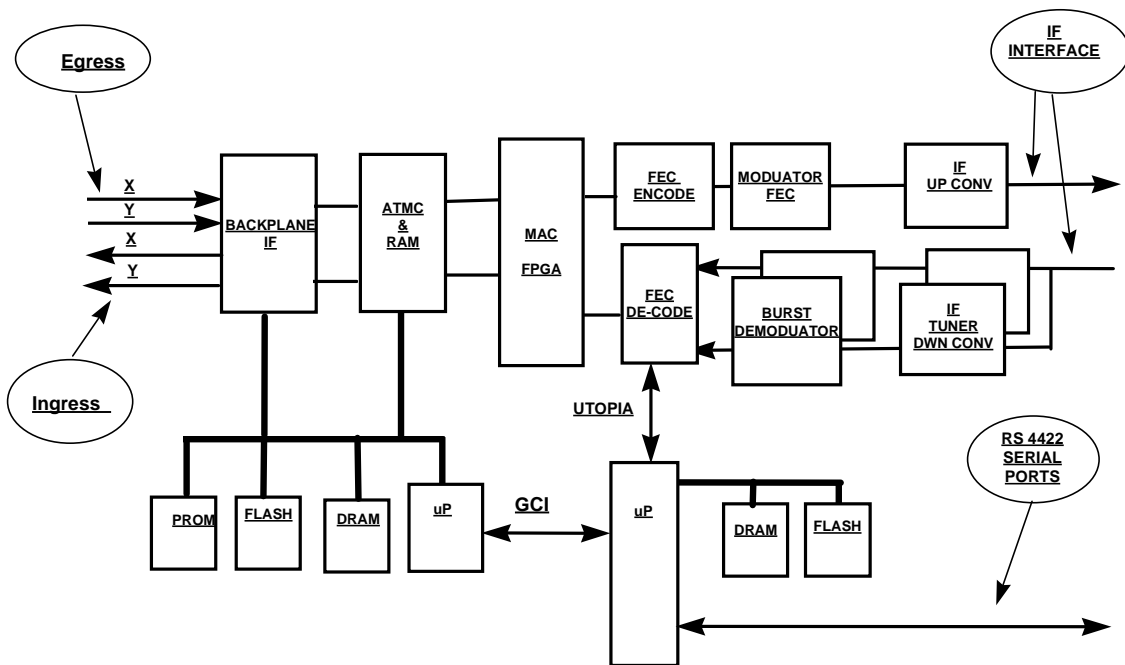- •Support of UBR and CBR traffic ATM classes of service

Figure 1.9 TDMA ARIC Block Diagram

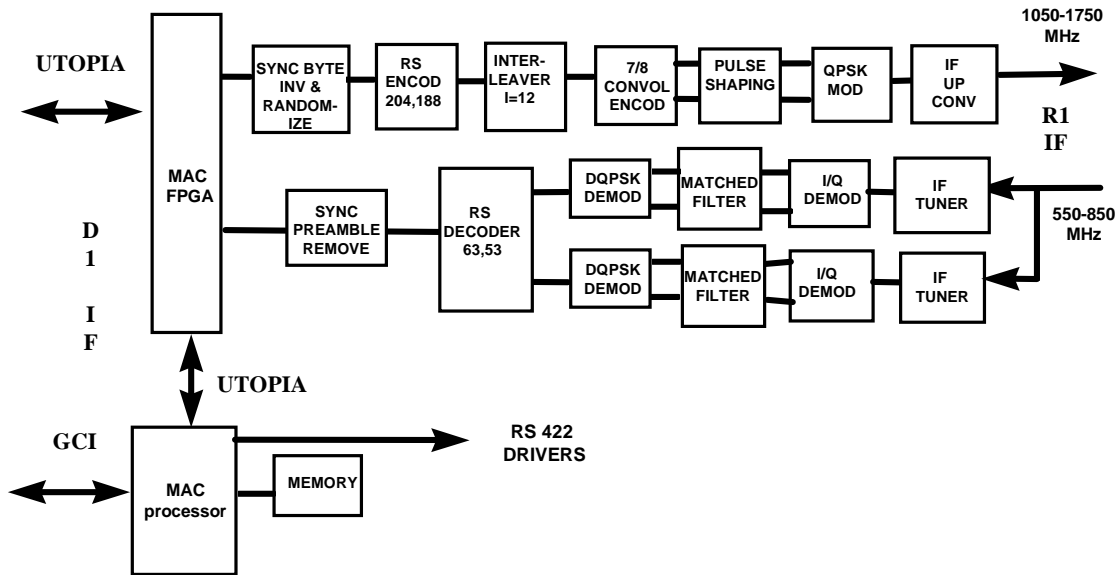A detailed block diagram of the TDMA ARIC modem is shown in Figure 1.10 below.

Figure 1.10 TDMA ARIC Modem Block Diagram