

1. Several minor layout changes to improve manufacturability and improve performance margins (but held to original specifications).
2. Removed some unused parts from the layout.
3. Replaced some parts that were obsolete / discontinued by manufacturer.
4. Changed some passive components to improve RF matching internally.
5. Changed amplifiers (LNAs) at front end of reverse path(s) to improve noise figure.
6. Added capability (involved adding some digital lines, adding some gates, and changing software) to download FPGA code via external microprocessor service port instead of on-board JTAG connector.
7. Added an additional RS-232 port that customer may use to transport data of their choosing across the fiber link. Does not affect product function and uses existing capability of the product - primarily adds additional connector and routing to that connector on front panel.
8. Many significant software upgrades (primarily bug fixes) affecting the user interface were made at the same time (primarily to Element Management System (EMS)).
9. Very few minor mechanical changes to cabinet to improve manufacturability (changed labeling on external ports, moved one board mounting standoff 0.1" to match PWB hole pattern, added customer RS-232 port).

No changes were made to any frequencies including clocks, references, IF and Baseband frequencies. Also no changes made to power supply, cabling, shielding, or the LPA.