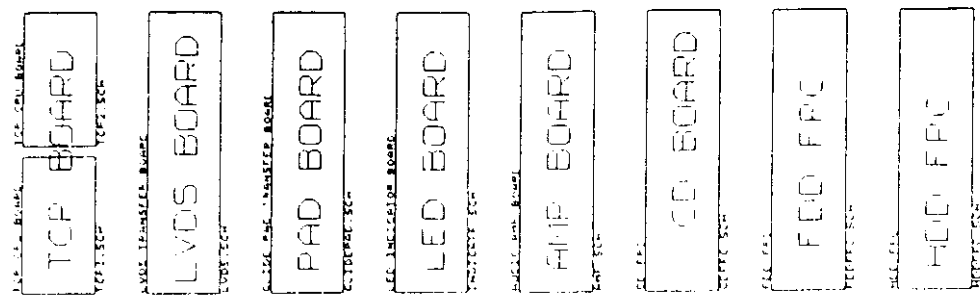


U
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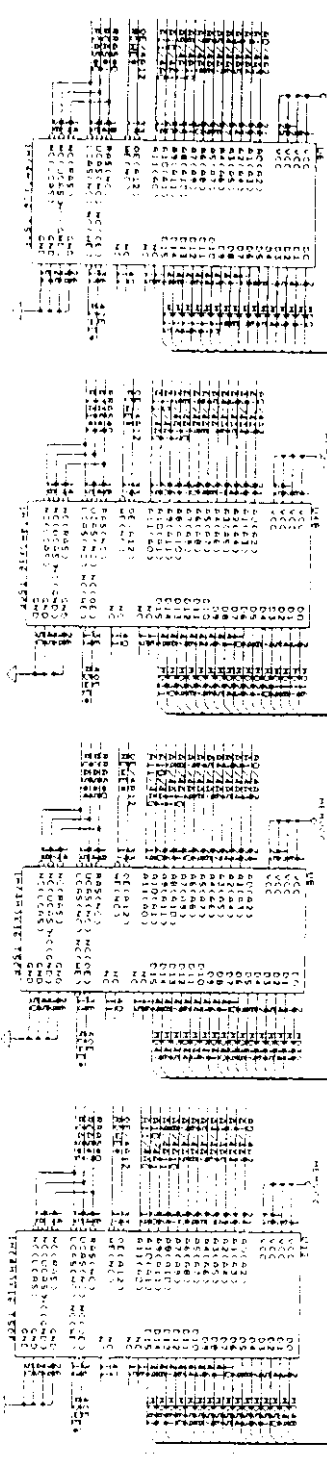
62-1012



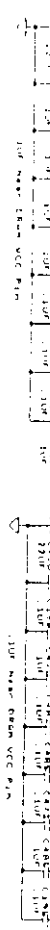


FOR CH-16

SCHEMATIC SYMBOLS

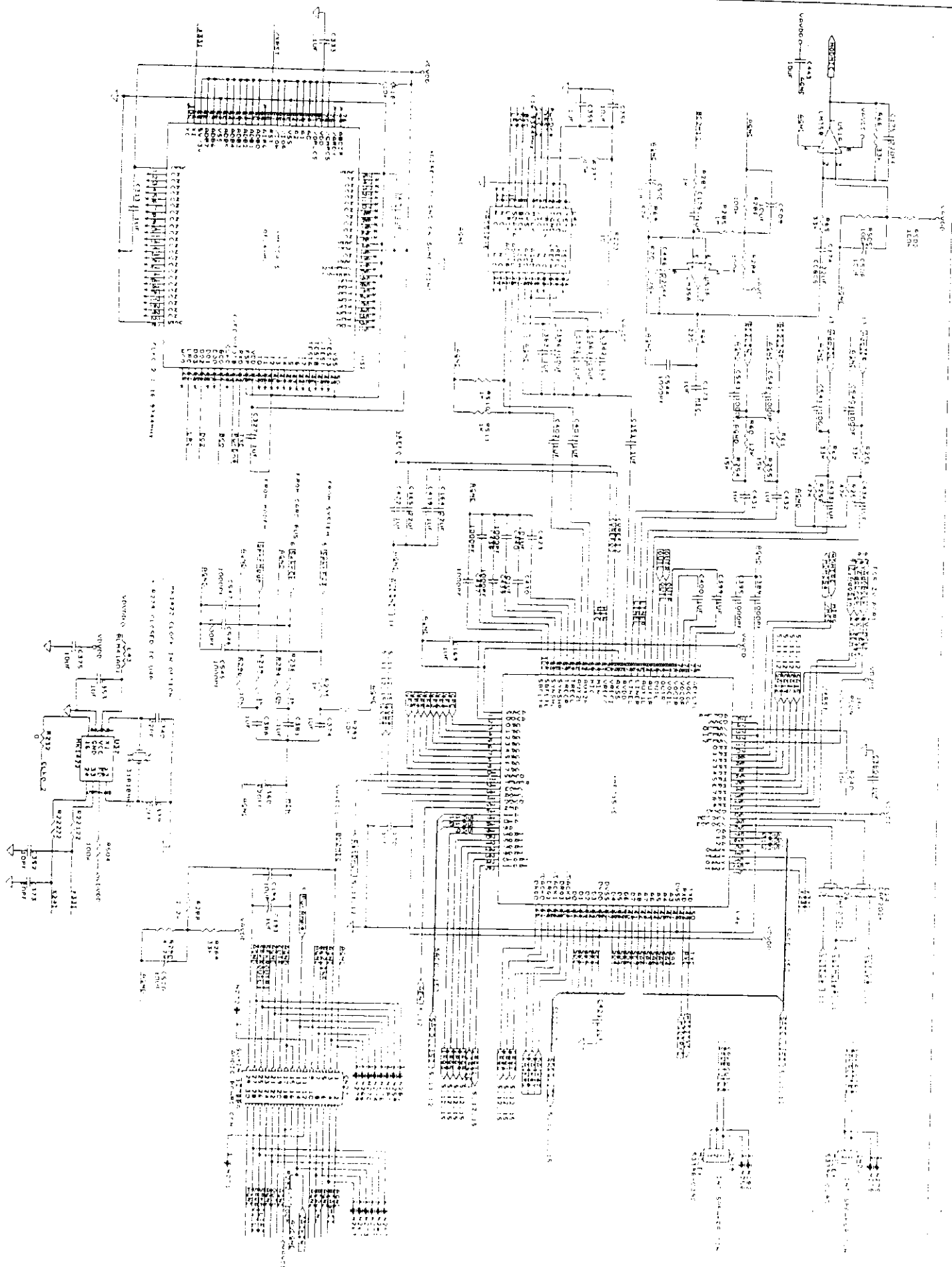


BANK 0



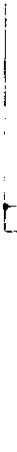
BANK 1



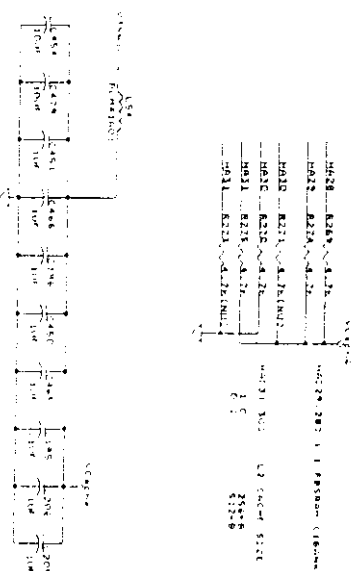
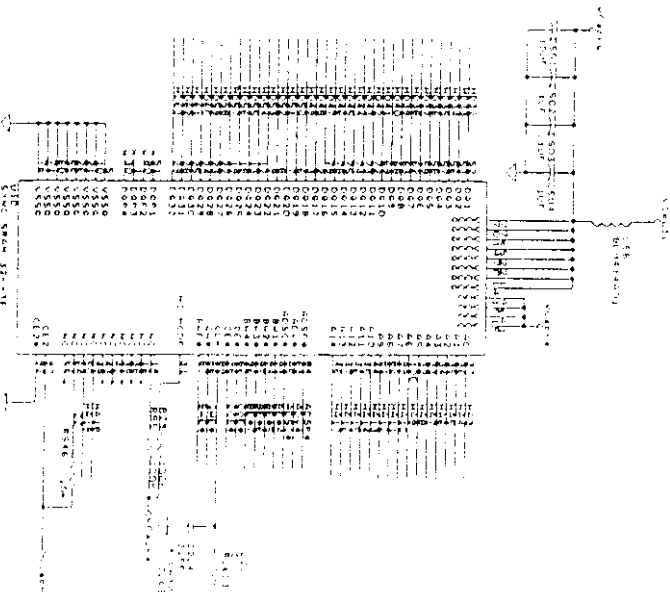
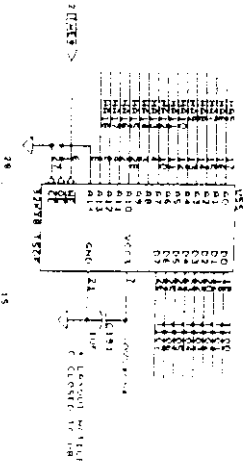


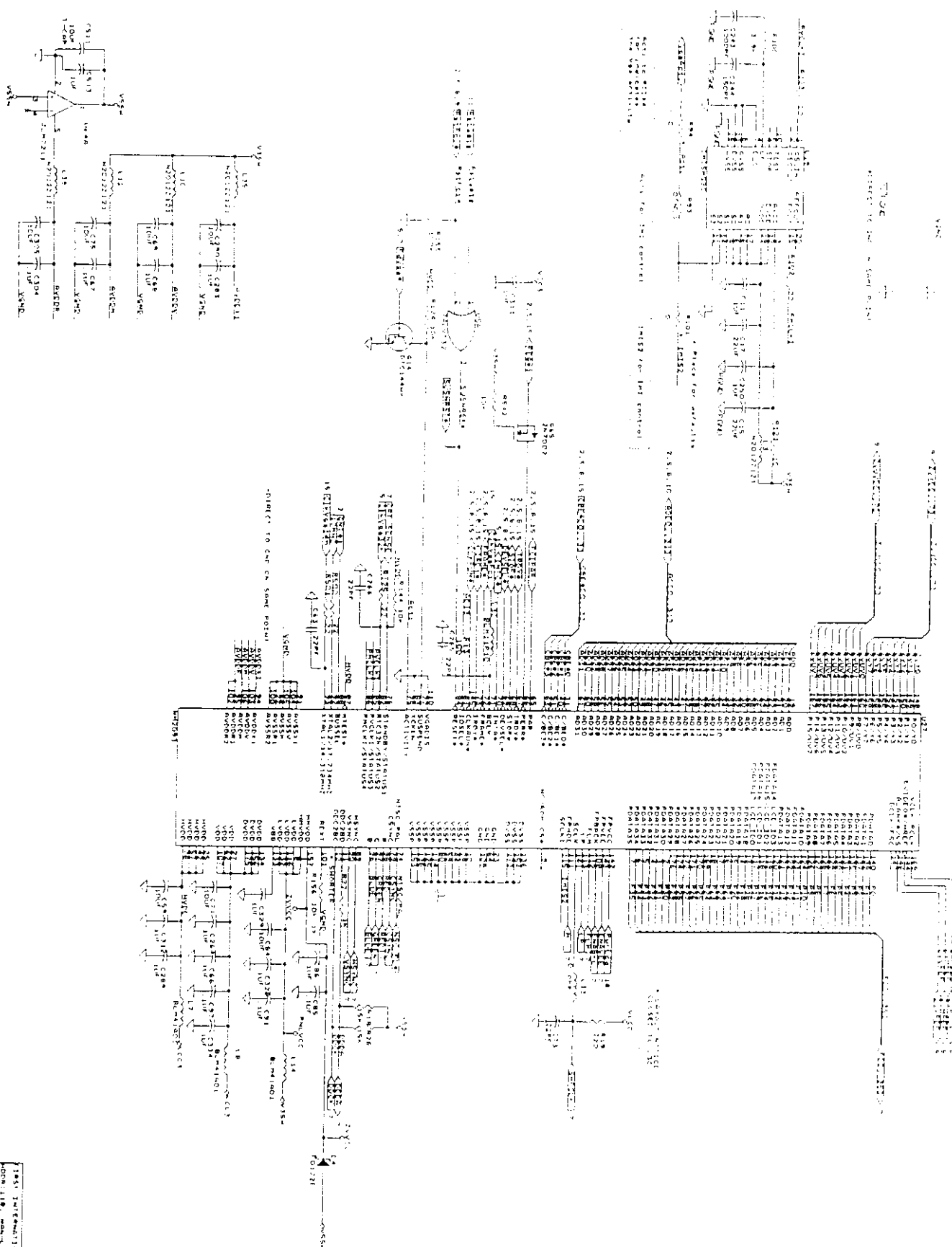
ARCHITEX TELCOM CORP

EXAMPLE

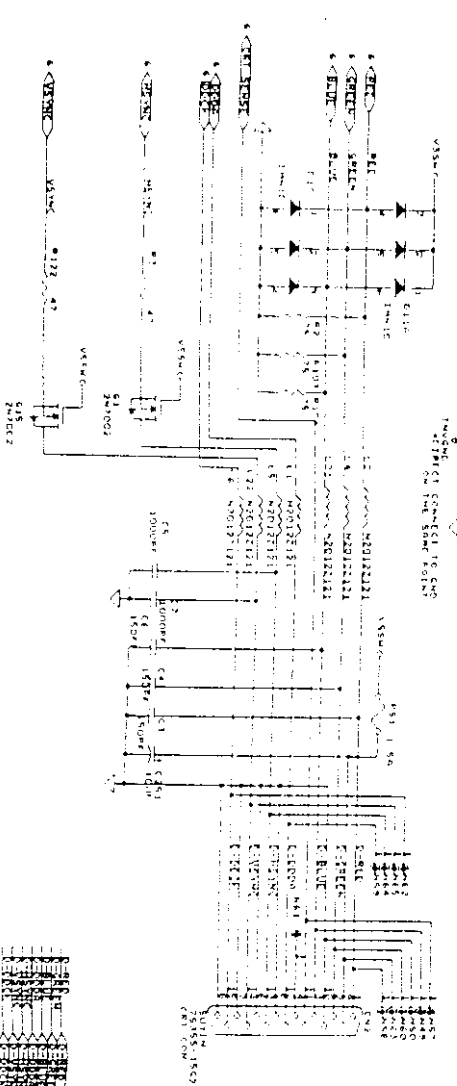
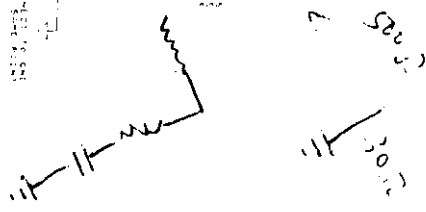
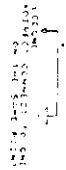
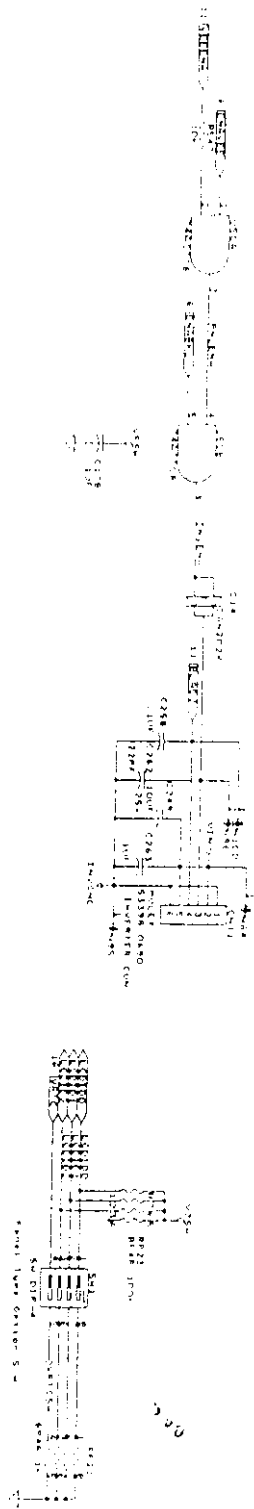
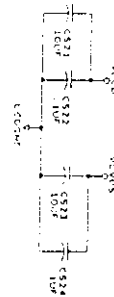
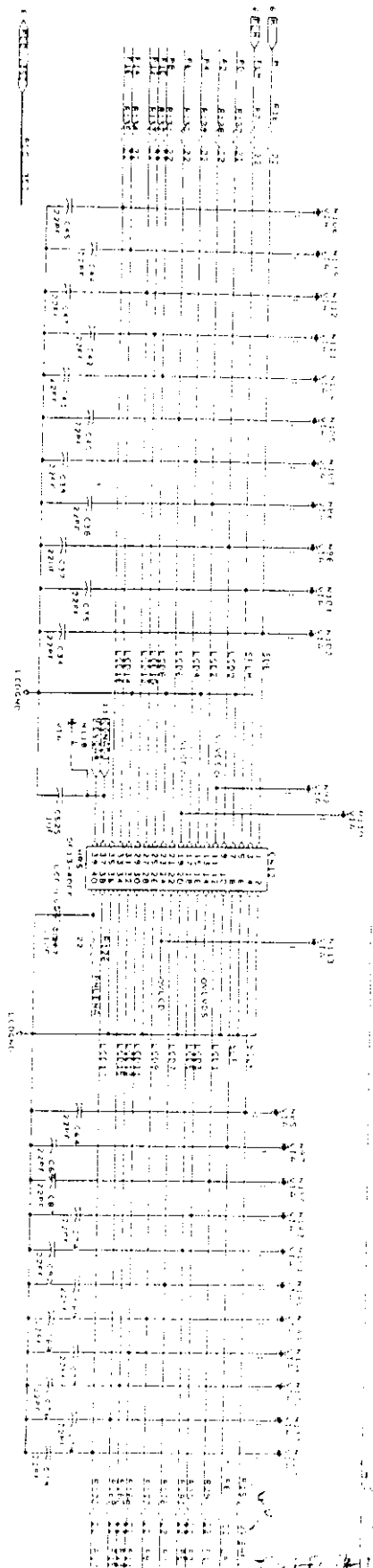


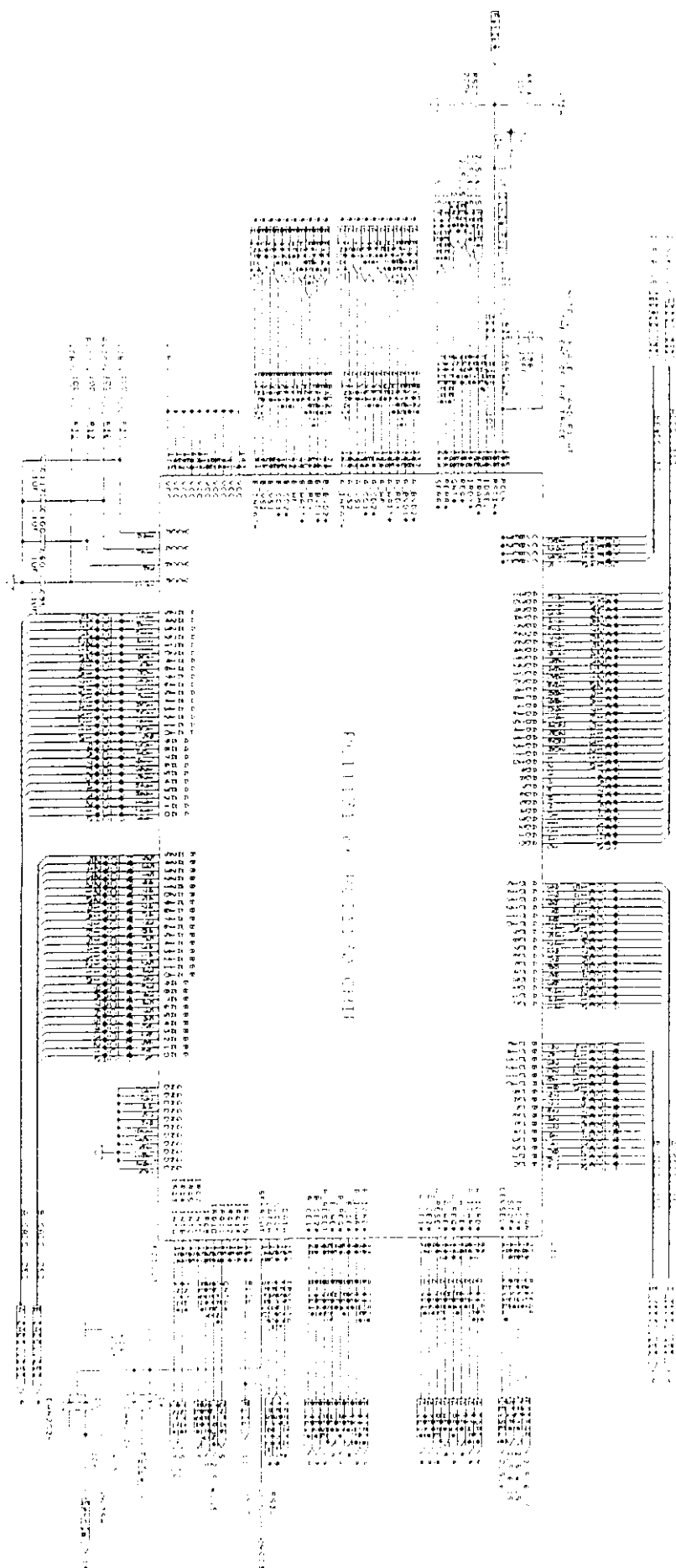
This technical drawing illustrates a mechanical assembly, possibly a pump or engine component, with a detailed cross-section view. The drawing includes various parts, dimensions, and a detailed cross-section view. The main assembly is shown in a perspective view, with a detailed cross-section view on the right. The cross-section view shows the internal components, including a central shaft, a piston, and a crankshaft. The drawing is labeled with various dimensions and part numbers. The dimensions are given in inches and fractions of an inch. The part numbers are given in a code that includes a letter and a number. The drawing is a technical drawing of a mechanical assembly, likely a pump or engine component, showing various parts, dimensions, and a detailed cross-section view.

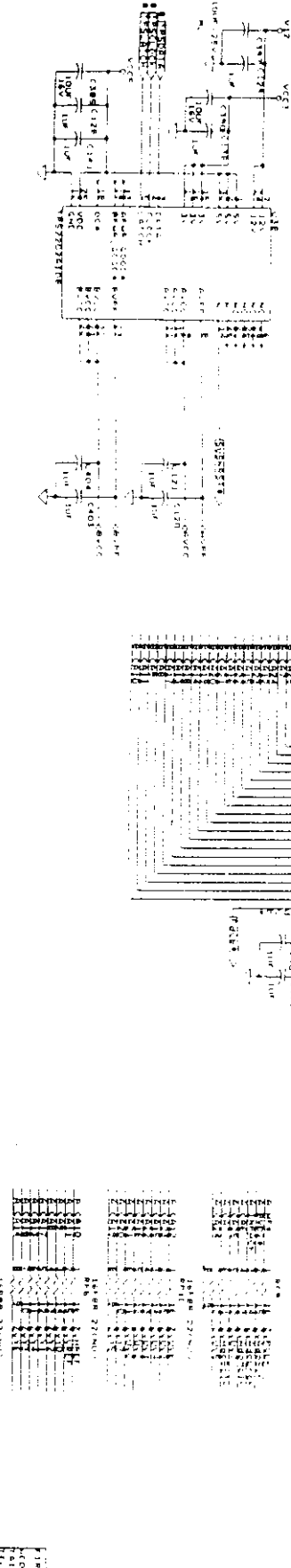
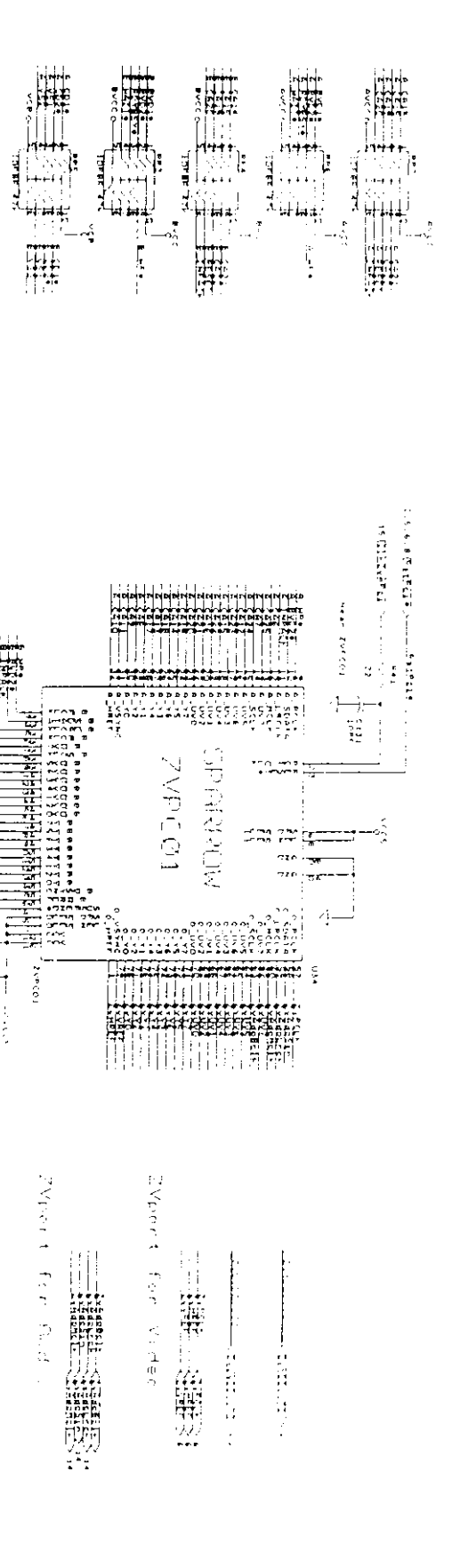
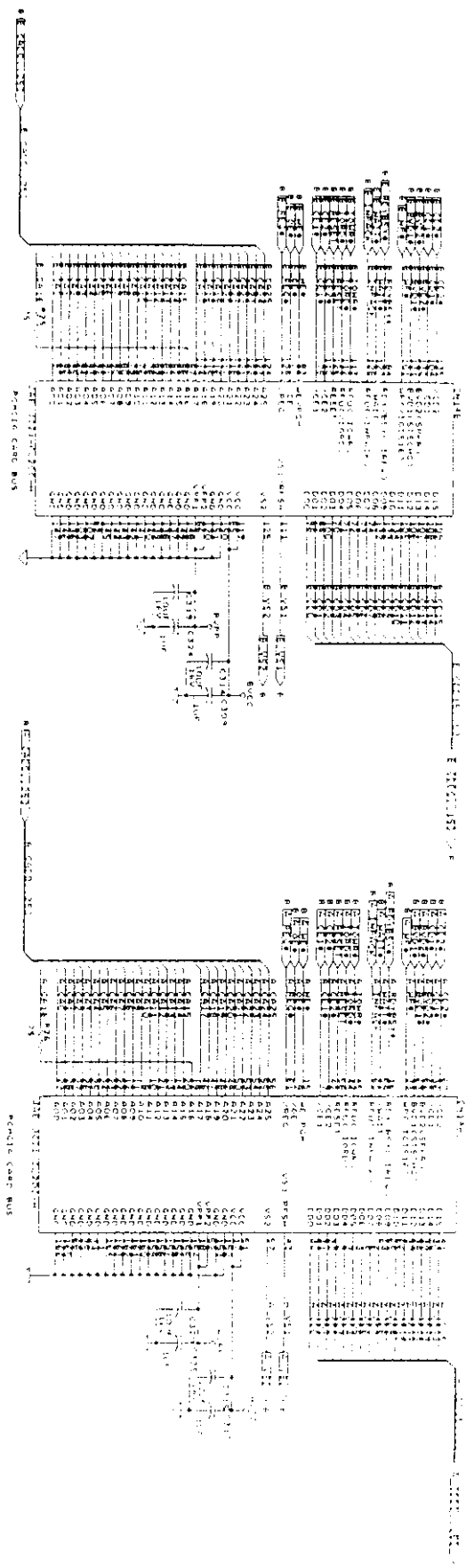




1961 International Computer Inc. 7-708-0101
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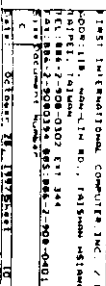
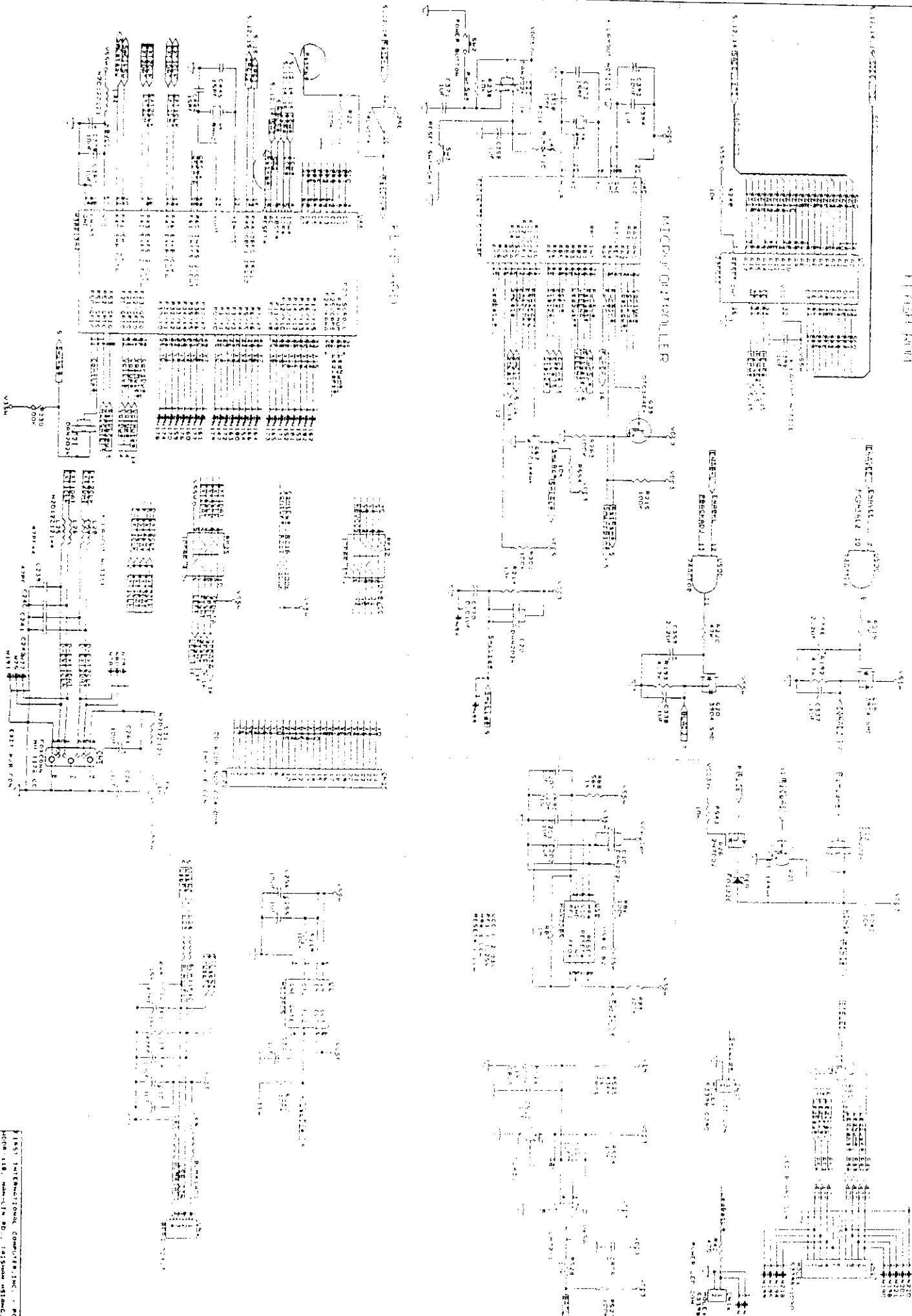
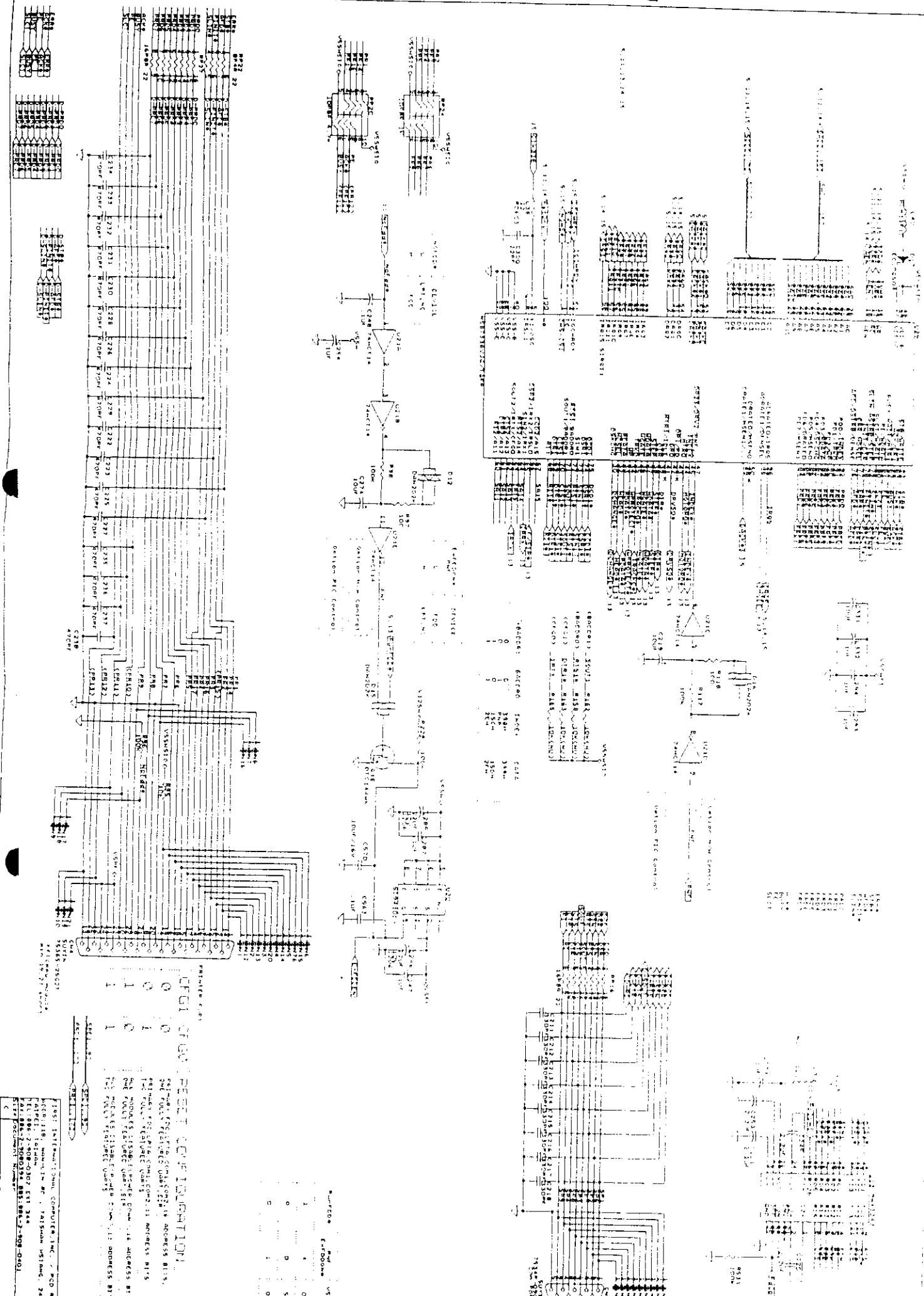


FIGURE 1





REGISTRATION

[illegible]

25365-25679

1951: International Computer Inc. c/o
Bldg. 118, West 1st St., Fairport Harbor,
Ohio, 44840
Tel: 206-2-000-030, Ext. 344
Fax: 206-2-000-034, 206-2-908-040
Size: Document Number
C
Date: 06/05/78, 1997, 2001
Page: 17 of 17

FIGURE 1
SIGNAL INPUT

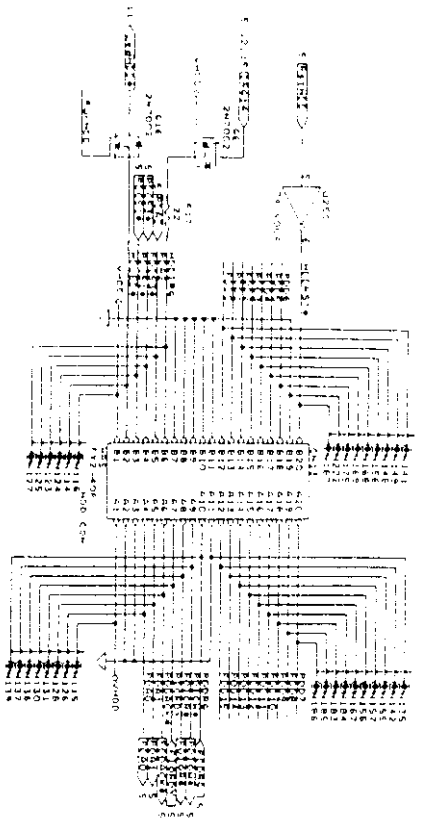


FIGURE 2
SIGNAL INPUT

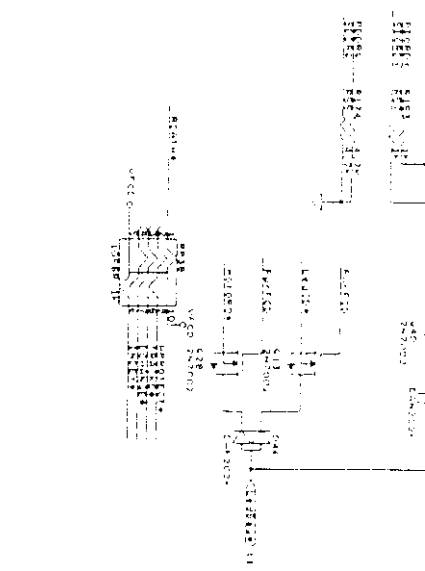


FIGURE 3
SIGNAL INPUT

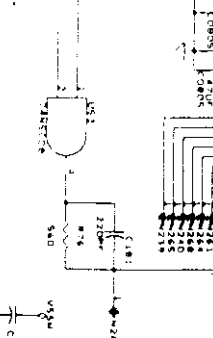
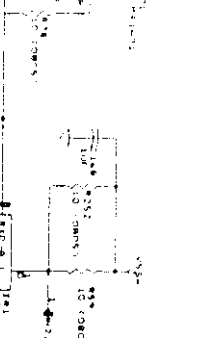
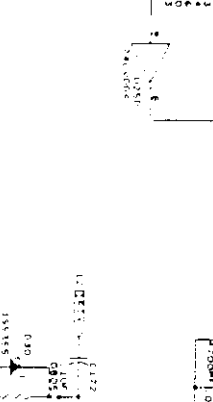
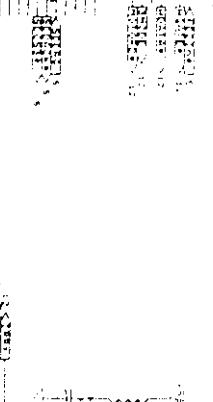
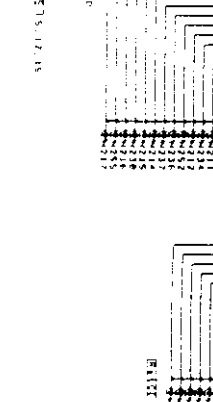
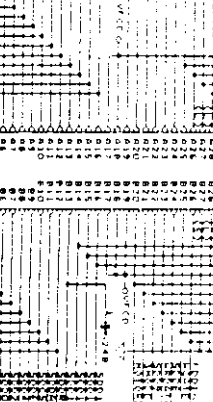
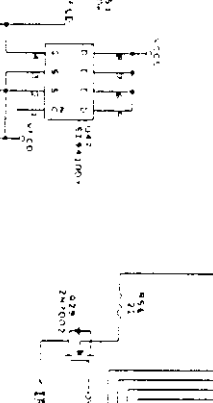
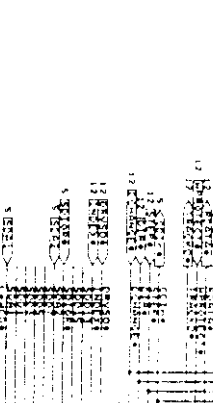
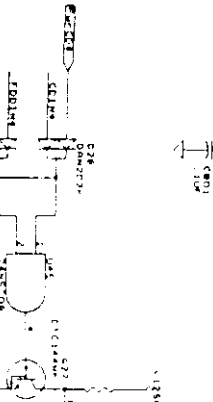
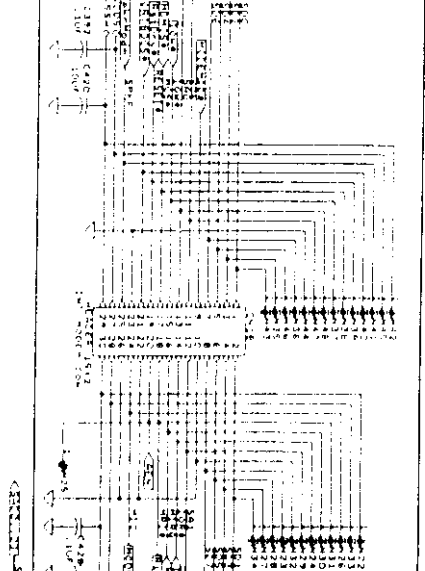
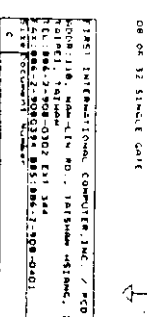


FIGURE 14
SIGNAL INPUT



REF 14. 3164H7

CLK/JOE 19.3164H7 for P144 08T IN
 CLK/JOE 19.3164H7 for P144 08T IN
 CLK/JOE 19.3164H7 for P144 08T IN

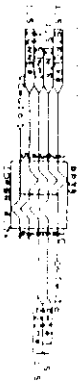
HDMI CLOCK 60/50/40MHz

CLK/JOE 60/50/40MHz for CPU CLK IN
 CLK/JOE 60/50/40MHz for CPU CLK IN
 CLK/JOE 60/50/40MHz for CPU CLK IN
 CLK/JOE 60/50/40MHz for CPU CLK IN
 CLK/JOE 60/50/40MHz for CPU CLK IN

CPU



Legend: CPU Clock Signal Line Between CPU and P144 08T IN



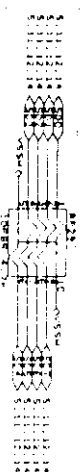
Legend: CPU Clock Signal Line Between CPU and P144 08T IN



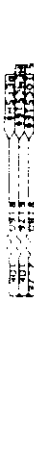
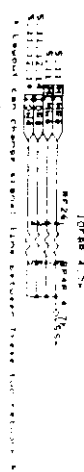
Legend: CPU Clock Signal Line Between CPU and P144 08T IN



Legend: CPU Clock Signal Line Between CPU and P144 08T IN



ISA

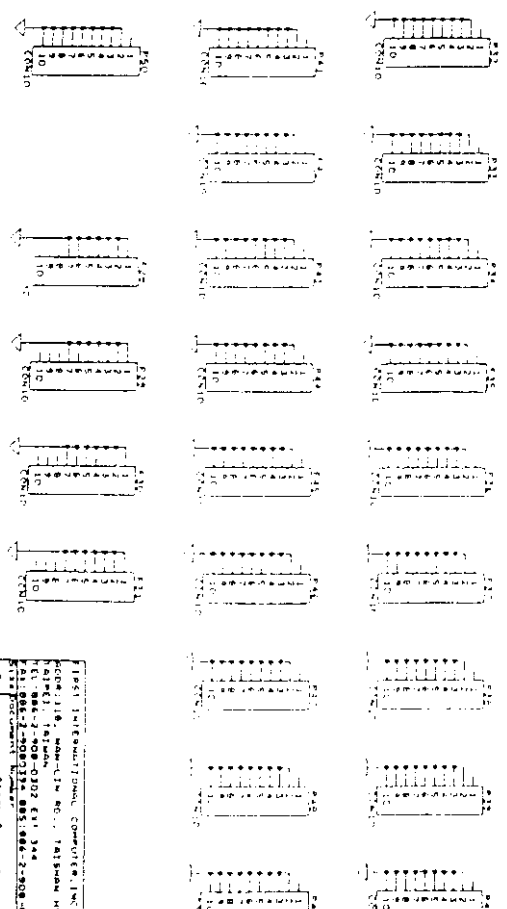
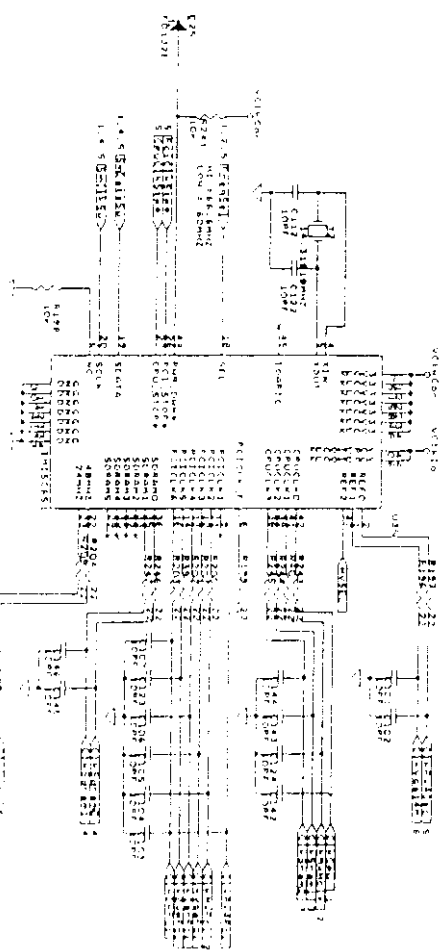


PCI CLOCK 50/40/33MHz

CLK/JOE 50/40/33MHz for P144 08T IN
 CLK/JOE 50/40/33MHz for P144 08T IN
 CLK/JOE 50/40/33MHz for P144 08T IN
 CLK/JOE 50/40/33MHz for P144 08T IN
 CLK/JOE 50/40/33MHz for P144 08T IN

MISC. CLOCK

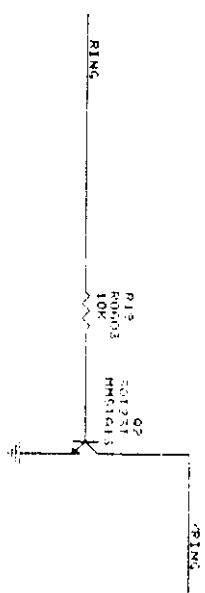
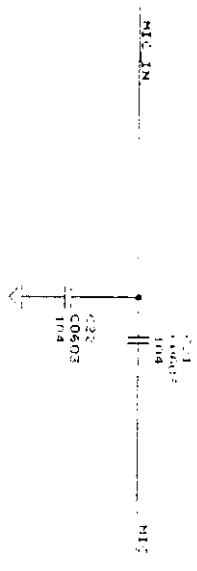
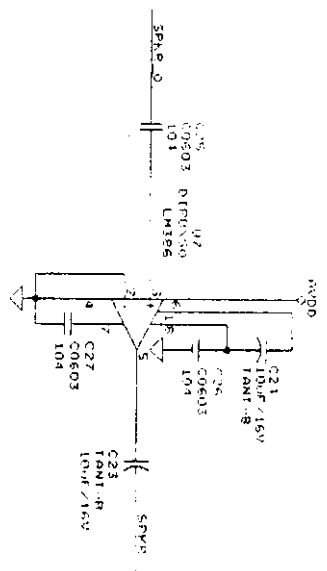
CLK/JOE 50/40/33MHz for P144 08T IN
 CLK/JOE 50/40/33MHz for P144 08T IN
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 CLK/JOE 50/40/33MHz for P144 08T IN



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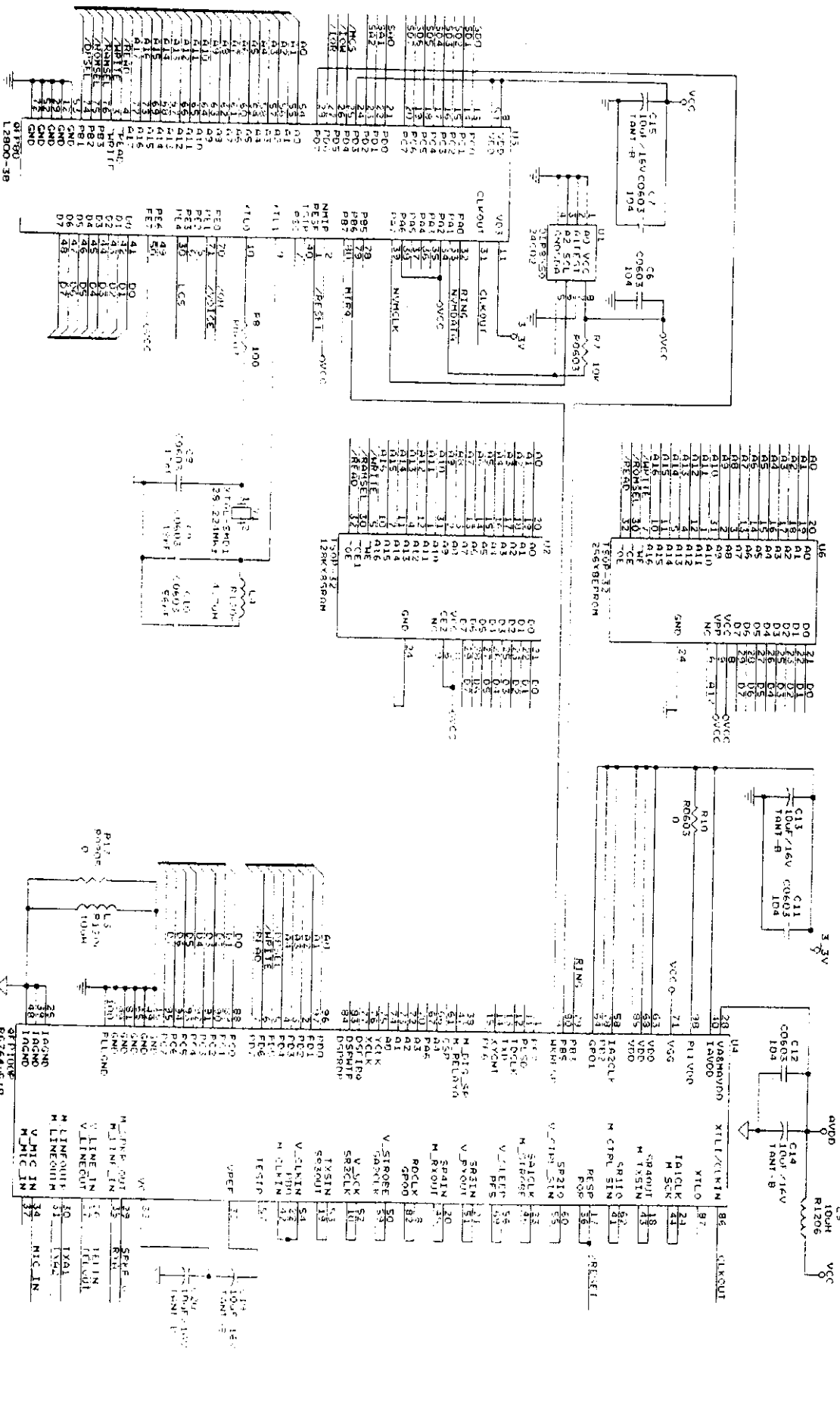
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SD1	U1	U2	U3	U4	U5	U6	U7	U8	U9	U10	U11	U12	U13	U14	U15	U16	U17	U18	U19	U20	U21	U22	U23	U24	U25	U26	U27	U28	U29	U30
SD2	U1	U2	U3	U4	U5	U6	U7	U8	U9	U10	U11	U12	U13	U14	U15	U16	U17	U18	U19	U20	U21	U22	U23	U24	U25	U26	U27	U28	U29	U30
SD3	U1	U2	U3	U4	U5	U6	U7	U8	U9	U10	U11	U12	U13	U14	U15	U16	U17	U18	U19	U20	U21	U22	U23	U24	U25	U26	U27	U28	U29	U30
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SD5	U1	U2	U3	U4	U5	U6	U7	U8	U9	U10	U11	U12	U13	U14	U15	U16	U17	U18	U19	U20	U21	U22	U23	U24	U25	U26	U27	U28	U29	U30
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SD7	U1	U2	U3	U4	U5	U6	U7	U8	U9	U10	U11	U12	U13	U14	U15	U16	U17	U18	U19	U20	U21	U22	U23	U24	U25	U26	U27	U28	U29	U30
SD8	U1	U2	U3	U4	U5	U6	U7	U8	U9	U10	U11	U12	U13	U14	U15	U16	U17	U18	U19	U20	U21	U22	U23	U24	U25	U26	U27	U28	U29	U30
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127365-100/2
SUNIM-30 M CON



AUDIO & TSA CONNECT

$F_{\text{form}} = \frac{\pi}{4} d^2 l$



MDP & DSP