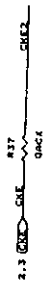


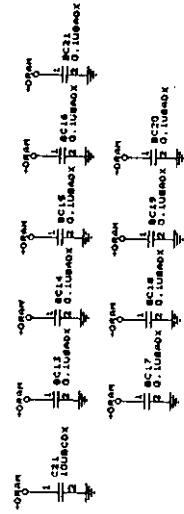
BANK 0/1

NU PARTS :
 R30, R31

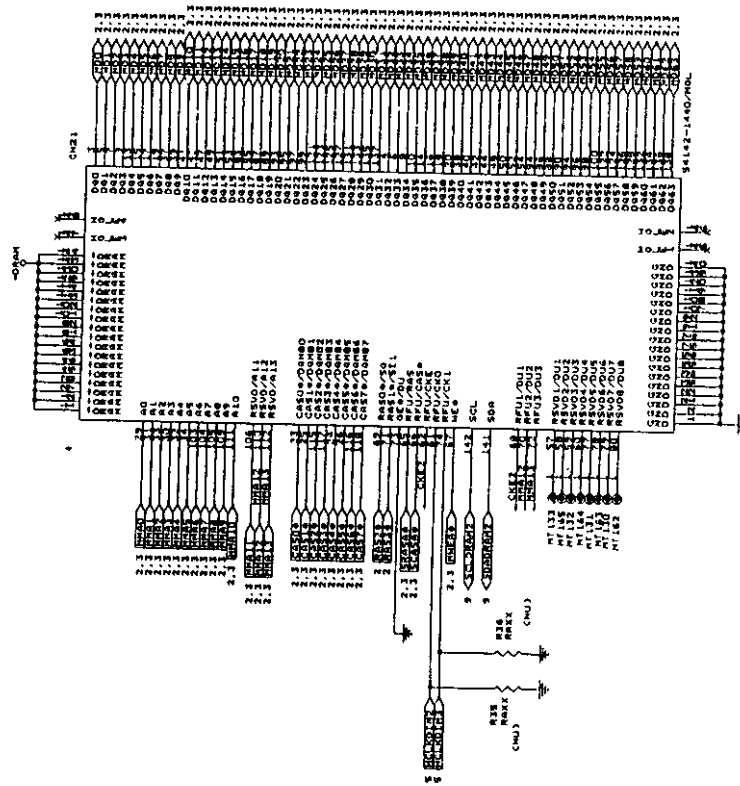
BANKS 0/1



BANK 2/3



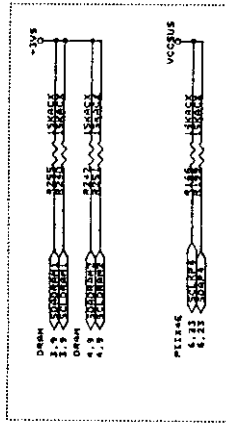
NU PARTS :
R35,R36



BANKS 2/3

設計處
87.8.19
機密文件

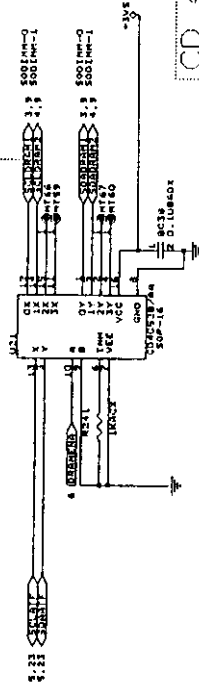
IIC PULL UP



NU PARTS :

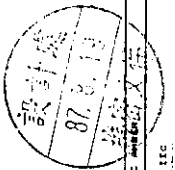
R255, R240, R242, R251

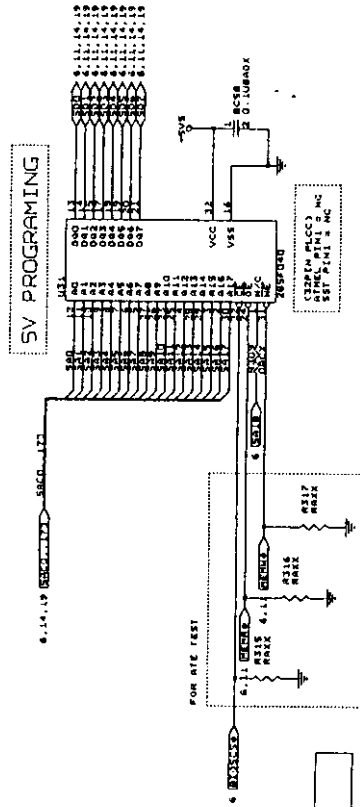
Read SDRAM-2 slot EPROM need program DRAMENA:H
 Read SDRAM-1 slot EPROM need program DRAMENA:L



CD 4052 True table

INH	A	B	
0	0	0	0X,0Y
0	1	0	1X,1Y
0	0	1	2X,2Y
1	0	1	3X,3Y

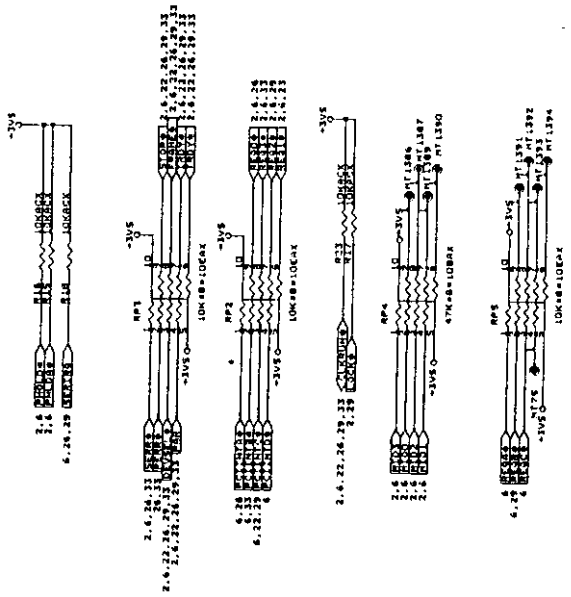




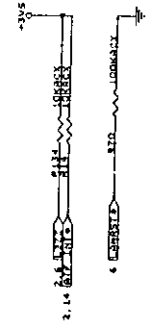
NU PARTS :
R315.R316.R317

NOTE: BIOS Socket 1002-B32I1R

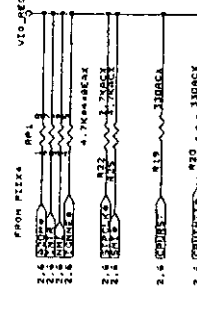
PCI BUS PULL RESISTOR



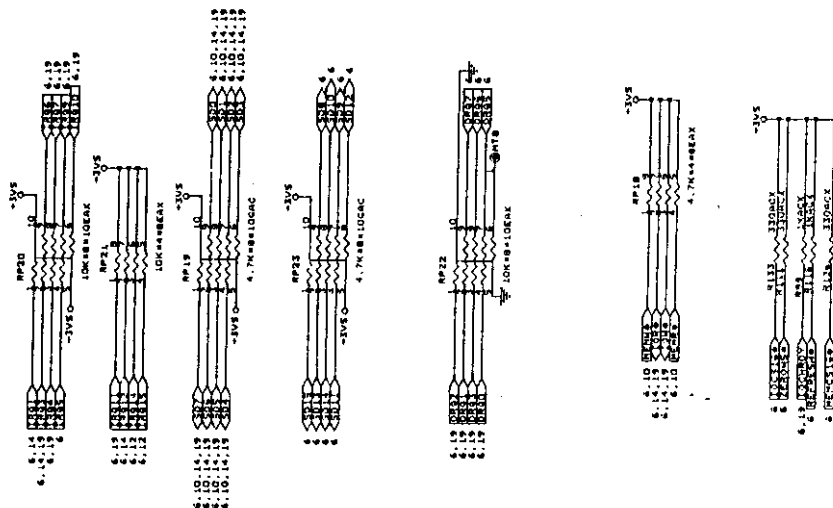
GPIO PULL RESISTOR



CPU BUS PULL RESISTOR



ISA BUS PULL RESISTOR



設計圖
 87.8.19
 機密文件

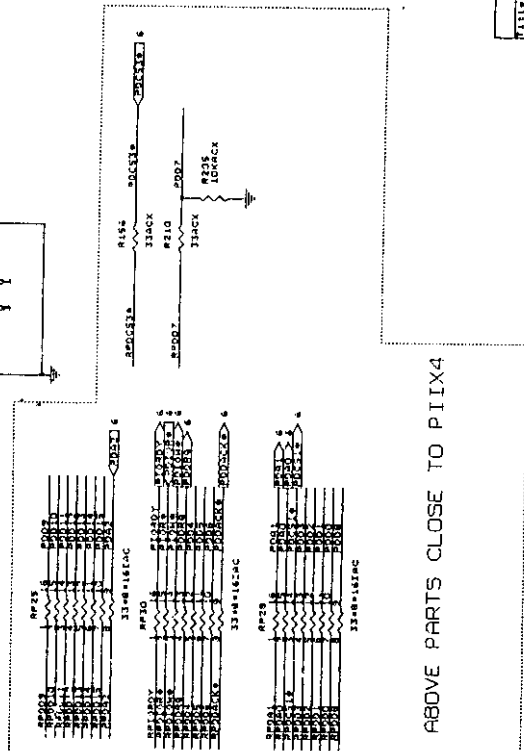
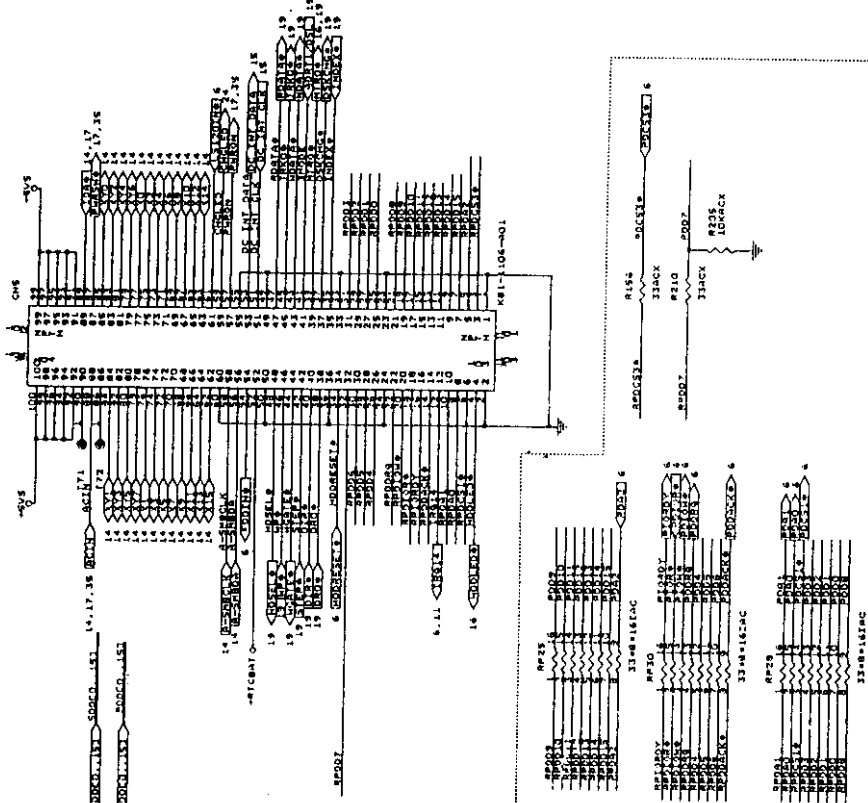
REV. 3 AND REV. 4
 7/3 (REV. 3) CANCEL 170 THEN CHANGE
 TO 100A

HDD connector
 CONN. PIN MAX 0.34

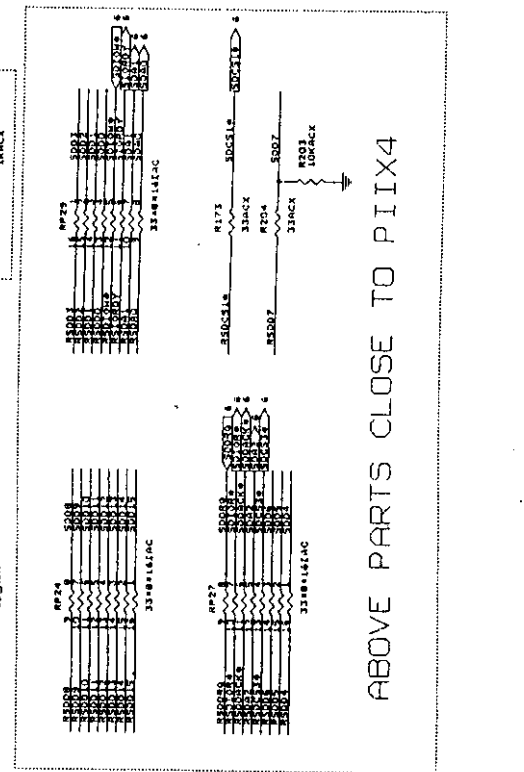
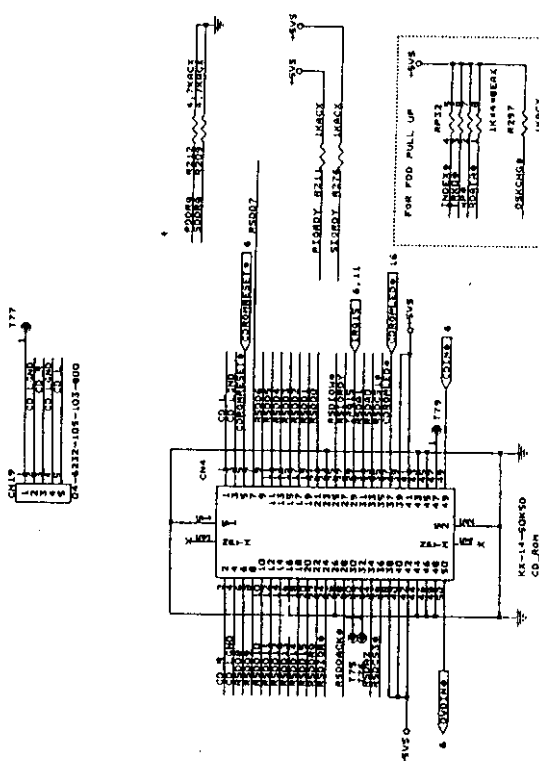
POWER
 L. 1.5V
 L. 2.0V MAX 1A
 C. 100V MAX 1.5A
 COMPACT 1.5A
 COMPACT 1.5A

FDD connector
 P00 MAX 0.75A
 CONN. PIN 1A

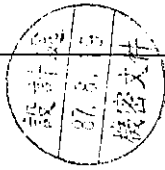
DEVICE connector circuit to I/O Board



ABOVE PARTS CLOSE TO PIIX4

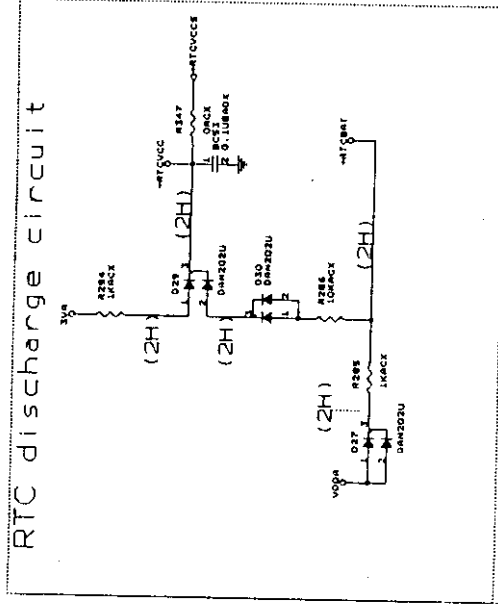


ABOVE PARTS CLOSE TO PIIX4



FILE
 HDD-CD-ROM-FDD-KVIB-GLI05-PAD-R1C7CF
 P1C-ANBER

REV. 0.4 AND REV. 0.5
 8/2 ADD RES7C03 THEN CONNECT TO -RT0VCC5



RTC discharge circuit

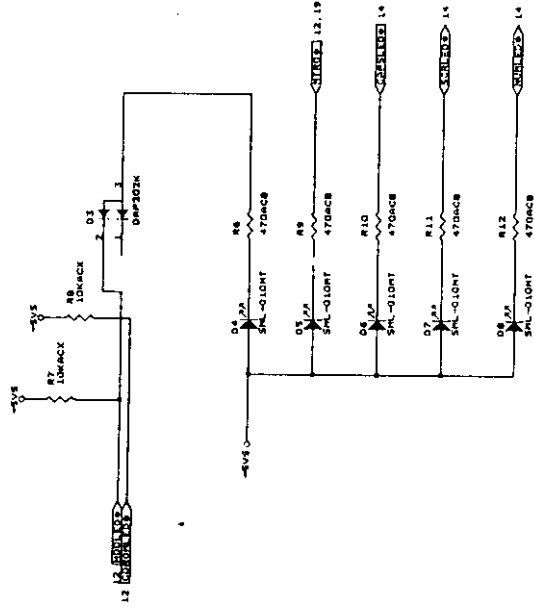
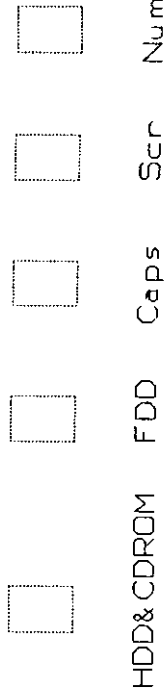
Under AC Adapter		
Power ON	Suspend	Power Off
Charge	Charge	Charge

Under Battery		
Power ON	Suspend	Power Off
Charge	Charge	Charge



REV.0.4 REV.0.5
 0/2 CUT DCELL TRACE

LED STATUS



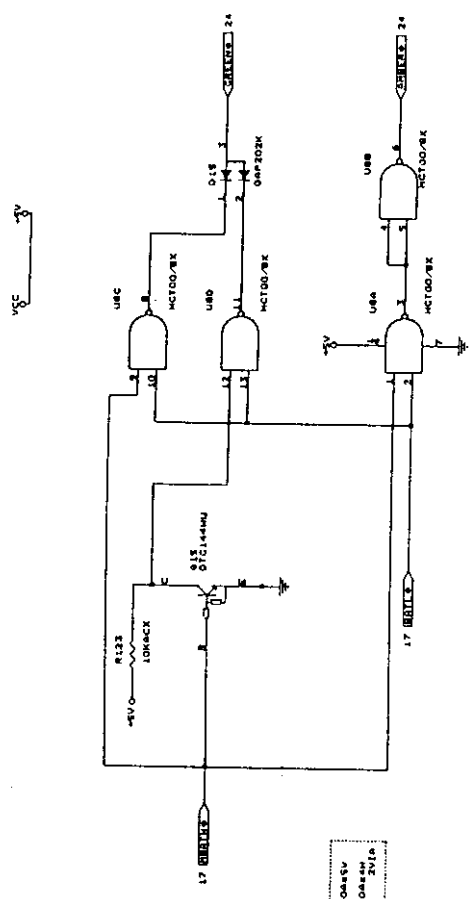
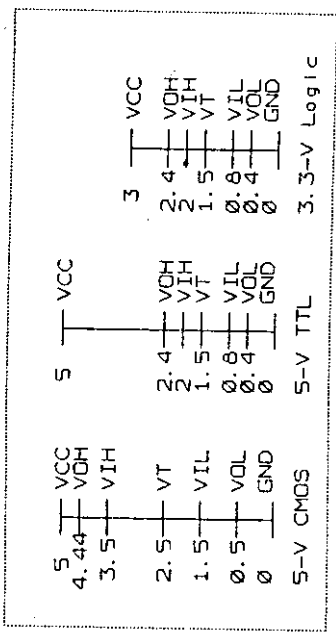
87.8.19
 機密文件
 機密
 機密

F114 LED BURNED CONNECTOR

REV.0.3 AND REV.0.4
7/7 U16 CHANGE PART TO LPTPPL

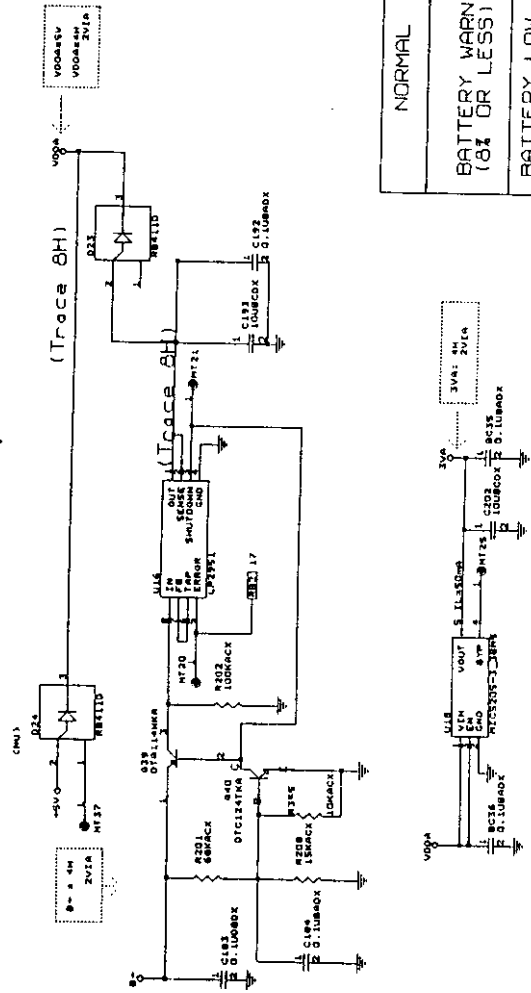
REV.0.4 AND REV.0.5
8/2 Q24 CHANGE TO NU PART
8/2 Q23 PART CHANGE TO RB411D
8/2 U16(C) ADD R235
8/2 U16(C) ADD NODES R22
8/2 U16(C) CONNECT TO 800CC

RB411D
Forward voltage: V160, 2V0, 3V0, 4V0, 5V0, 6V0, 7V0, 8V0, 9V0, 10V0



POWER STATUS LED TRUTH TABLE :

	UNDER AC		GREEN#	AMBER#
	MBATW#	BATL#		
NORMAL	1	1	0	1
BATTERY WARNING (8% OR LESS)	1	1	0	1
BATTERY LOW (3% OR LESS)	1	1	0	1
	UNDER AC		GREEN#	AMBER#
	MBATW#	BATL#		
NORMAL	1	1	0	1
BATTERY WARNING (8% OR LESS)	0	1	0	0
BATTERY LOW (3% OR LESS)	1	0	1	0

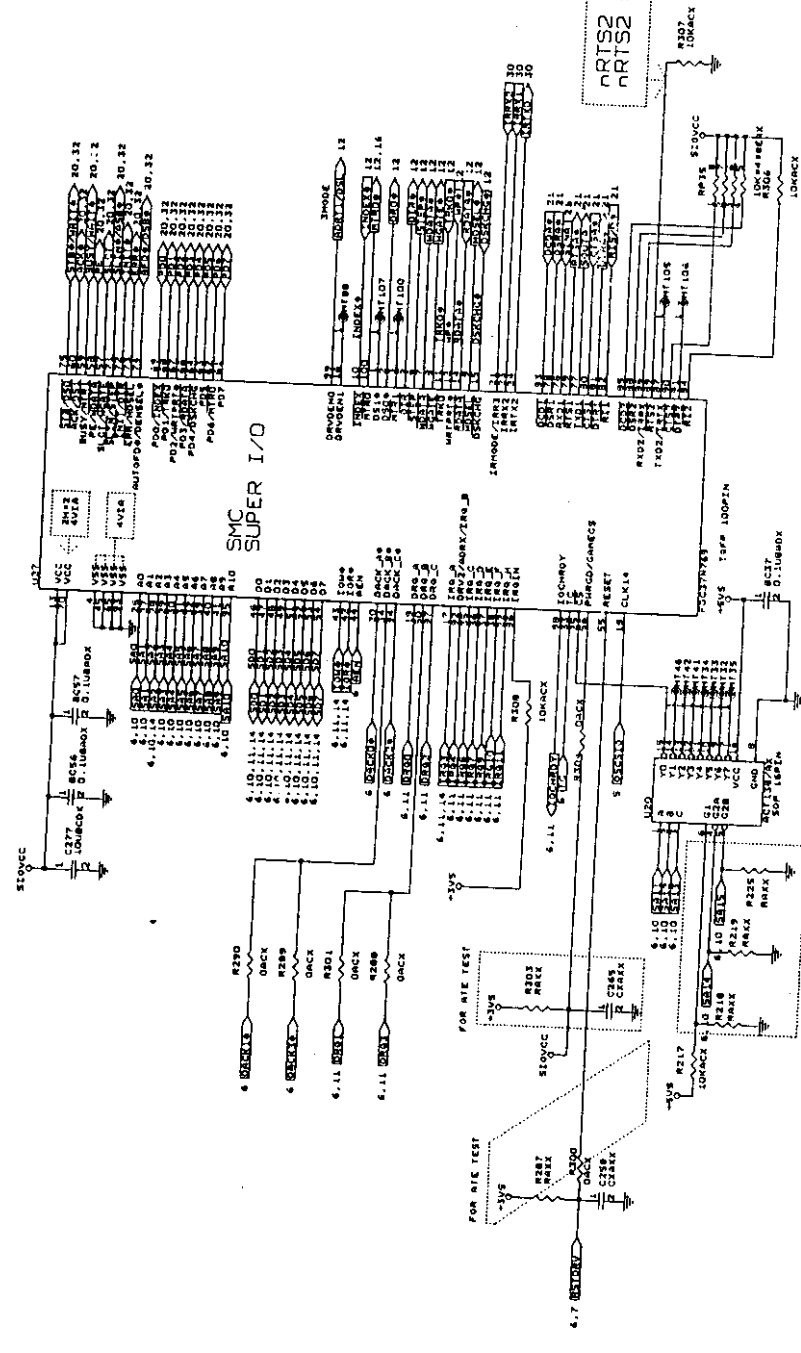


NU PARTS:

D24

設計處
87.8.19
機密文件

REC AMBER
REC POWER

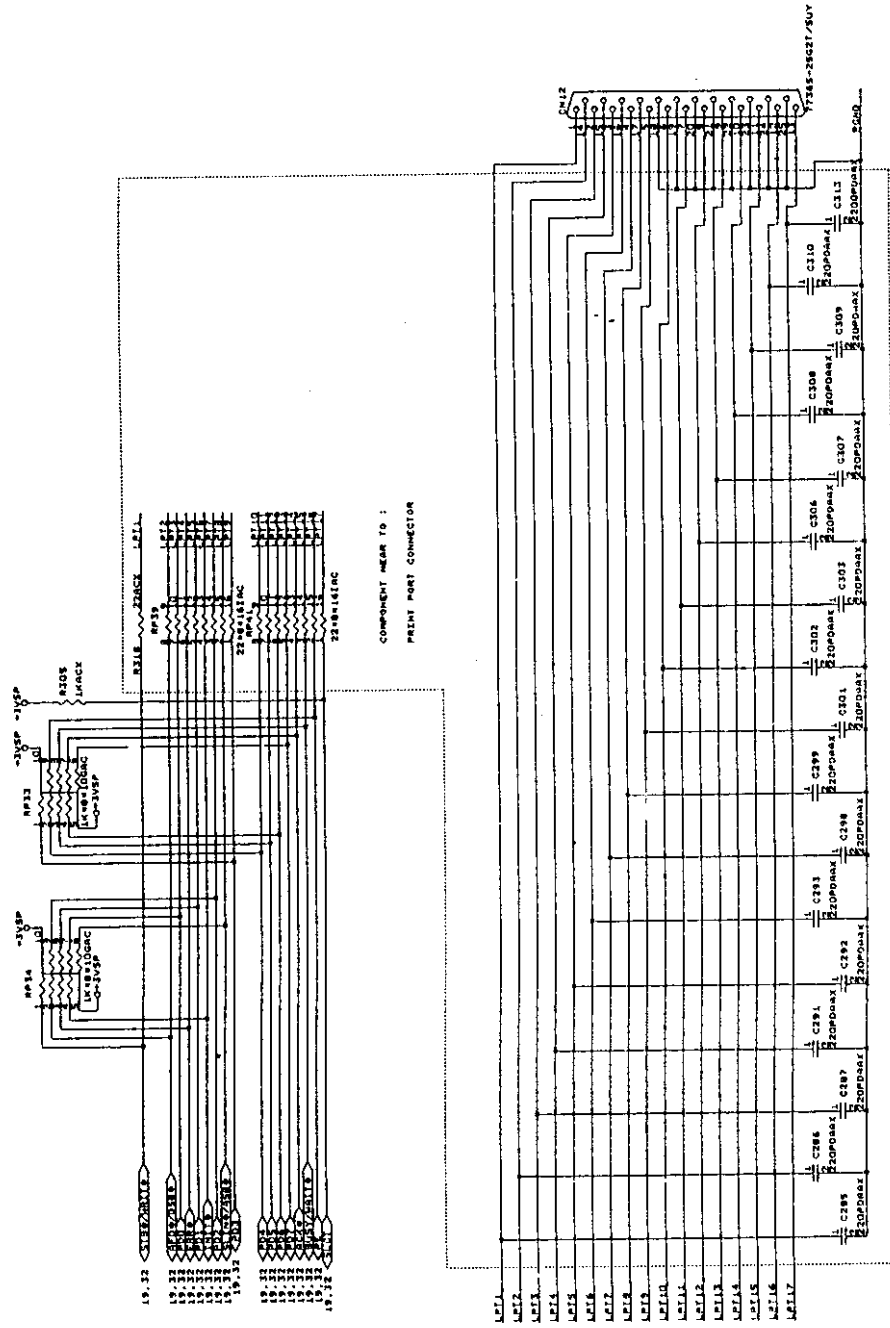
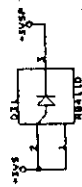


nRTS2 = 0: Index base I/O address 3F0
 nRTS2 = 1: Index base I/O address 370

NU PARTS :
 R289, R288, R287, C258, R303, C265, R218, R219, R225

1114 SUPER I/O 3827C666PRLV
 PEC NUMBER
 87.8.10
 機密文件

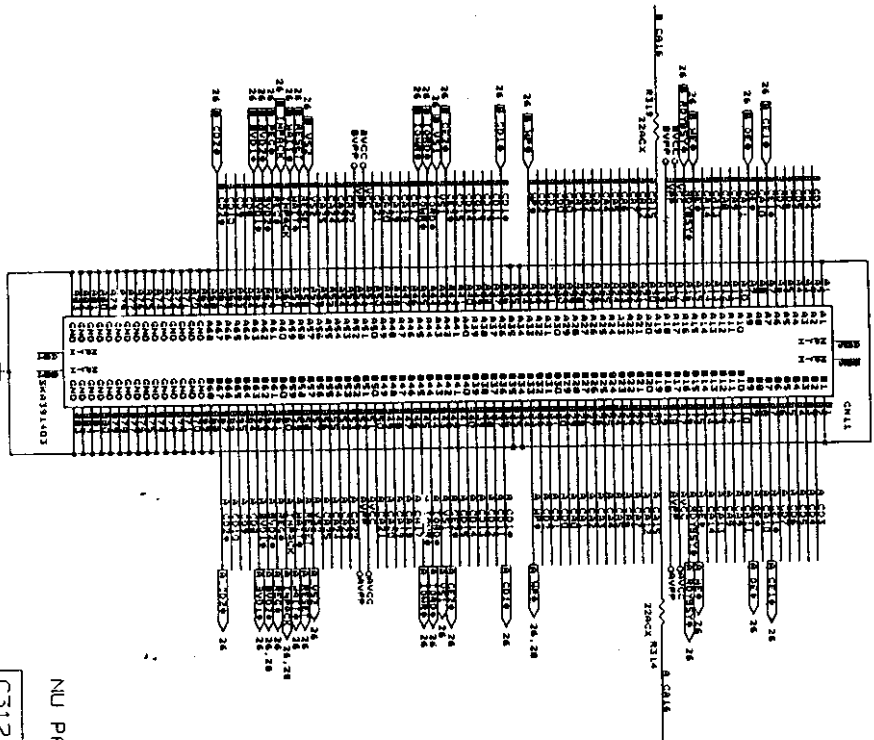
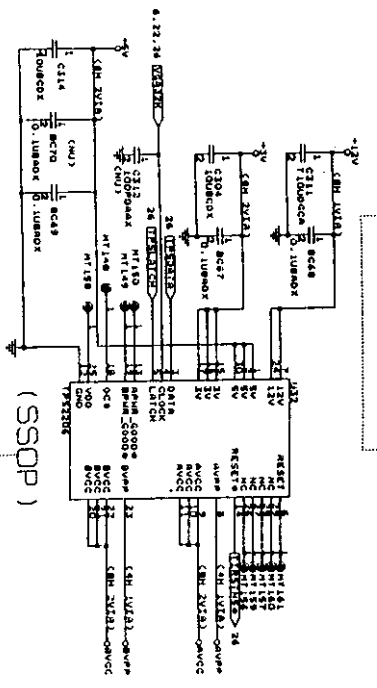
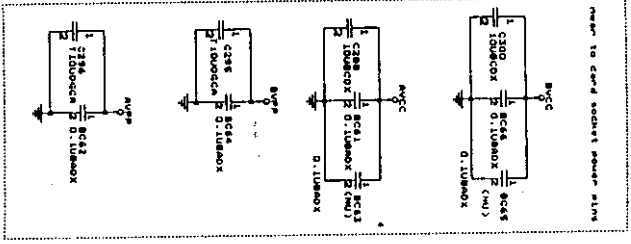
PRINT PORT
STANDARD CIRCUIT



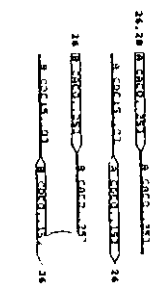
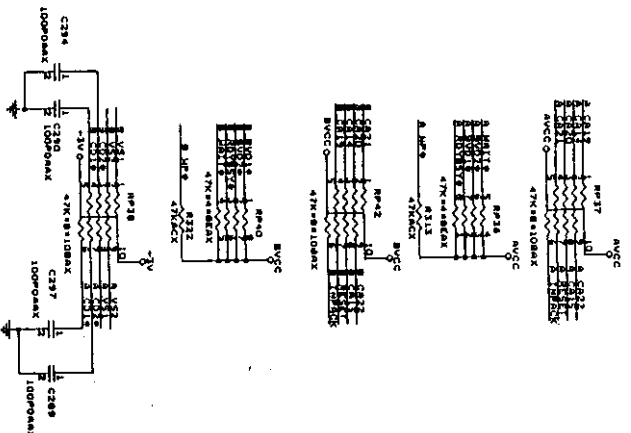
COMPONENT HEATS TO :
PRINT PORT CONNECTOR

設計處
87.8.19
機密文件

PEC ANKER
PARALLEL PORT
LETT



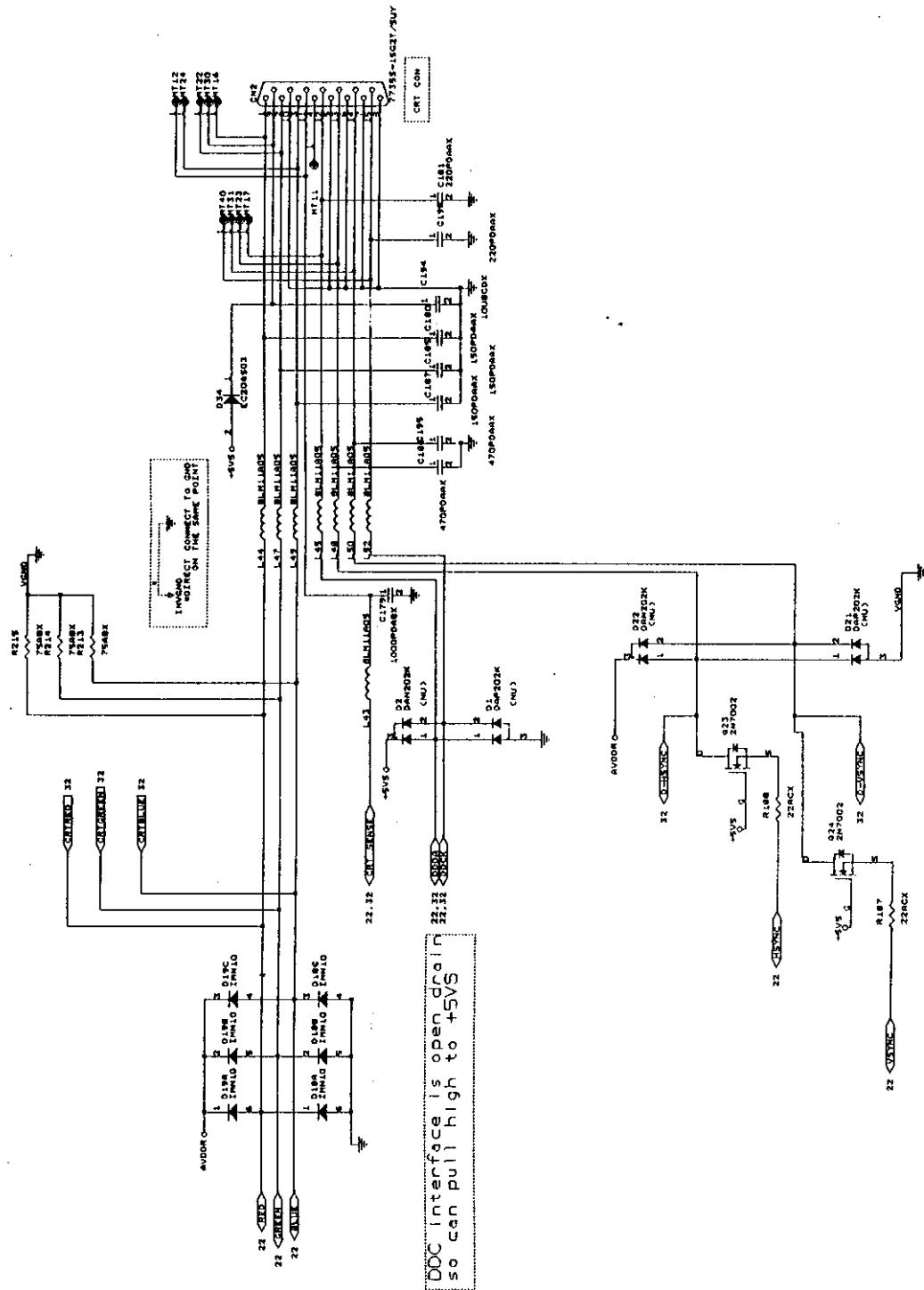
NU PARTS :
 C312



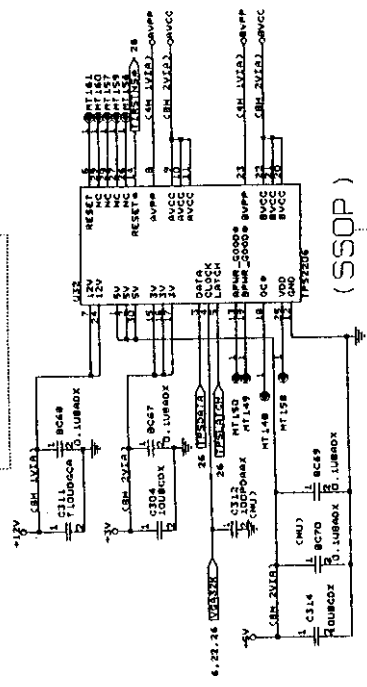
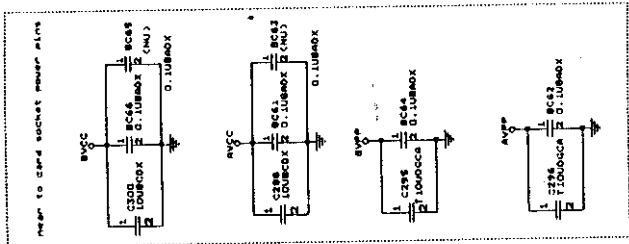
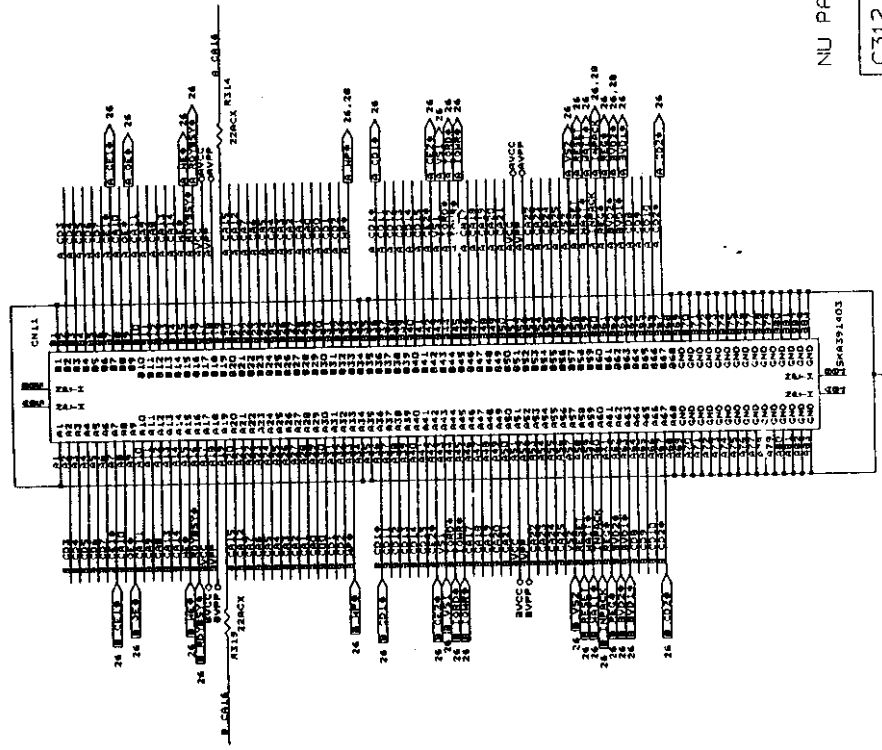
PTC NUMBER
 CARD BUS SOCKET



REV. 0.4 REV. 0.5
 4/78 PL CHANGE TO DISK/ELECTRONICS



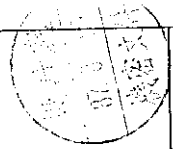
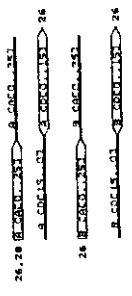
REV0.2 -> REV0.3
6/17 C299, C294, CELL CHANGE TO T10UBOCC



(SSOP)

U21 use SSOP package

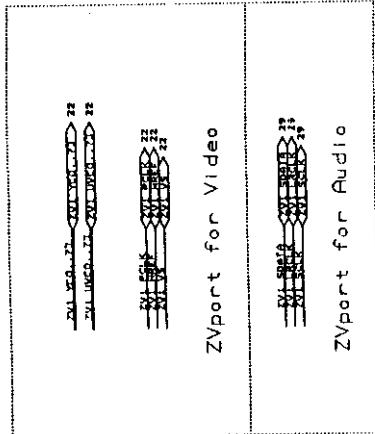
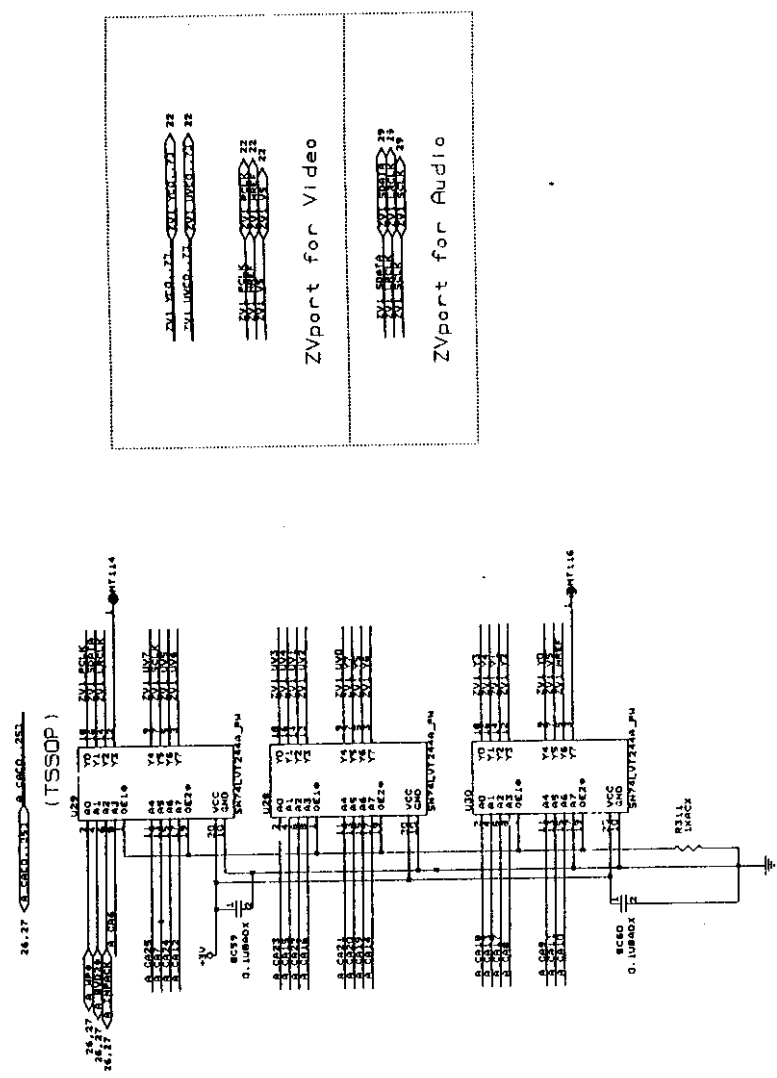
NU PARTS :
C312

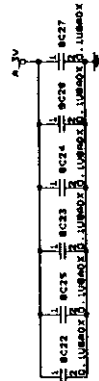


FILE NO. 01
CARD BUS SOCKET
REV

設計處
87.8.19
機密文件

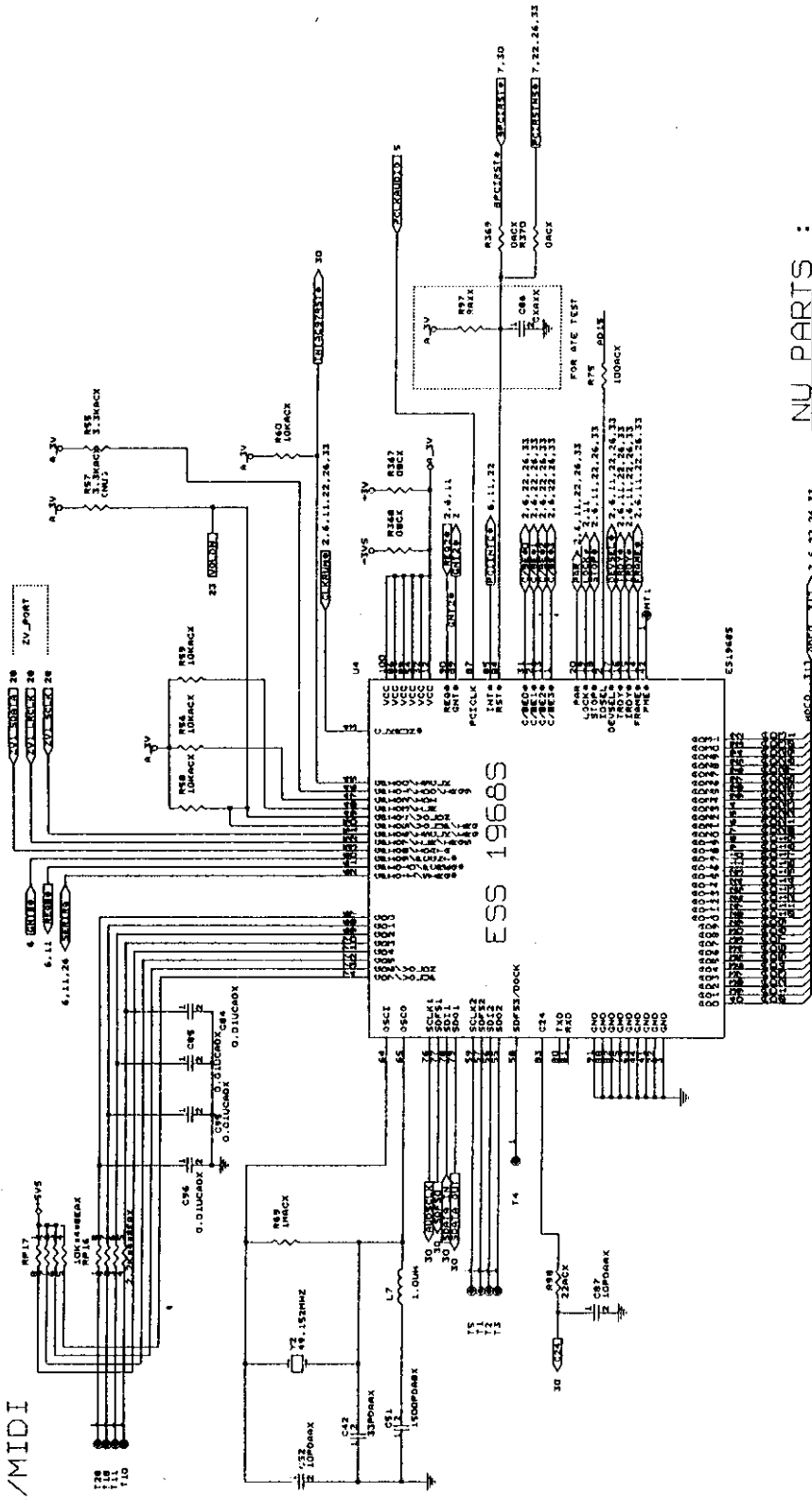
FIG NUMBER
ZVPORT
Drawing Number
REV





JOYSTICK/MIDI

REV. 0.1 REV. 0.2 MODIFICATION
 5/1 V2 CHANGE TYPE THEN CANCEL RT4,RT5
 REV.0.4 REV.0.5
 5/2 REV CHANGE TO NU PART
 5/2 UAC49,RS7,RS5,RS0,UAC100...),RS7 CONNECT TO A_3V
 5/2 UAC49,RS0 SHORT THEN ADD MODULE VOLDM
 5/2 UAC100...),ADD R349,RS57 TO -5VS,-3V
 5/2 UAC68) RS4 CANCEL THEN SHORT AND ADD R349,RS70



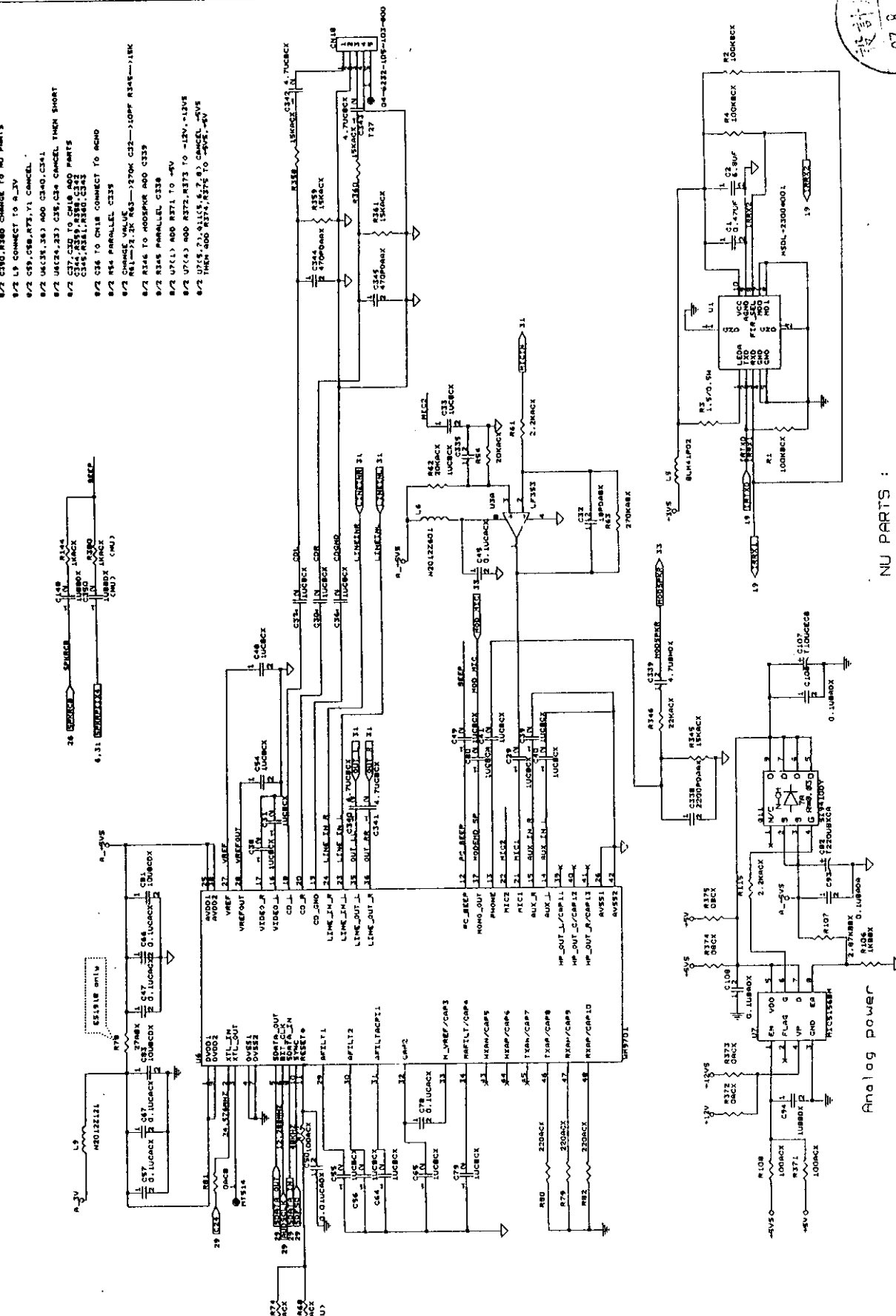
NU PARTS :
 R97, C86, R57

REV. 0.1 REV. 0.2 MODIFICATION



REV. 0.2 → REV. 0.3
 6/17 MODIFIED A00 R344, R345 22K
 REV. 0.4 → REV. 0.5

- 6/2 C170, R380 CHANGE TO NU PARTS
- 6/2 L9 CONNECT TO A-1V
- 6/2 C59, C58, R73, V1 CANCEL
- 6/2 U6C24, R31, R30, C34, C31
- 6/2 U6C24, R31, C35, C34 CANCEL THEN SHORT
- 6/2 C24, C23, R31, R30, C34, C31 PARTS
- 6/2 C16 TO C18 CONNECT TO A0M0
- 6/2 R54 PARALLEL C133
- 6/2 CHANGE VALUE
 R81 → 2.2K R63 → 270K C32 → 100PF R345 → 15K
- 6/2 R346 TO MODIFIED A00 C133
- 6/2 R345 PARALLEL C138
- 6/2 U7(1) MOD R371 TO -12V, -12V5
- 6/2 U7(1) MOD R372, R373 TO -12V, -12V5
- 6/2 U7(1) MOD R374, R375 TO -12V, -12V5
- 6/2 U7(1) MOD R376, R377 TO -12V, -12V5



設計
 87.8.19
 敬請文件

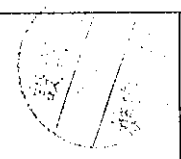
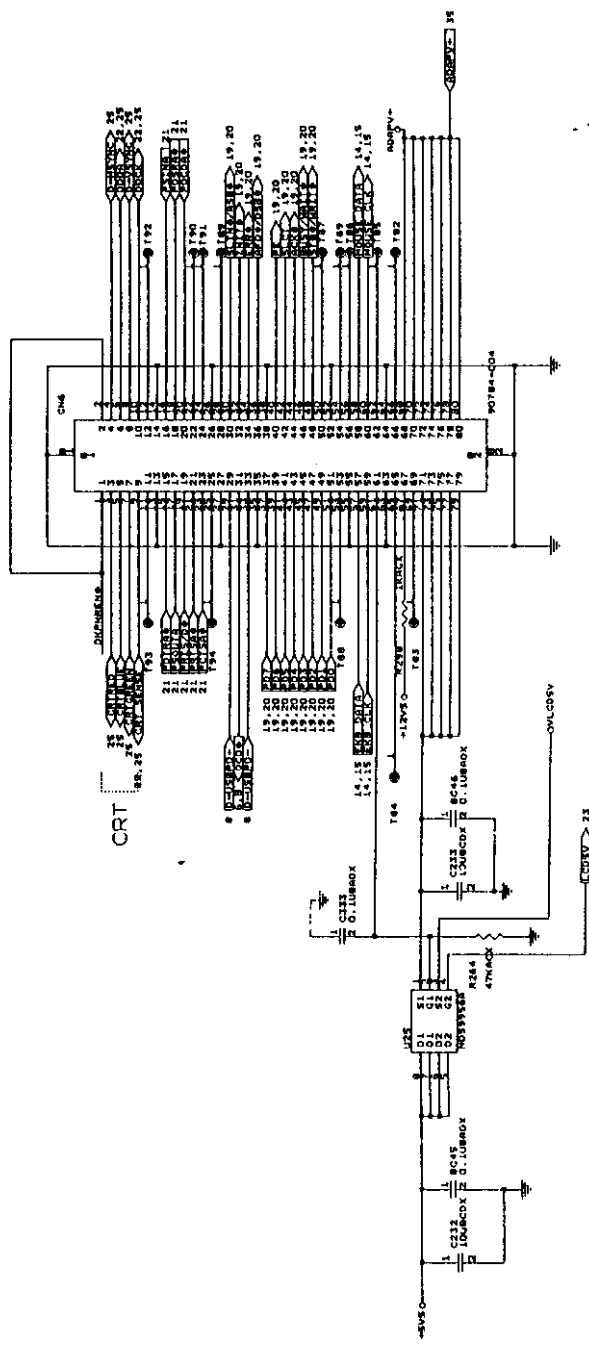
NU PARTS :
 R78, C59, C58, Y1, R73
 R68, C78, C79, R80
 R79, R82, C350, R350

FILE: Garnet audio board M07 CaseC
 SIZE: DOCUMENT NUMBER: 240075E.504 (FOR V03.0 A.)
 PIC AMBER

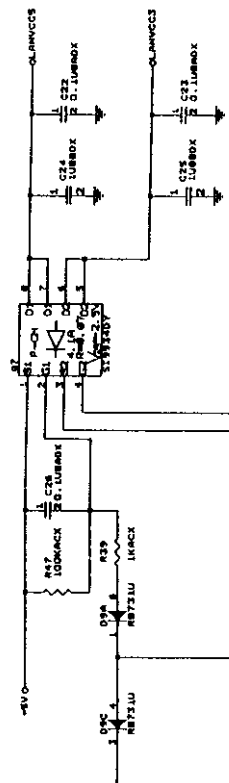
ESS AUDIO
 +5V : NU
 R108, R373,
 R374, R368,
 R369
 +5VS : NU
 R371, R372,
 R375, R367,
 R370

DOCKING CONN

REV. 1
 6/17 USE PIN2 AND 0.1UF TO GND
 6/17 USE PIN3 CHANGE TO VLSBY
 6/17 USE PIN4 CHANGE TO VLSBY
 6/17 ONE PIN1, 75, 76, 77, 79 SHORT

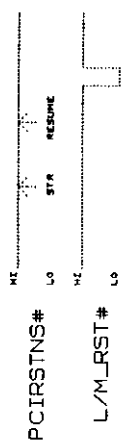


MODEM/LAN CONNECTOR



REV. 0.1 REV. 0.2 MODIFICATION
 5/78 CANCEL 54.R41.011,REV.018,54.R42.02
 5/78 CANCEL 54.R41.011,REV.018,54.R42.02
 54.82 CHANGE TO D.11111111
 54.83 CHANGE TO D.11111111
 54.84 CHANGE TO D.11111111
 54.85 CHANGE TO D.11111111
 54.86 CHANGE TO D.11111111
 54.87 CHANGE TO D.11111111
 54.88 CHANGE TO D.11111111
 54.89 CHANGE TO D.11111111
 54.90 CHANGE TO D.11111111
 54.91 CHANGE TO D.11111111
 54.92 CHANGE TO D.11111111
 54.93 CHANGE TO D.11111111
 54.94 CHANGE TO D.11111111
 54.95 CHANGE TO D.11111111
 54.96 CHANGE TO D.11111111
 54.97 CHANGE TO D.11111111
 54.98 CHANGE TO D.11111111
 54.99 CHANGE TO D.11111111
 55.00 CHANGE TO D.11111111
 578 ADD MODULE L/M_RST#,PWR

STR RESET TIMING



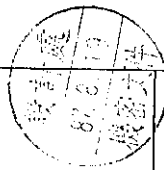
LAN/MODEM POWER CONTROL TRUTH TABLE

POWER	STR	MODEM/PC	ACEN	L/MO_TYPE	LANCTL0
1	1	0	1	1	0
1	0	0	1	1	0
0	1	1	1	1	1
1	0	1	1	1	1
1	1	0	1	1	0
0	1	1	0	1	1
1	1	0	1	0	0
1	0	0	1	0	0
0	1	1	0	1	1
1	0	1	1	0	1
1	1	0	0	0	0
1	0	0	0	0	0
0	1	1	0	0	1
1	0	1	0	0	1

設計
 87.8.19
 機密文件

PCB NUMBER
 MODEM/LAN CONN
 54.82 CHANGE TO D.11111111
 54.83 CHANGE TO D.11111111
 54.84 CHANGE TO D.11111111
 54.85 CHANGE TO D.11111111
 54.86 CHANGE TO D.11111111
 54.87 CHANGE TO D.11111111
 54.88 CHANGE TO D.11111111
 54.89 CHANGE TO D.11111111
 54.90 CHANGE TO D.11111111
 54.91 CHANGE TO D.11111111
 54.92 CHANGE TO D.11111111
 54.93 CHANGE TO D.11111111
 54.94 CHANGE TO D.11111111
 54.95 CHANGE TO D.11111111
 54.96 CHANGE TO D.11111111
 54.97 CHANGE TO D.11111111
 54.98 CHANGE TO D.11111111
 54.99 CHANGE TO D.11111111
 55.00 CHANGE TO D.11111111
 578 ADD MODULE L/M_RST#,PWR

REV.0.4 (REV) REV.0.5
 8/7 836(4) MOD R27A.C349 THEN C349 IS NU PART



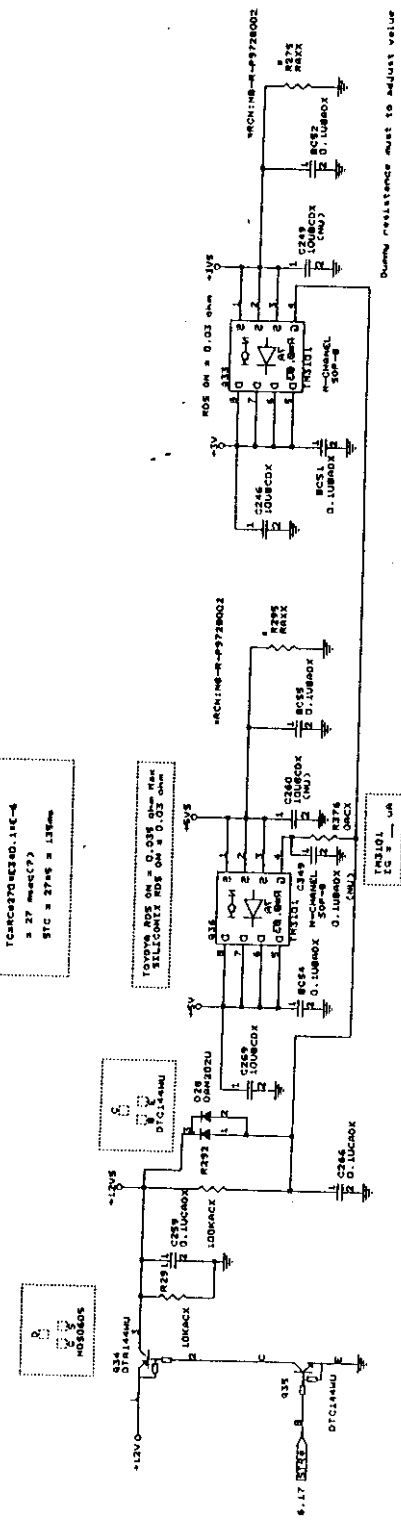
PEC NUMBER
 7275
 POWER PLANE
 87619
 DC:IF.SCH (PCB VER:0.6)

NU PARTS :
 R295, R275, C349

MODE transfer

ON	SIR	STD	ME	OFF
+5V	5	0	0	0
+5VS	5	0	0	0
+12V	12	0	0	0
+12VS	12	0	0	0
+3V	3.3	0	0	0
+3VS	3.3	0	0	0
VDDA	5	5	0	0
BM+	10~20	10~20	0	0
B+	10~20	10~20	0	0
VIO_REGUL	2.5	0	0	0
+DRAM	3.3	3.3	0	0
3VA	3.3	3.3	3.3	0
VCCBUS	3.3	3.3	3.3	0
+RICKVCCS	3.3	3.3	3.3	3.3

POWER PLANE



Dummy resistance must to adjust value

12V 844.4uA (ON ?)
 TCRACR370R330.14E-4
 R 37 mACCP ?
 BTC = 2746 = 186m

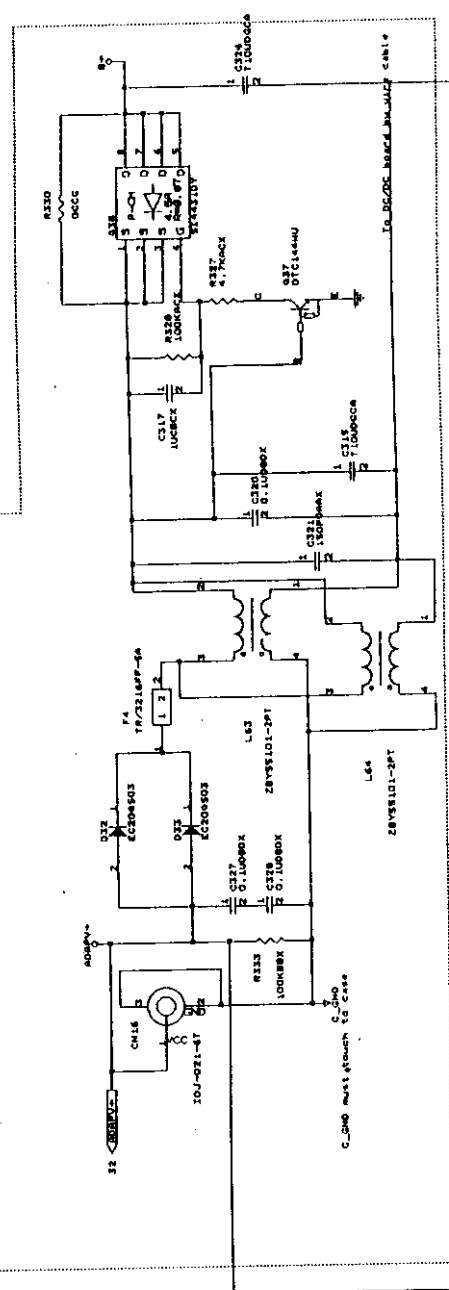
12V 844.4uA (ON ?)
 TCRACR370R330.14E-4
 R 37 mACCP ?
 BTC = 2746 = 186m

IM3101
 IC V - UA

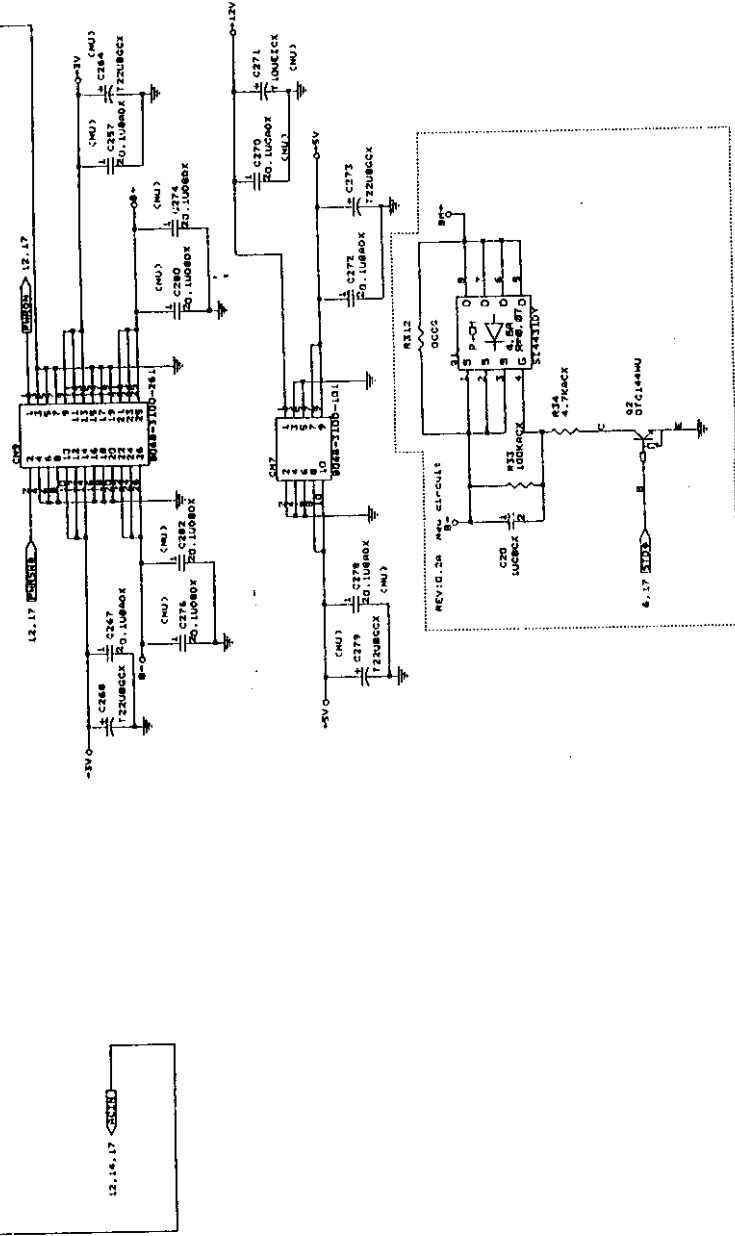
REV0.2 -> REV0.3

6/17 CH16 PINE.3 SHORT
6/17 CH15, C325 CHANGE TO 1000PFC

INRUSH current circuit



DC/B to M/B connector



NU PARTS :

- C321, C320, C315, C317
- R326, R327, R37, Q38
- C326, C20, R33, R34, Q2, O1
- C274, C280, C282, C276

ITEM FILE NAME

- 01 : LINK.SCH LINK SHEET
- 02 : MMIO.SCH
- 03 : DRAM-1.SCH
- 04 : DRAM-2.SCH
- 05 : CLCK.SCH
- 06 : PIIX4.SCH
- 07 : PLET.SCH
- 08 : USB.SCH
- 09 : IIC.SCH
- 10 : BIOS.SCH
- 11 : RESISTOR.SCH
- 12 : DEVICEN.SCH
- 13 : RTC.SCH
- 14 : KB.SCH
- 15 : P32.SCH
- 16 : LEDCN.SCH
- 17 : PIC.SCH
- 18 : PIPWR.SCH
- 19 : 669.SCH
- 20 : PRINT.SCH
- 21 : COM.SCH
- 22 : NSVGA.SCH
- 23 : NSPOWER.SCH
- 24 : NSLCDN.SCH
- 25 : NSCRTN.SCH
- 26 : 1220.SCH
- 27 : ICPDR1.SCH
- 28 : AUDM2.SCH
- 29 : GACODEC.SCH
- 30 : IGAMPJACK.SCH
- 31 : IDCKING.SCH
- 32 : MDLNCN.SCH
- 33 : IDC11F.SCH
- 34 : IDC21F.SCH

DESCRIPTION

- AMBER.SCH LINK SHEET
- MMIO.CONNECTOR
- SODIMM.CONNECTOR BANK 0,1
- SODIMM.CONNECTOR BANK 2,3
- CLOCK.GENERATOR
- PIIX4E.SOUTH BRIDGE
- PLET.LOGIC IC / FAN CONNECTOR
- USB.CONNECTOR
- IIC.BUS SELECTER
- FLASH ROM (2Mb BIOS)
- PULL UP / PULL DOWN RESISTOR
- HDD.COROM.LS120,FDD/IF
- RTC.BATTERY
- KEYBOARD.CONTROLLER
- P32 / GLIDE PAD / INTERNAL KB CONN
- LED.CONNECTOR
- MICRO.CONTROLLER (PIC 16C62-04 SSOP)
- MICRO.CONTROLLER POWER
- SUPER I/O SMC 37N769
- PARALLEL.PORT
- SERIAL.PORT
- COM.PORT
- NSVGA.CONTROLLER - NMGS
- VGA.POWER
- VGA.CONNECTOR/LVDS TRANSFER
- CRT.CONNECTOR
- CARD.BUS CONTROLLER (TI1220)
- CARD.BUS SOLT
- CARD.BUS ZV PORT
- AUDM2.CHIIP
- GACODEC
- AUDIO AMP & JACK
- DOCKING.CONNECTOR
- MODEM/LAN.CONNECTOR
- DC-DC.REGULATOR
- DC-DC.CONNECTOR

RESISTOR

EX: 1K X X X X
 CAPACITOR VALUE
 1. CAPACITOR VALUE
 2. SIZE: D, 18, 25, 32, 48, 60, 80, 100, 120, 150, 180, 220, 250, 330, 470, 560, 680, 820, 1000
 3. PERCENTAGE: 0.1K, 0.15K, 0.22K, 0.33K, 0.47K, 0.68K, 1.0K, 1.5K, 2.2K, 3.3K, 4.7K, 6.8K, 10K
 4. VENDOR: A: ROHM, B: TAIYO YUDEN, C: PANASONIC, D: KEMET, E: AVX, F: VISHAY, G: TDK

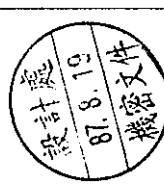
CAPACITOR

EX: 7P MO-CAP X X X X
 CAPACITOR VALUE
 1. CAPACITOR VALUE
 2. VOLTAGE: A: 1.5V, B: 1.8V, C: 2.5V, D: 5V, E: 6V, F: 12V, G: 16V, H: 18V, I: 20V, J: 25V, K: 30V, L: 35V, M: 50V, N: 60V, O: 75V, P: 100V
 3. PERCENTAGE: A: 0.1, B: 0.2, C: 0.5, D: 1, E: 2, F: 5, G: 10, H: 20, I: 50, J: 100
 4. VENDOR: A: ROHM, B: TAIYO YUDEN, C: PANASONIC, D: KEMET, E: AVX, F: VISHAY, G: TDK, H: SANDEN, I: TECCO, J: TOKI

Layout note:

ISA	[1]	Component side
VCC	[2]	
PCI	[3]	
CLOCK/MEMORY/LCD	[4]	
GND	[5]	
ISA	[6]	Solder side

Handwritten notes in Chinese: 零件表, 10/8



ITEM FILE NAME

- 01 : DMA Channel 1, Device
- 02 : DMA Channel 2, Device
- 03 : DMA Channel 3, Device
- 04 : DMA Channel 4, Device
- 05 : DMA Channel 5, Device
- 06 : DMA Channel 6, Device
- 07 : DMA Channel 7, Device
- 08 : DMA Channel 8, Device
- 09 : DMA Channel 9, Device
- 10 : DMA Channel 10, Device
- 11 : DMA Channel 11, Device
- 12 : DMA Channel 12, Device
- 13 : DMA Channel 13, Device
- 14 : DMA Channel 14, Device
- 15 : DMA Channel 15, Device

DESCRIPTION

- IR0 Channel 1, Description
- IR0 Channel 2, Description
- IR0 Channel 3, Description
- IR0 Channel 4, Description
- IR0 Channel 5, Description
- IR0 Channel 6, Description
- IR0 Channel 7, Description
- IR0 Channel 8, Description
- IR0 Channel 9, Description
- IR0 Channel 10, Description
- IR0 Channel 11, Description
- IR0 Channel 12, Description
- IR0 Channel 13, Description
- IR0 Channel 14, Description
- IR0 Channel 15, Description

REQ	CHIP
REQ0	TI (1221)
REQ1	VGA
REQ2	AUDIO ESS
REQ3	MODEM/LAN
REQ4	
REQ5	

REQ	CHIP
REQA	N.C.
REQB	ESS
REQC	

DMA Channel	Device
DMA0	FIR (SAS9618, Rev. 057, 00132) (MODEM/LAN)
DMA1	ECP
DMA2	FLOPPY DISK
DMA3	AUDIO
DMA4	(Cascade)
DMA5	Unused
DMA6	Unused
DMA7	Unused

Handwritten notes: 設計, 8/10