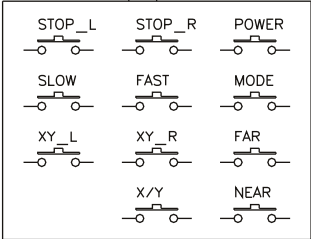


- 1ch 429.2500MHz
- 2ch 429.2625MHz
- 3ch 429.3000MHz
- 4ch 429.3625MHz
- 5ch 429.4375MHz
- 6ch 429.5250MHz
- 7ch 429.6500MHz
- 8ch 429.6750MHz
- 9ch 429.7375MHz



DRAW	CHECK	APPROVE	Title
A.Yamashita			RC-30 CPU BLOCK DIAGRAM
99.10.23			ALINCO INC.
			Sheet 1 of 1