

Figure 9 Battery Bottom

P/N: SPI-0029-001 A IC: 864D-DEC08

SW: R6 FPGA: V1

MOD: SPI, 8 Channel, Deco S/N: **65900031**

FCC ID: E09DEC08

(492H)

THIS DEVICE COMPLIES WITH PART 15 OF FCC RULES. OPERATION IS SUBJECT TO THE FOLLOWING TWO CONDITIONS: (1) THIS DEVICE MAY NOT CAUSE HARMFUL INTERFERENCE, AND (2) THIS DEVICE MUST ACCEPT ANY INTERFERENCE RECEIVED, INCLUDING INTERFERENCE THAT MAY CAUSE UNDESIRED OPERATION.

Figure 10 Label