

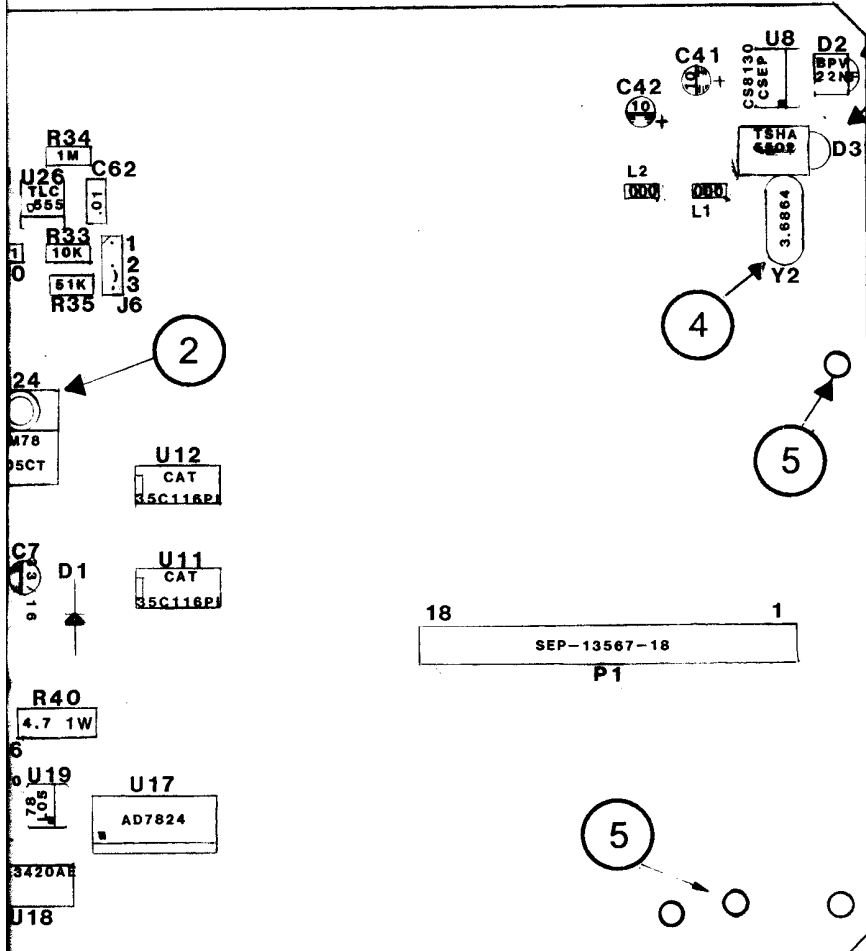
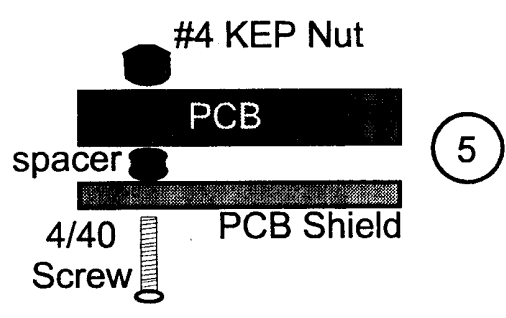
4

3

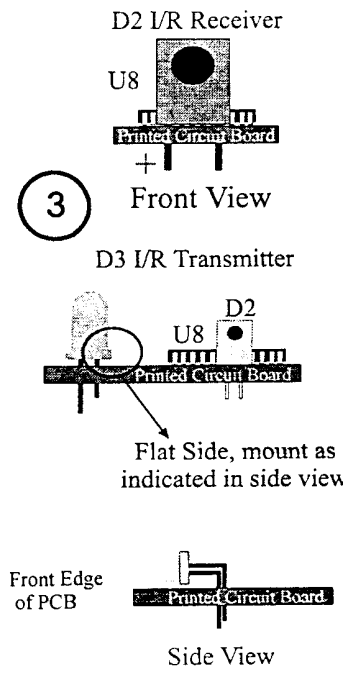
2

1

REV.	ZONE	PER ECN NO.	INITIAL & DATE		REV.	ZONE	PER ECN NO.	INITIAL & DATE	
			DRAWN	APPROVED				DRAWN	APPROVED
2	ALL	192-01	AC	6/96	AC			AC	1/97
2		192-03	A	5/97	A			A	5/97
2		192-07	A	12/97	A			A	12/97



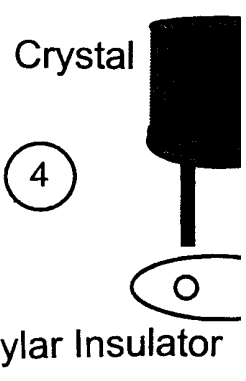
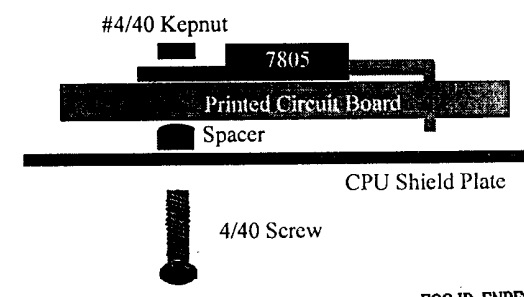
D2,D3- I/R LED Mounting



Notes

1 facing the front of
 2 e of part Silkscreen.
 3 4 and P2 mounting
 4 Option.

U24 Regulator Mounting Diagram



the Signal Strength option is requested:

FCC ID: ENPESTEEM192M

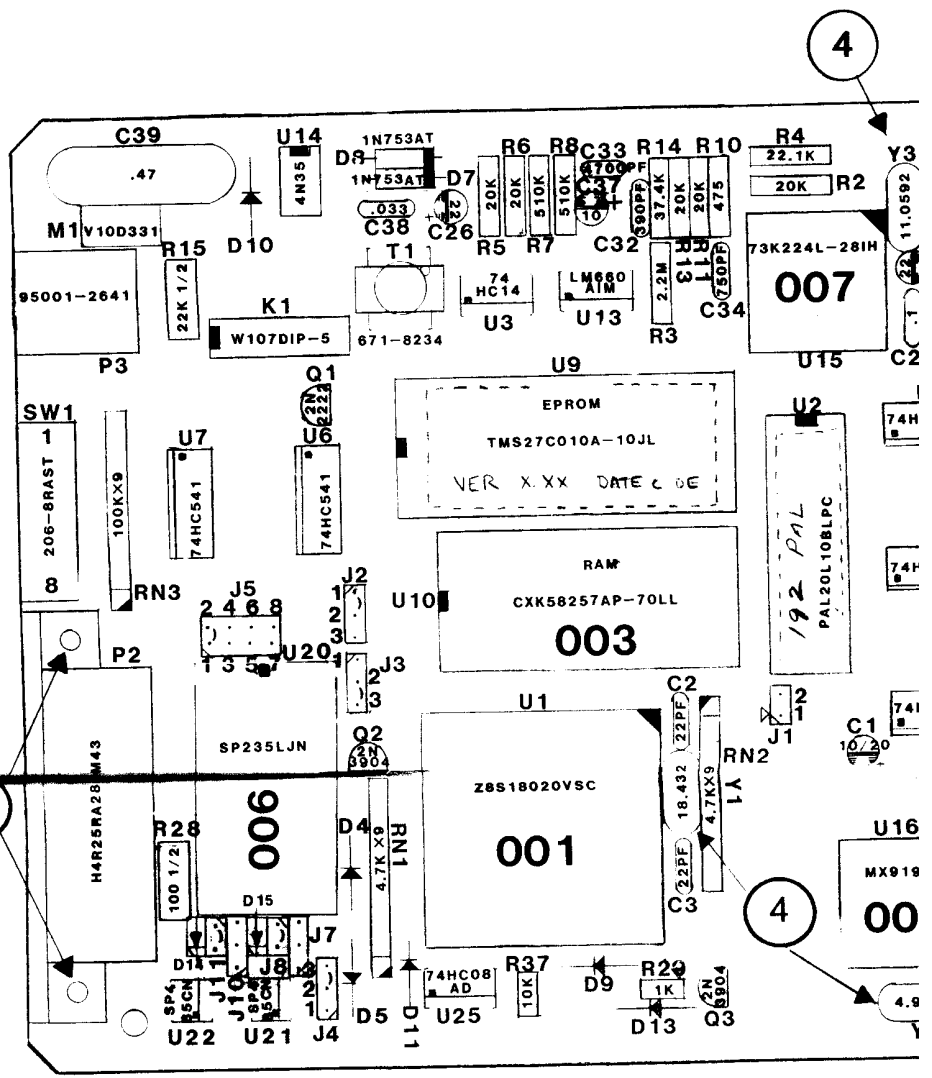
DESIGNED	PREL. 6/96		ELECTRONIC SYS TECHNOLOGY
DRAWN DM	PROTO.		
CHECKED AC 8/96	QUAL. 8/96		
	PROD. 8/96		
UNLESS OTHERWISE SPECIFIED	NEXT ASSY. 80A13/14	SYSTEM	192 ESTEEM
TOLERANCES	REFERENCE DWG. A00373	TITLE	192CPU ASSEMBLY COMP SIDE
DECIMAL XX ±0.10			
FRACTIONAL XXX ±0.005			
ANGULAR ±1/2			

D

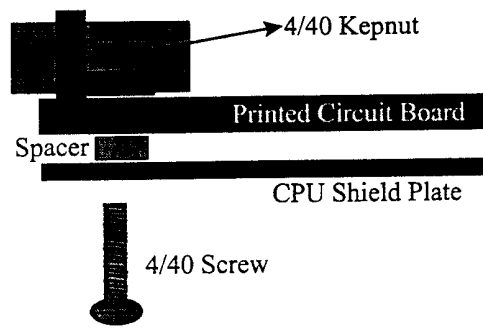
C

B

A



P2 Connector Mounting Diagram



ESTeem 192 CPU
Top

All resistors values are in ohms and are 1/8 watt unless noted o
 All capacitor values are in micro farads unless noted otherwise.
 Capacitor C7 is 33µF, 16Vdc.
 Capacitors C1 and C46 are 10µF, 20 Vdc.
 Capacitors C37, C41, and C42 are 10µF, 10 Vdc.

Capacitor C47 is 1µF, 35Vdc.
 Diodes D1, D4, D5, D11, and D13 are 1N4148 or accepted ec
 Diodes D9 and D12 are SD103 or accepted equivalent.
 Install Diode D3 with the flat side of IR LED facing U8. Insta
 the Printed Circuit Board. See D2 - D3 IR LED mounting d
 Apply Heatsink compound to the bottom of U24.
 Install mini-shunts at J1, J2, J3, J4, J5, J8, and J11
 P1 connector installed on solder side of PCB after wave solde
 Install #4 X 1/8" spacer between CPU PCB and CPU Shield
 diagrams.
 Install mylar insulator(s) under crystals Y1, Y2, Y3, and Y4

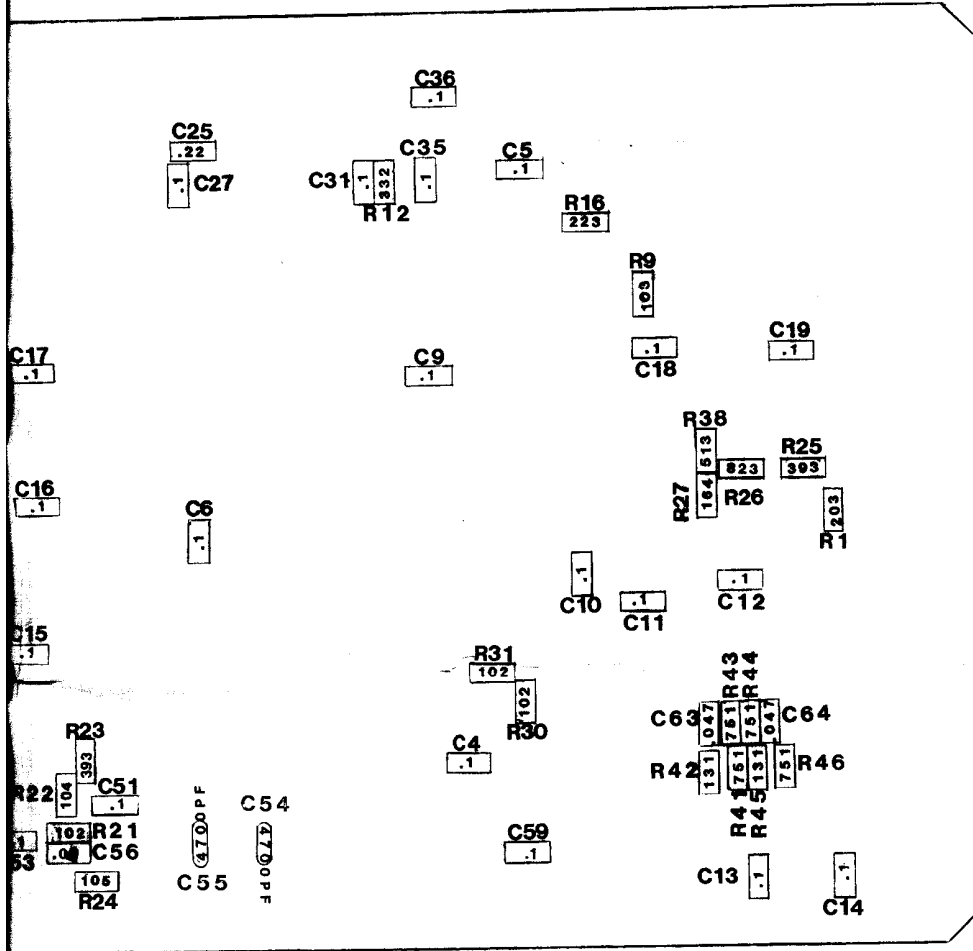
The following parts are not installed unless the Phone option is requested:

- Surge Suppressor M1
- Capacitors C22 thru C29, and C30 thru 39.
- Diodes D7, D8 and D10.
- Resistors R2 thru R16.
- Crystal Y3


The following parts are no

- ICs U17, U18, and U

REV.	ZONE	PER ECN NO	INITIAL & DATE		REV.	ZONE	PER ECN NO	INITIAL & DATE	
			DRAWN	APPROVED				DRAWN	APPROVED
2	ALL	192-01	AC	12/96					
2	ALL	192-03		8/97					5/97
2	ALL	192-07		12/97					12/97



FCC ID: ENPESTEEM192M

DESIGNED	PREL.		ELECTRONIC SYSTEMS TECHNOLOG
DRAWN	PROTO.		
CHECKED AC 8/96	QUAL. 8/96		
	PROD. 8/96		
UNLESS OTHERWISE SPECIFIED	NEXT ASSY. 80A13/14	SYSTEM 192 ESTEEM	
TOLERANCES	REFERENCE DWG. A00373	TITLE 192 CPU ASSEMBLY SOLDER SIDE	
DECIMAL XX ±0.10		SCALE	
XXX ±0.05		SIZE	
FRACTIONAL ±1/84		DWG & PART NO.	
ANGULAR ±1/2			
±0.004			

8

7

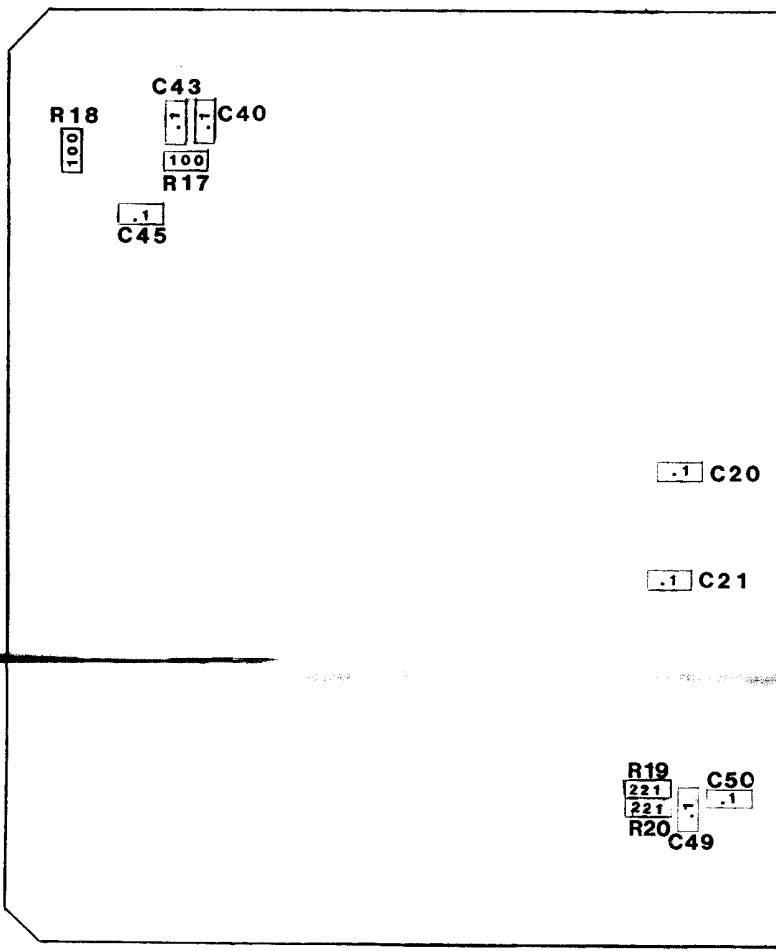
6

5

ESTeem 192 CPU Assembly Notes
Bottom Side

1. All resistors are 1/8 watt, 5% tolerance, chip size 1206 unless otherwise noted.
2. All resistor values are in ohms. Mount all chip resistors with the value markings visible.
3. All capacitors are chip size 1206.

D



C

B

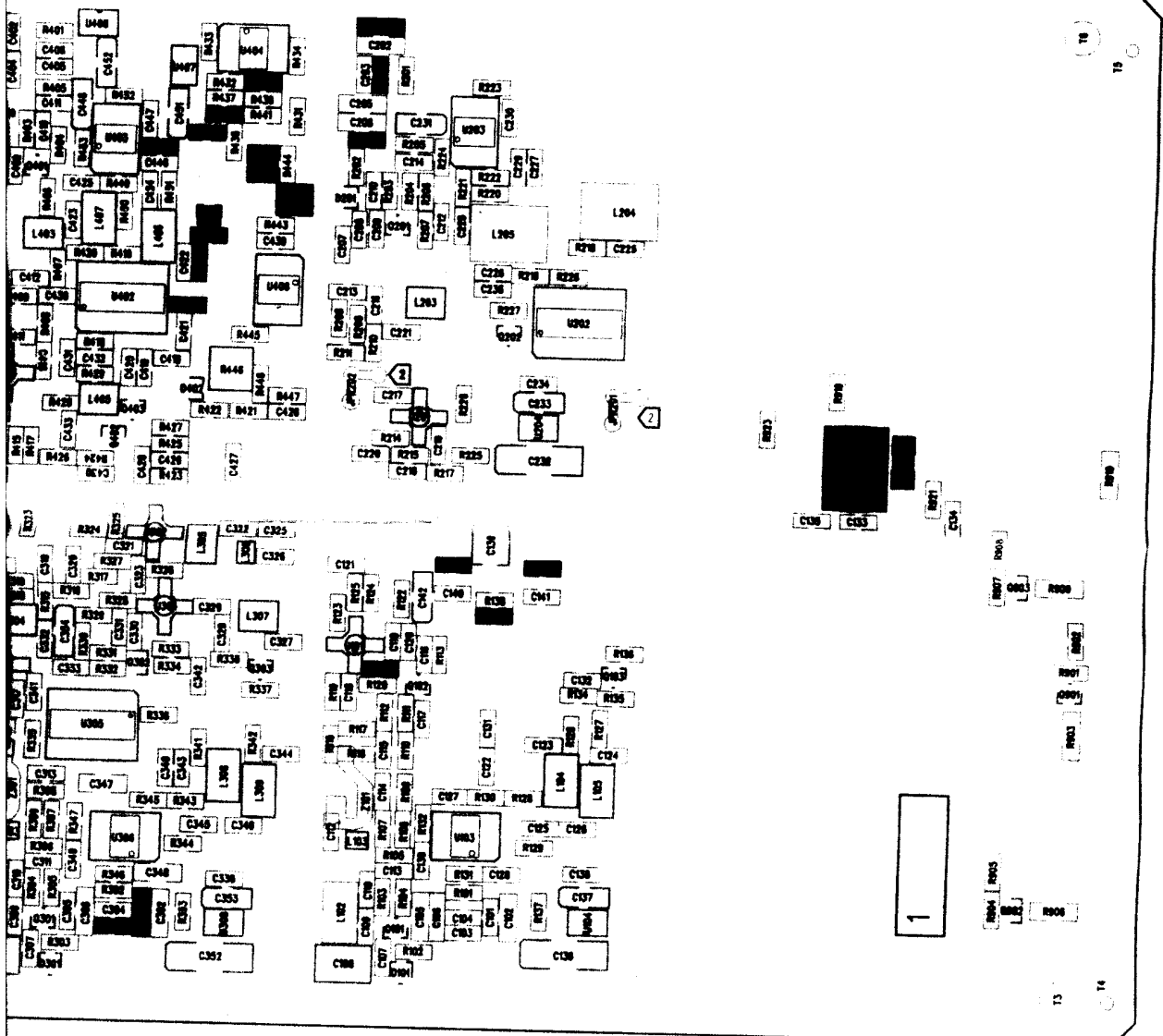
A

3

2

1

DRAWN	REV	ECO /	DESCRIPTION	CHECK	DATE



D

C

B

A

FCC ID: ENPESTEEM192M

TAIL "B"

DESIGNED 1	IDAFA B	PREL	EST	ELECTRONIC SYSTEMS TECHNOLOGY
DRAWN 7/81	C. CLARK	PROF'D		
CHECKED 1		QUAL		
UNLESS OTHERWISE SPECIFIED TOLERANCES: DECIMAL: .1, .15, .25 FRACTIONAL: 1/16, 1/8, 1/4 ANGULAR: ±.004 HOLE: ±.002		NEXT ASSY: 80A014	SYSTEM	192M WIRELESS MODEM
		QWC NUMBER	TITLE	TXRX ASSEMBLY DRAWING
		B00407		
ALL DIMENSIONS IN INCHES (DO NOT SCALE THIS DWG.)		SHEET 1 OF 2	SCALE: N/A	SIZE: C
		PART NO:	80B0160200A	REV: A

7

6

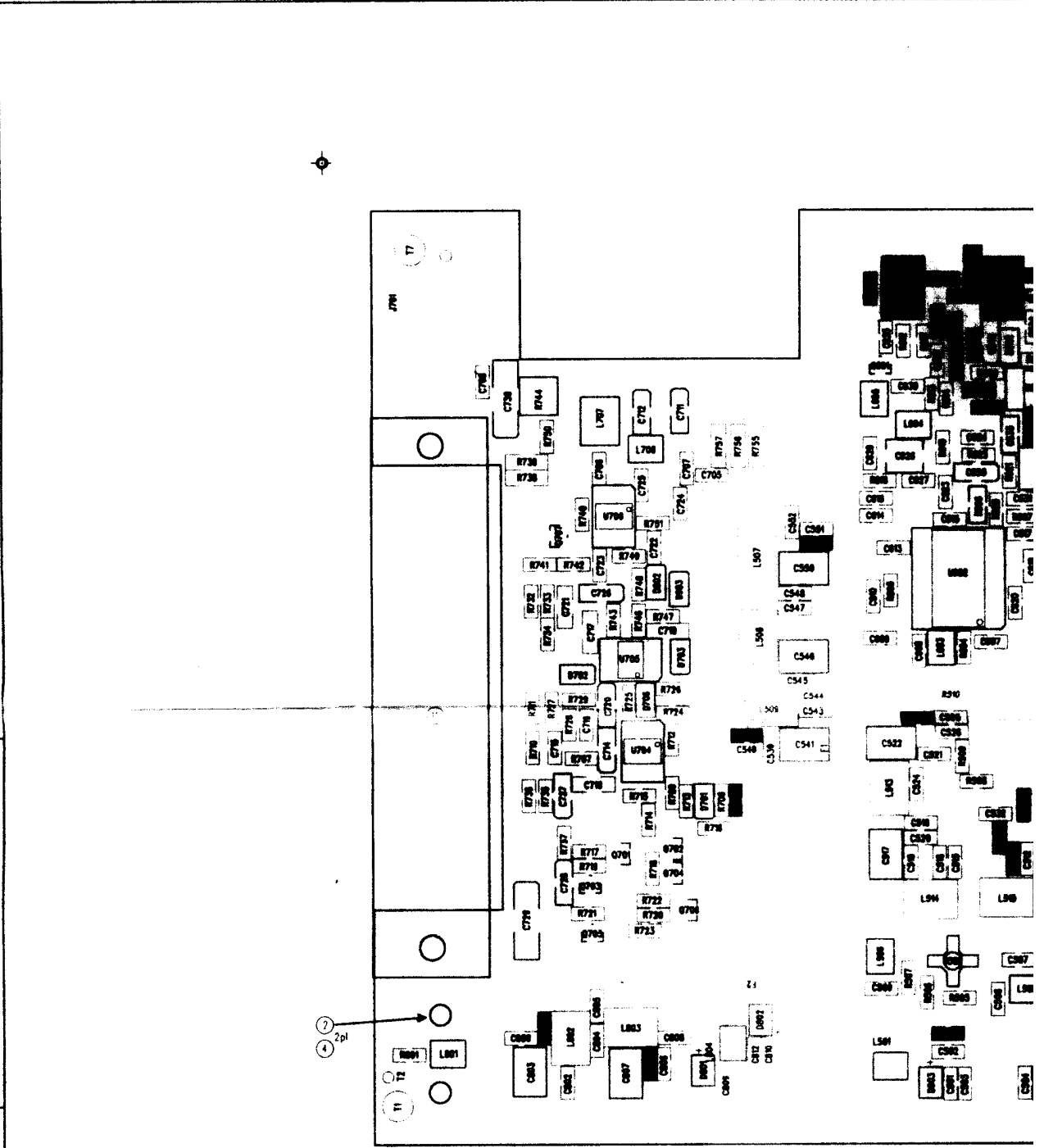
5

D

C

B

A



NOTES:

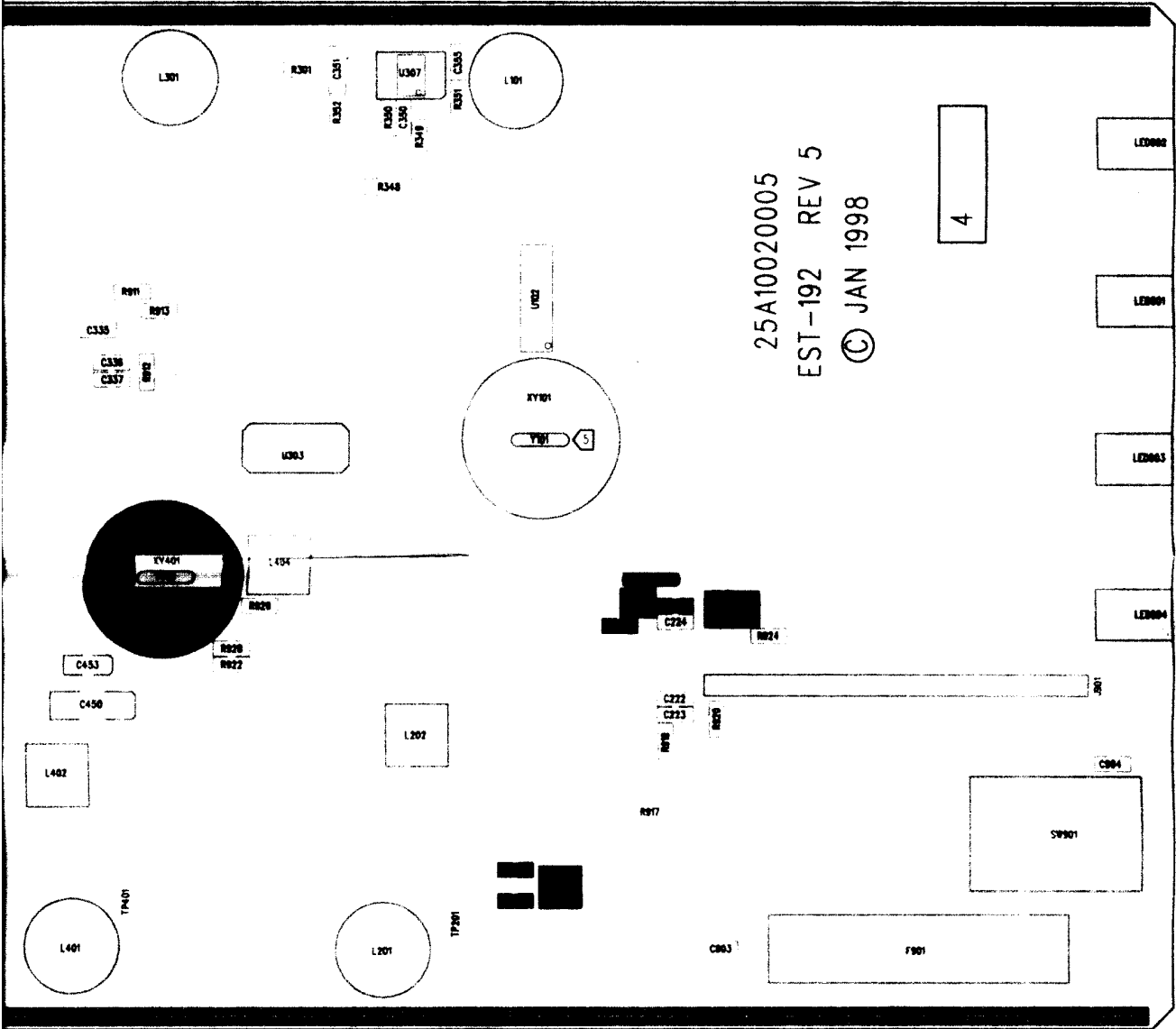
- ① NO STUFF
- ② CUT JUMPERS JPR201, JPR202, AND JPR501 BETWEEN 1
- ③ SOLDER COAX BRAID PARALLEL TO PAD
- ④ APPLY THIN LAYER OF HEATSINK COMPOUND
- ⑤ INSTALL FOAM AND MYLAR INSULATORS UNDER ALL 3 C
- ⑥ L106 & L310 WILL BE INSTALLED BY EST.

3

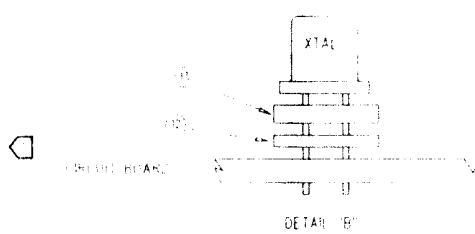
2

1

DRAWN	REV	ECO /	DESCRIPTION	CHECK	DATE



FCC ID: ENPESTEEM192M



IDAFAB		ELECTRONIC SYSTEMS TECHNOLOGY	
DESIGNER	C. CLARK	PROJECT	
DATE		QA	
REV		PRICE	
PART NUMBER		SYSTEM	
B00407		192M WIRELESS MODEM	
SHEET		TITLE	
2 of 2		TXRX ASSEMBLY DRAWING	
NA		C	
80B0160200A		A	

7

6

5

D

C

B

A

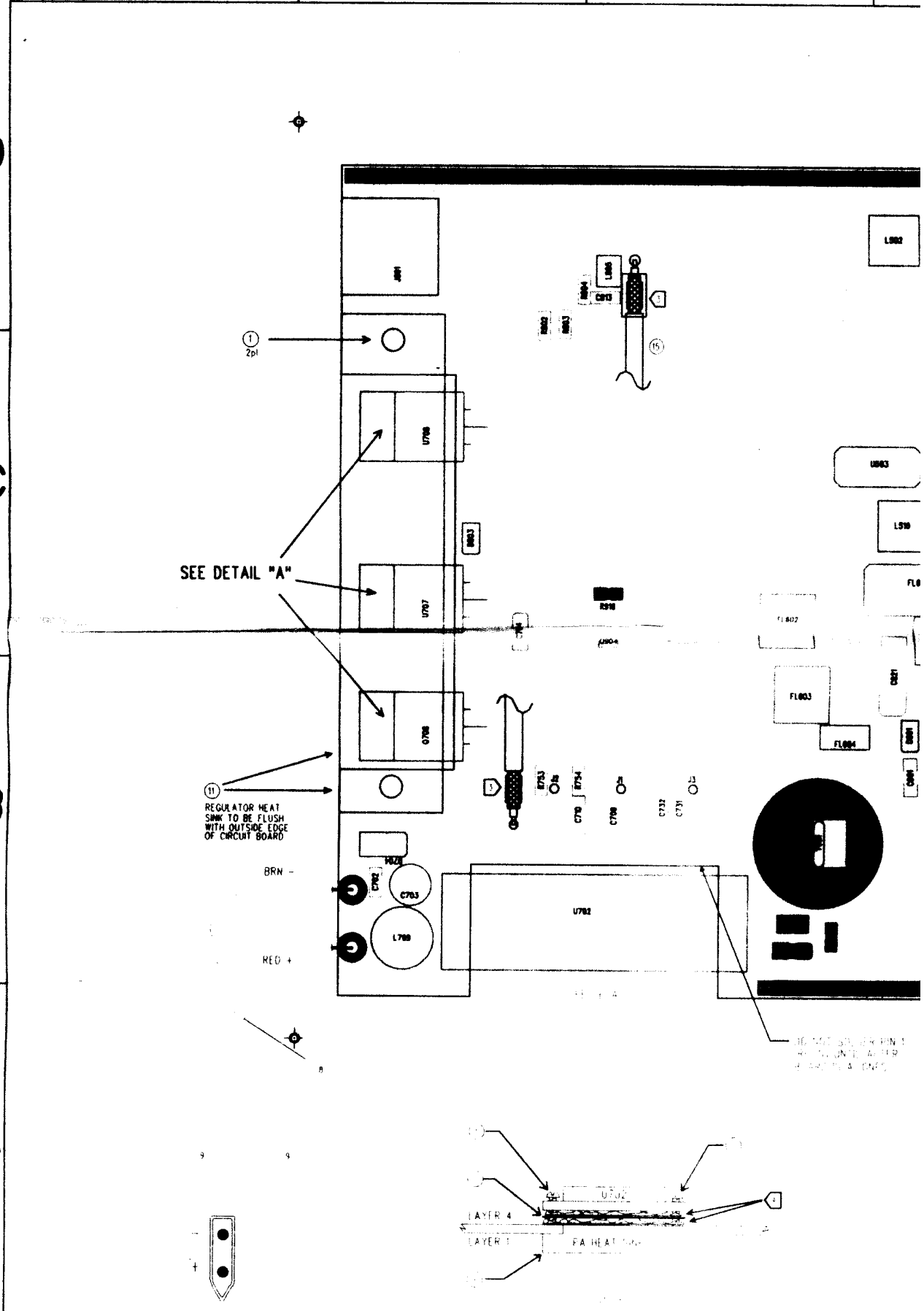
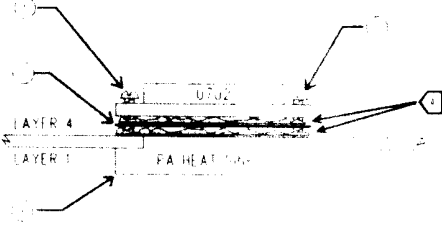
SEE DETAIL "A"

REGULATOR HEAT SHW TO BE FLUSH WITH OUTSIDE EDGE OF CIRCUIT BOARD

BRN -

RED +

HEAT SINK FOR PIN 1
REMOVABLE AFTER
WELDING IS DONE



3

2

1

DRAWN	REV	ECO #	DESCRIPTION	CHECK	DATE

D

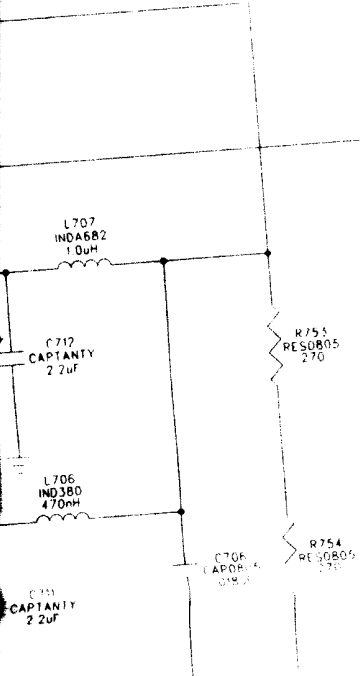
C

B

A

To T/R sw & output filter

RF_OUT



FCC ID: ENPESTEEM192M

DESIGNED BY IDAFAB ENG	PREP 6/97	EST	ELECTRONIC SYSTEMS TECHNOLOGY
DRAWN C. CLARK	RELEASE PROTO X		
CHECKED B. STRECKER	QUAL X INSD X		
UNLESS OTHERWISE SPECIFIED TOLERANCES	NET ASSY 80B016	SYSTEM 192M WIRELESS MODEM	
DECIMAL .XX +/- .010 RES +/- .005	DWG NUMBER C00406	TITLE 192M POWER AMP	
FRACTIONAL +/- 1/64	SCALE NA	SIZE C	PART NO 80B0160200A
ANGULAR +/- 1/2	SHEET 9	OF 11	REV A
HOLE +/- .004	ALL DIMENSIONS IN INCHES DO NOT SCALE THIS DWG		
WAVE +/- .002			

6

5

4

D

From PA Stage driver
(stages 1 & 2)

PA_STAGE_3

From up-converter
mixer sw / filter

TX_FIL_PA

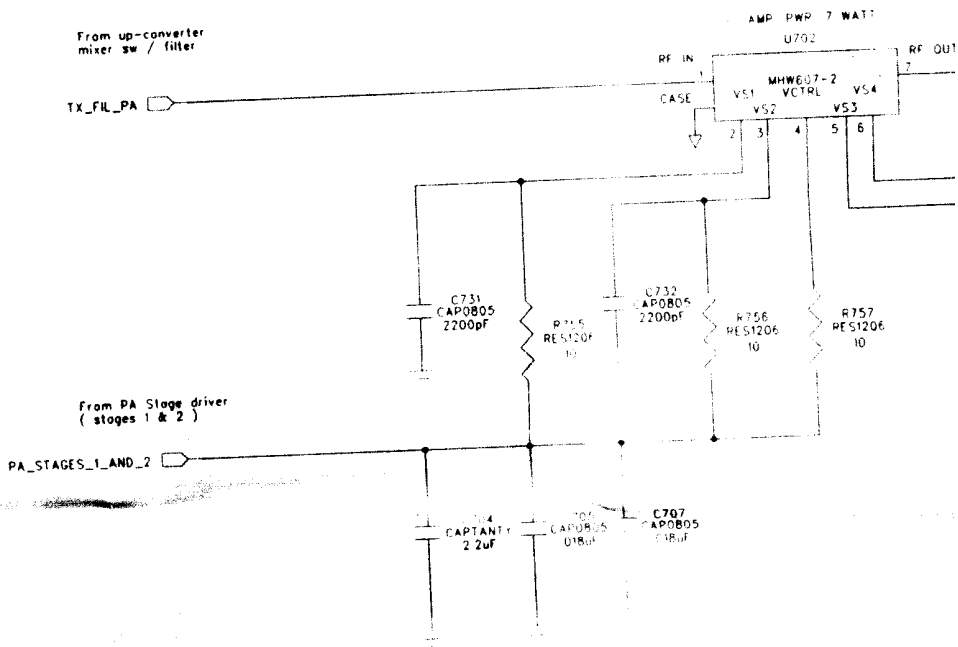
C

From PA Stage driver
(stages 1 & 2)

PA_STAGES_1_AND_2

B

A



3

2

1

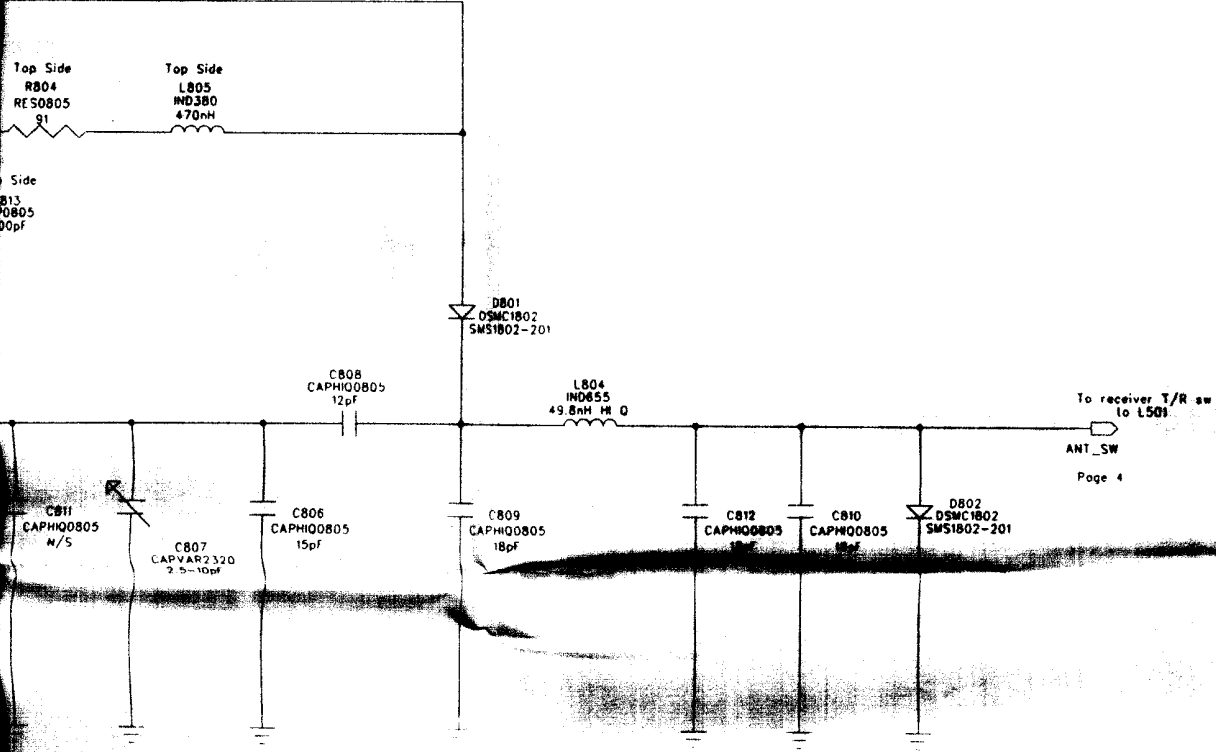
DRAWN	REV.	ECO #	DESCRIPTION	CHECK	DATE

D

C

B

A



Notes:

FCC ID: ENPESTEEM192M

DESIGNED 3/97 IDAFAB	PREL 4/97	EST	ELECTRONIC SYSTEMS TECHNOLOGY
DRAWN 4/97 C.Clark	RELEASE PROTD X QUAL X PROD X		
CHECKED 4/97 D.B.Strecker	NEXT ASSY		
UNLESS OTHERWISE SPECIFIED TOLERANCES	808016		
DECIMAL XX +/- .010 XXX +/- .005	FRAC NUMBER C00406	SYSTEM 192M WIRELESS MODEM	
FRACTIONAL +/- 1/64	TITLE ANT. FILTER & T/R SWITCH 800 SERIES		
ANGULAR +/- 1/2	SCALE NA C	PART NO 8080160200A	
HOLE +/- .002	SHEET 10 OF 11	REV A	
ALL DIMENSIONS IN INCHES			

6

5

4

D

Fm PA
PAGE 6
RF_OUT

PA_STAGES_1_AND_2

Top Side
D803
ZMM5228B
3.9V

T01
RES0
62

J801
TNC ANTENNA CONN

TNC
227838-1

C800
CAPHQ0805
12pF

L802
INDF55
33 10K HI 0

L801
IND380
470nH

C801
CAPHQ0805
N/S

C802
CAPHQ0805
15pF

C804
CAPHQ0805
100pF

R801
RES0805
1K

Part Des

R801 0.81

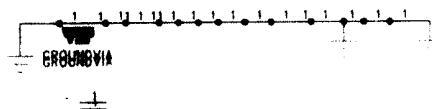
L804 0.802

C

B

A

V1 V2 V8A V8B V9 V10 V11 V12 V13 V14 V15 V16 V17 V18
GROUND VIA

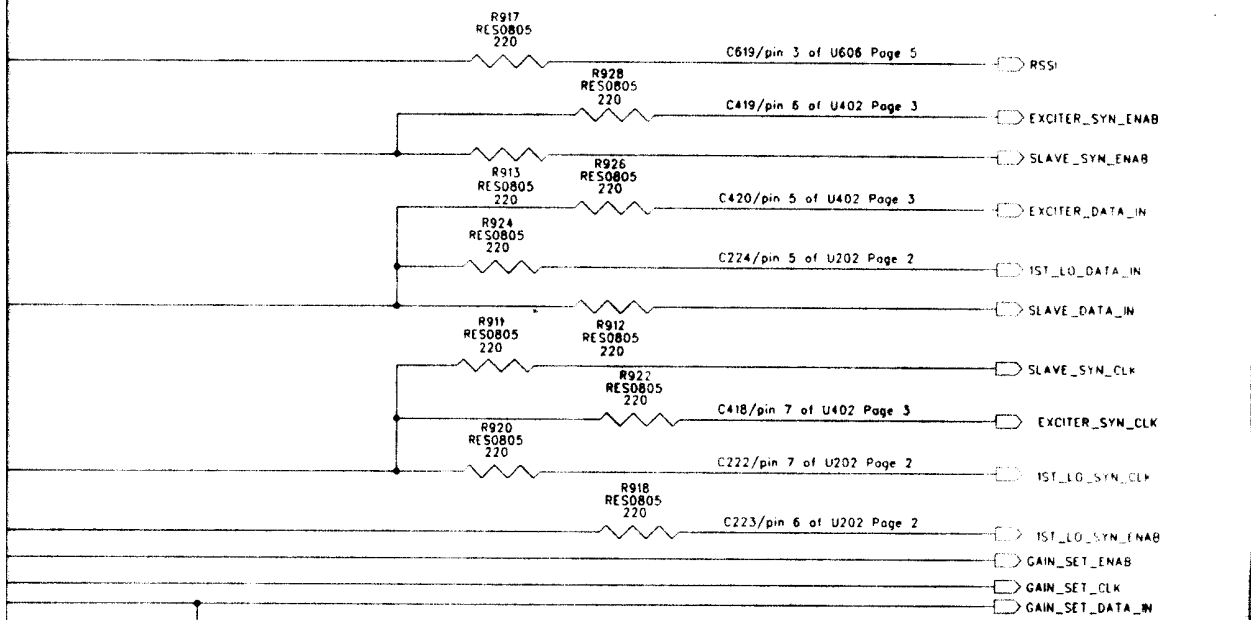


3

2

1

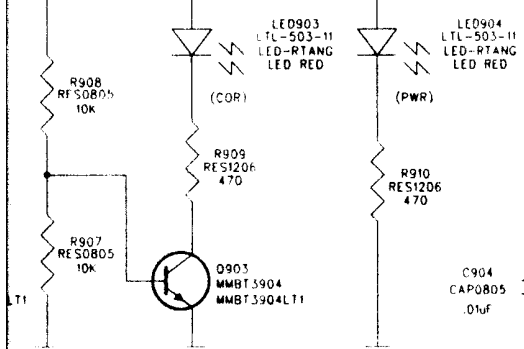
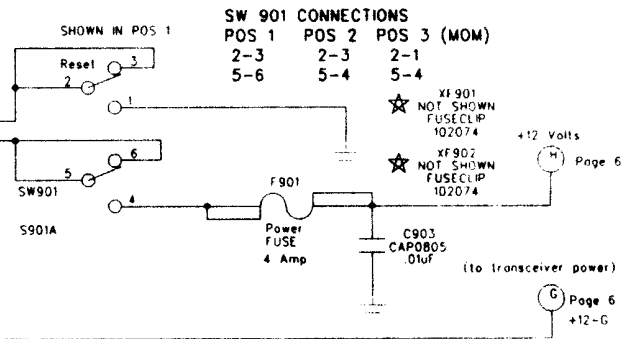
DRAWN	REV.	ECO #	DESCRIPTION	CHECK	DATE:



C905
CAP1206
N/S

N/S
74HC194D

- to R844 Page 5 → GAIN_SET_1
- to R846 Page 5 → GAIN_SET_2
- to R431 Page 3 → ANALOG_TX
- to R627 Page 5 → ANALOG_REC
- to R648 Page 5 → COR_DET
- to R708/R709 Page 6 → T/R_INP/IT
- to Q705/Q706 Page 6 → QV_TRAN_SW



Notes:
1 C902 not installed in current version

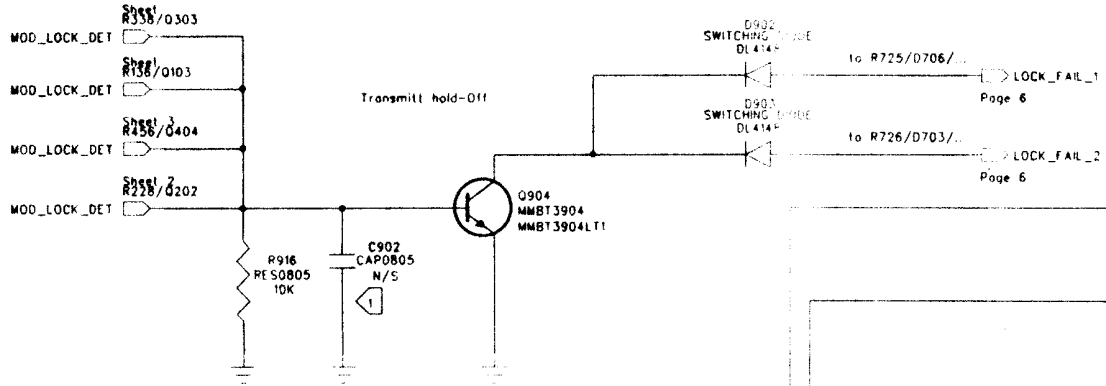
DESIGN ID 5/87	IDAF AB	PREL 4/97		ELECTRONIC SYSTEMS TECHNOLOGY
DRAWN 4/87	C.Clark	PROTO X		
CHECKED 4/87	D.B. Strecker	QUAL X		
		PROD X		
UNLESS OTHERWISE SPECIFIED TOLERANCES		NET ASST 80B016	SYSTEM 192M WIRELESS MODEM	
DECIMAL XXX +/- .010 XXX +/- .005		DWG NUMBER C00406	TITLE MISC/GLUE LOGIC 900 SERIES	
FRACTIONAL +/- 1/64				
ANGULAR +/- 1/2				
HOLE +/- .004 +/- .007				
		SCALE	SIZE	PART NO.
				REV

D

C

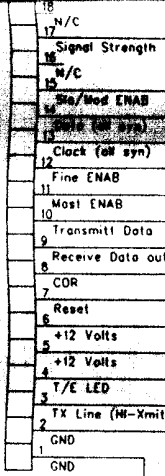
B

A



Missing Des.	Highest Des.
R927 R925	R928 D903
	Q905 C904
	J901 SW901
	LED904

J901 P901

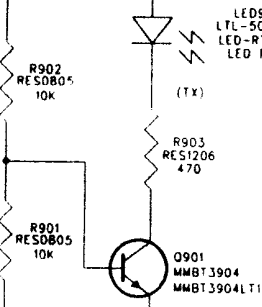


R923 RES0805 220

R919 RES0805 220

R921 RES0805 220

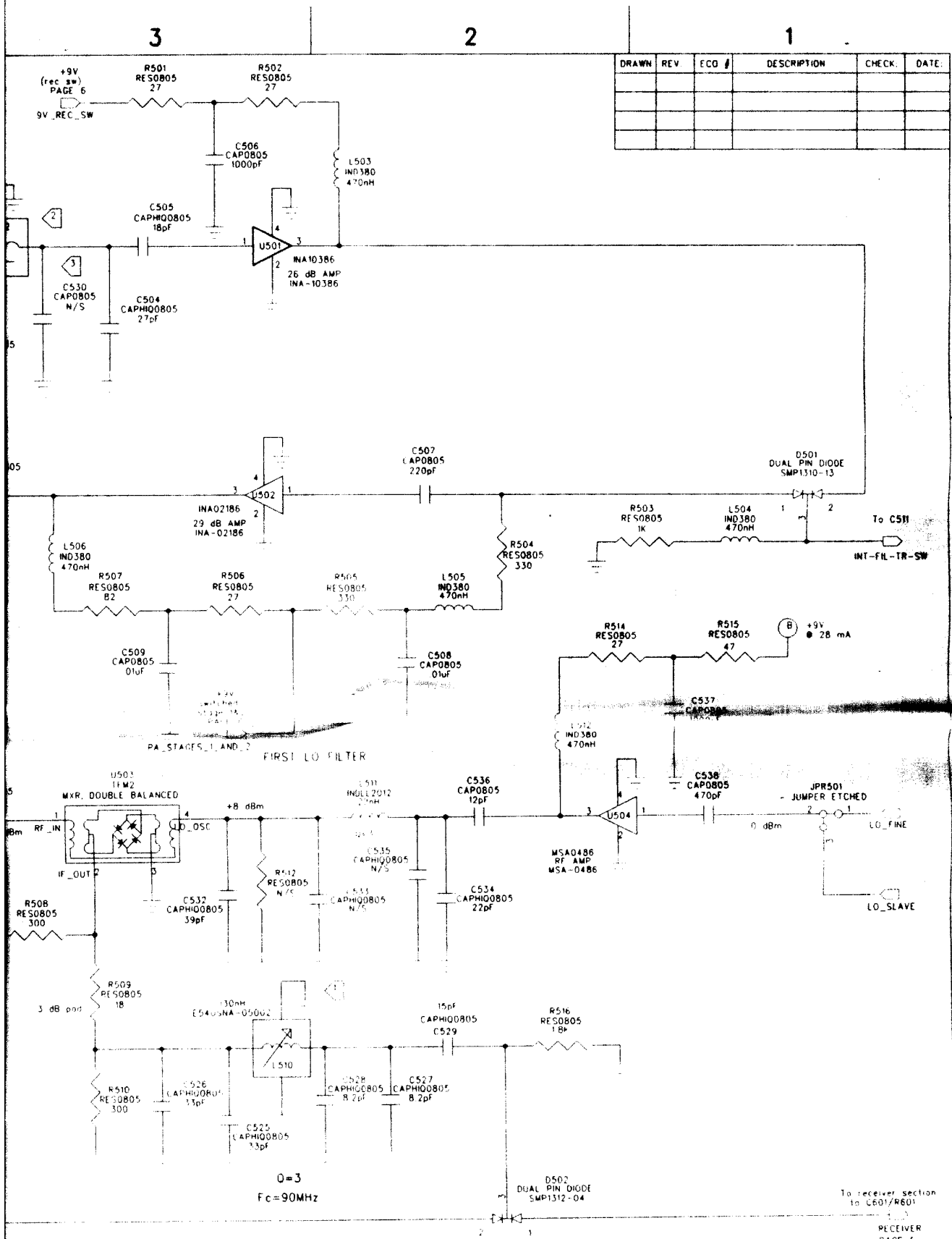
D901 SWITCHING DIODE DL4148



C901 CAP1206 1uF

R1 RES 1

R1 RES 4



DRAWN	REV	ECO #	DESCRIPTION	CHECK	DATE

FCC ID: ENPESTEEMI92M

DESIGNED 1/97	IDAFAB	REV	4/97		ELECTRONIC SYSTEMS TECHNOLOGY
DRAWN	C.Clark	PROT	X		
CHECKED	4-97 D.B Strecker	DUAL	X		
UNLESS OTHERWISE SPECIFIED TOLERANCES		PROT	X		
REF ASSEMBLY	808016	SYSTEM		192M WIRELESS MODEM	
TRC MA	XY	+/	010		MAIN TRANSCEIVER 500 SERIES
TRC MA	XY	+/	001		
TRC MA	XY	+/	1/84		
TRC MA	XY	+/	1/2		
TRC MA	XY	+/	004		
TRC MA	XY	+/	007		
SCALE	SIZE	PAPER	REV		

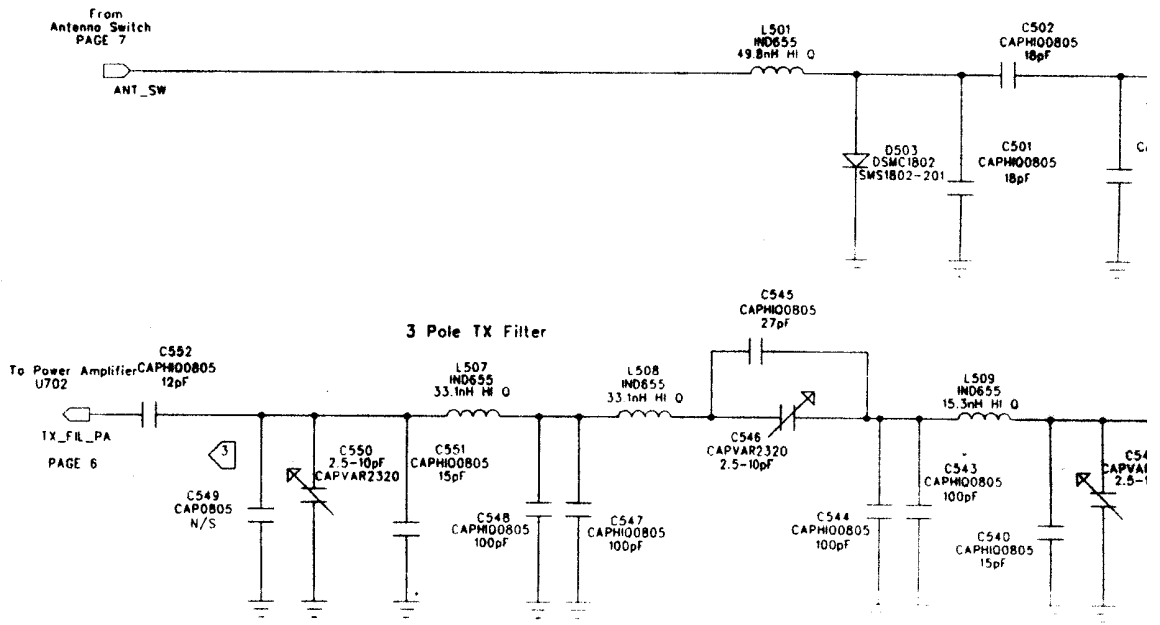
D

C

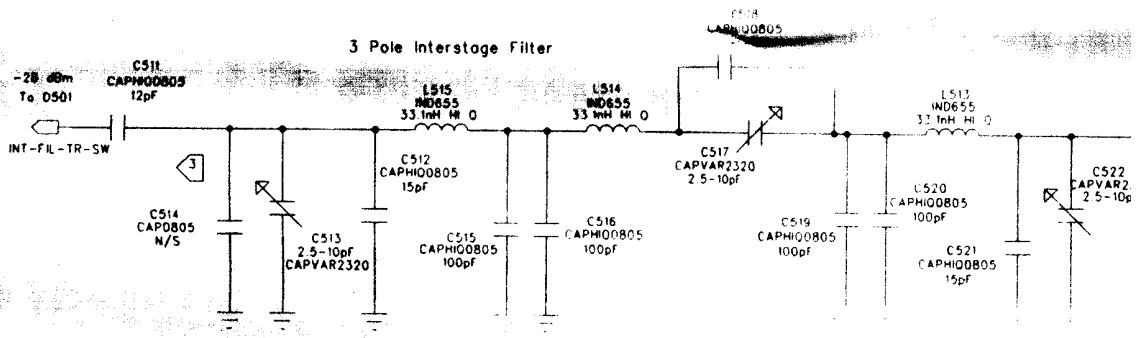
B

A

RECEIVER INPUT FILTER



3 Pole Interstage Filter



From exciter section from R416

EXCITER PAGE 3

Lost Des.	Missing Des.
R516 C552	C531
L515 D503	
U504	

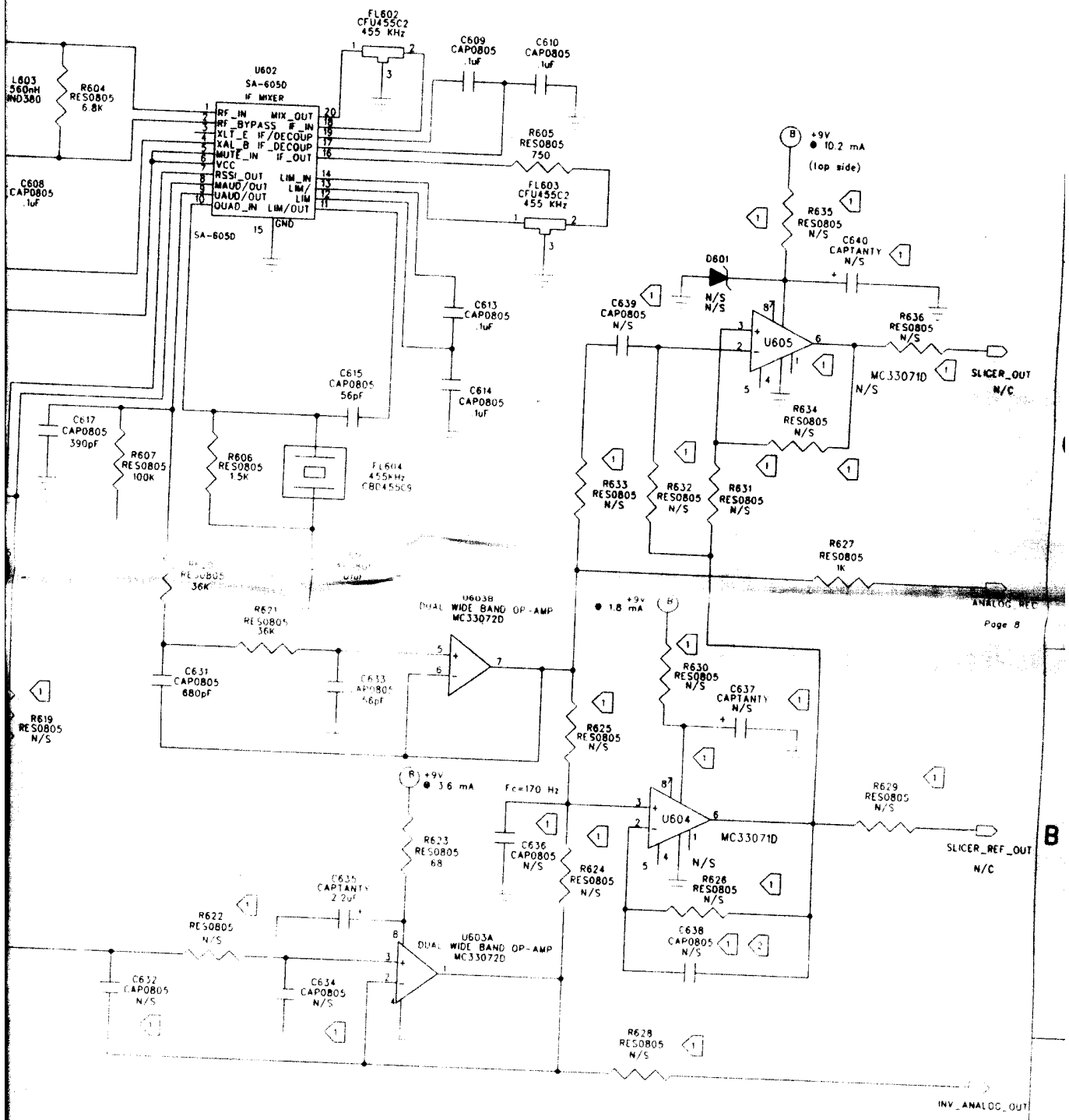
- Notes:
- ① 47 nH (72 MHz & 76 MHz)
 - ② 5.5 turns of # 24 wound on # 24 drill bit in small Toroid shield (76 MHz)
 - ③ Parts not installed C510, L514, C523, C530, C542 and C549

3

2

1

DRAWN	REV.	ECO #	DESCRIPTION	CHECK:	DATE



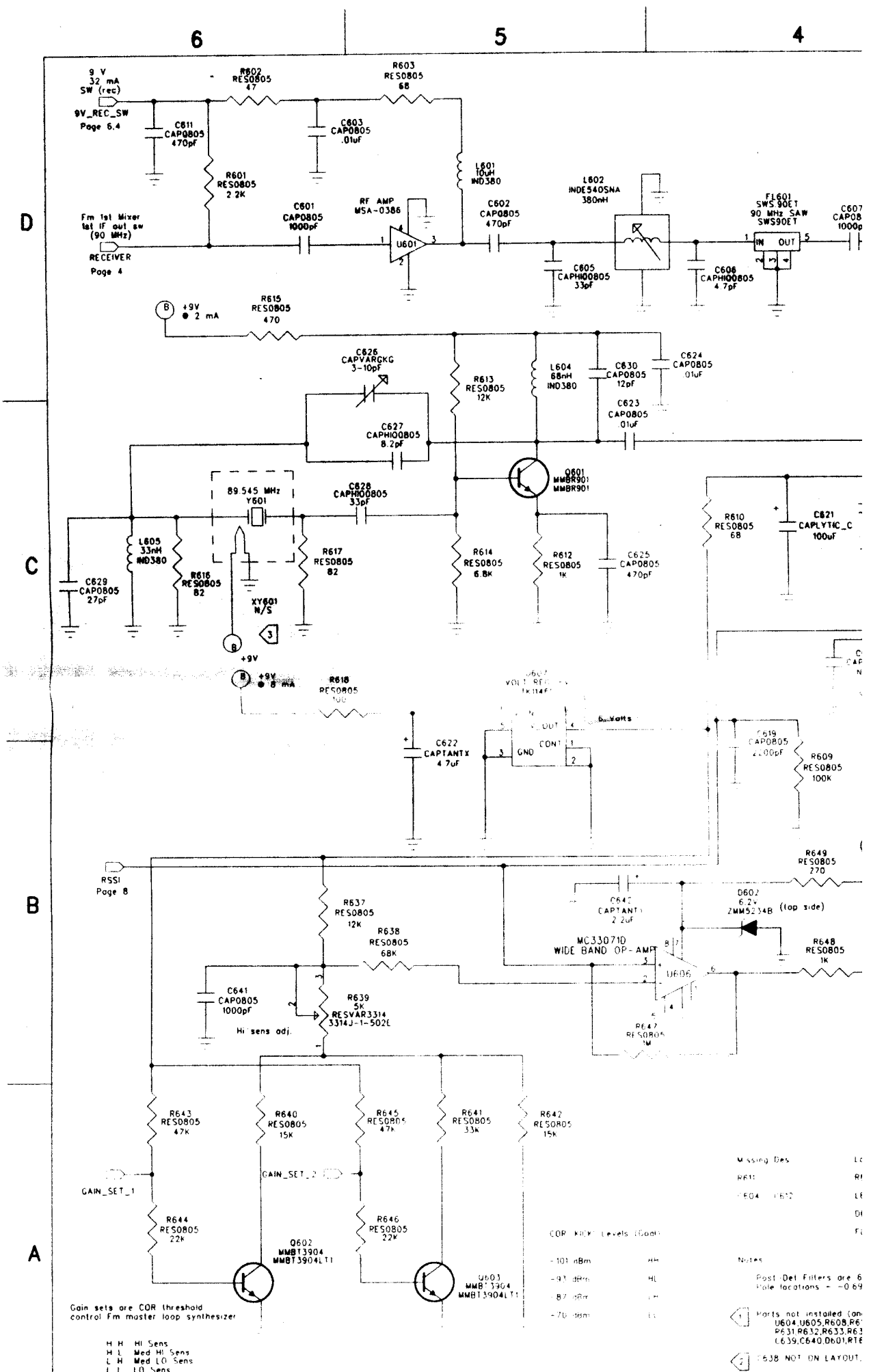
FCC ID: ENPESTEEM192M

deg.) L-P-Filters

R628, R629, R630, C636, C637, C638

DESIGNED 1/97	IDAF AB	DATE 4/97	PROJ y		ELECTRONIC SYSTEMS TECHNOLOGY
DRAWN 4/97	C.Clark	DUAL y	80B016		
CHECKED 4/97	D.B. Strecker	PROC y	192M WIRELESS MODEM		
UNLESS OTHERWISE SPECIFIED TOLERANCES:			RECEIVER 600 SERIES		
RESISTOR	XX	1%	Ω		
RESISTOR	XX	5%	Ω		
RESISTOR	XX	1/2%	Ω		
RESISTOR	XX	1/3%	Ω		
RESISTOR	XX	0.4%	Ω		
RESISTOR	XX	0.2%	Ω		

A



Gain sets are COR threshold control Fm master loop synthesizer

H H HI Sens
 H L Med HI Sens
 L H Med LO Sens
 L L LO Sens

COR Kick Levels (Lead)

-101 dBm	HH
-93 dBm	HL
87 dBm	LH
-70 dBm	LL

Missing Des: LC, RI, LE, DI, FI

Notes:
 Post-Det Filters are 6 Pole locations - -0.69

1 Parts not installed (an- U604, U605, R608, R61, R631, R632, R633, R63, C639, C640, D601, RTE)

2 R638 NOT IN LAYOUT.

3

2

1

DRAWN	REV.	ECO #	DESCRIPTION	CHECK	DATE

To Switched Transmitter Stages

9V_TRAN_SW

To Switched Receiver Stages

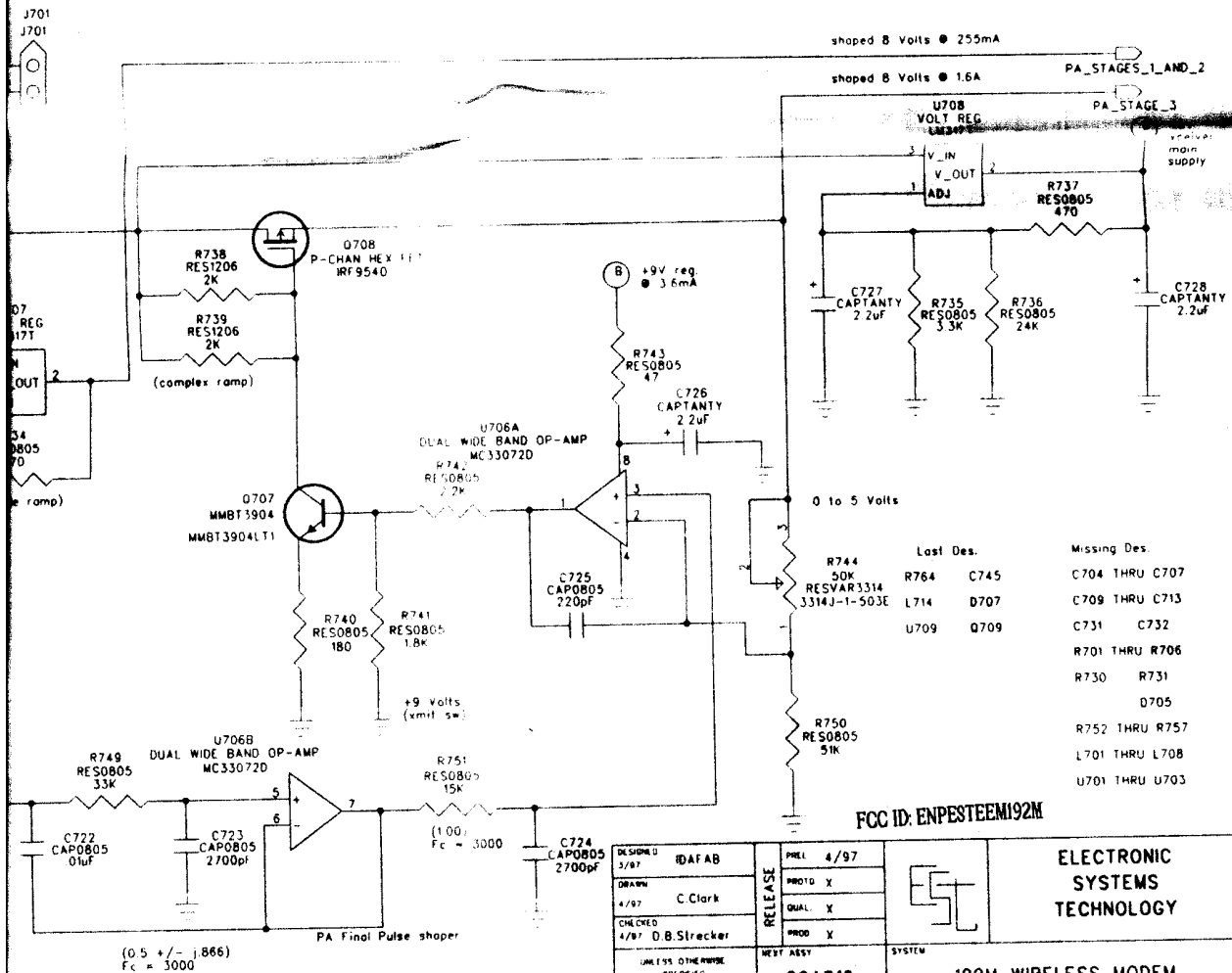
9V_REC_SW

D

C

B

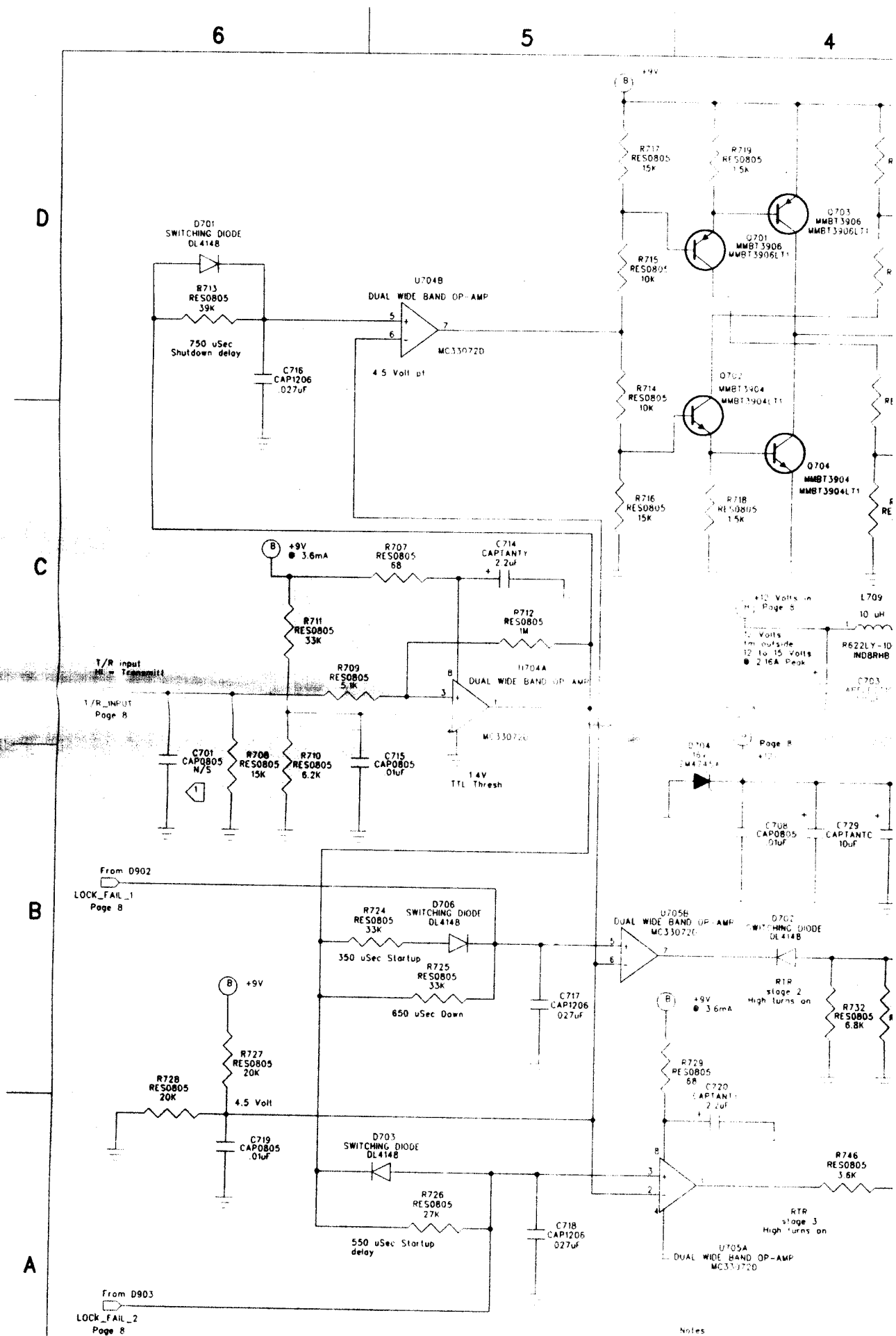
A



FCC ID: ENPESTEEMI92M

DESIGNED 3/87 IDAFAB	PREL 4/97		ELECTRONIC SYSTEMS TECHNOLOGY
DRAWN 4/97 C.Clark	PROTO X		
CHECKED 4/97 D.B.Strecker	QUAL X		
	PROD X		
UNLESS OTHERWISE SPECIFIED TOLERANCES	TEST ASSY 80A016	SYSTEM 192M WIRELESS MODEM	
DECIMAL XX +/- .010 XXX +/- .005	DWG NUMBER C00406	TITLE T/R SEQUENCER, PWR AMP 700 SERIES	
FRACTIONAL ANGULAR			

PA Stage Power Switch



D

C

B

A

6

5

4

T/R input
 HI = Transmitt

T/R_INPUT
 Page 8

From D902
 LOCK_FAIL_1
 Page 8

From D903
 LOCK_FAIL_2
 Page 8

+10 Volts in
 Page 5
 10 uH
 P622LY-10
 INDBRH
 C703
 APPL 100

D704
 10V
 M4741A
 Page 8
 +10V
 C708
 CAP0805
 01uF
 C729
 CAPTANTC
 10uF

+9V
 3.6mA
 R728
 RES0805
 68
 C720
 CAPTANTC
 2.2uF

RTR
 stage 2
 High turns on
 R733
 RES0805
 6.8K

RTR
 stage 3
 High turns on
 U705A
 DUAL WIDE BAND OP-AMP
 MC330720

Notes
 1 factory select as required

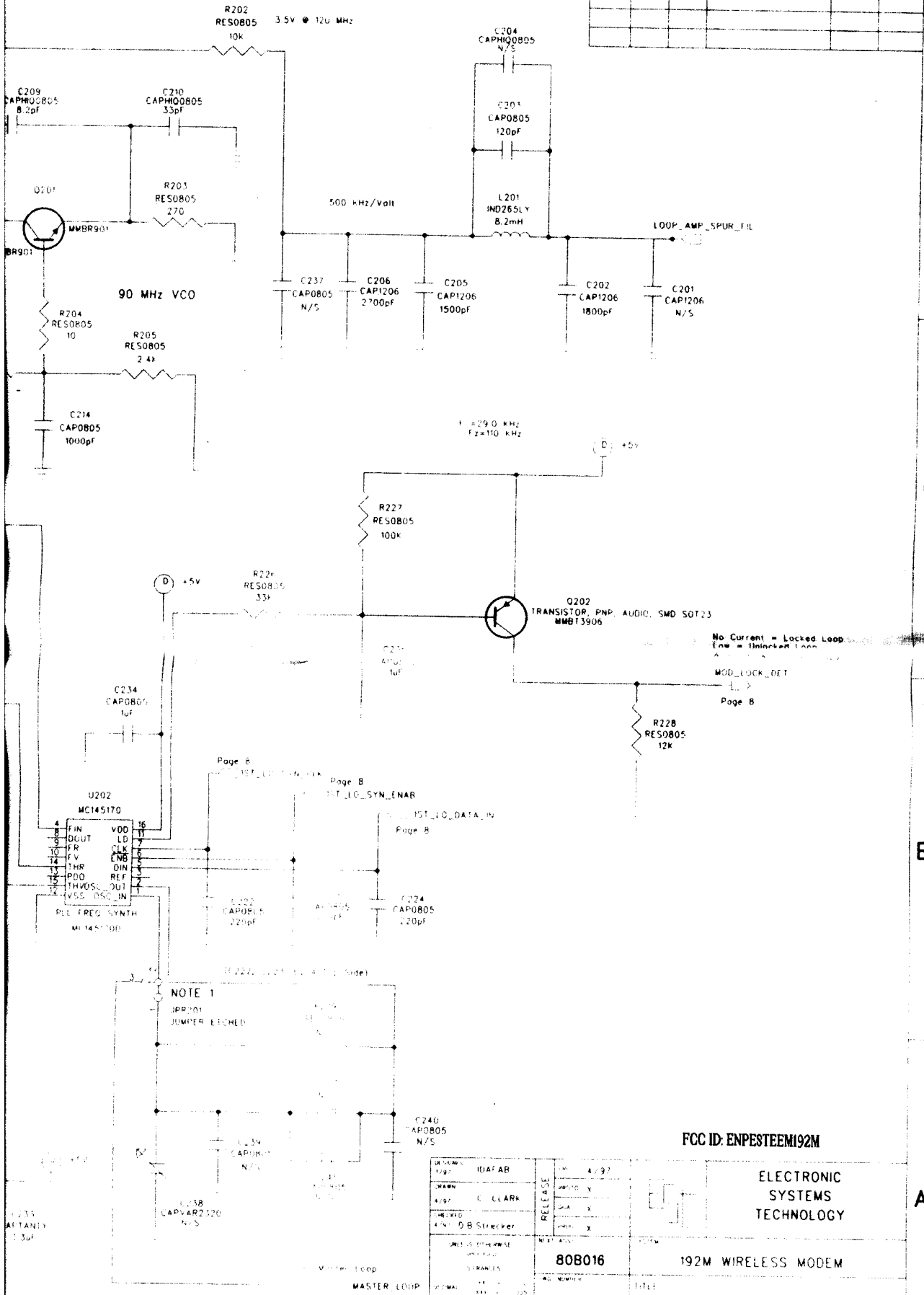
Transmit Power Sequencer

3

2

1

DRAWN	REV	ECO	DESCRIPTION	CHECK	DATE



D

C

B

A

No Current = Locked Loop
Low = Injected Loop
MOD_LOCK_DET
Page 8

NOTE 1
JPR201
JUMPER ETCHED

FCC ID: ENPESTEEM192M

DESIGNED BY: IDAFAR	DATE: 4/97	ELECTRONIC SYSTEMS TECHNOLOGY
DRAWN: C. CLARK	RELEASED: X	
CHECKED: 474 DB Strecker	APPROVED: X	80B016
QUANTITY: 1000	REV: 1	192M WIRELESS MODEM
DATE: 11/11/97	REV: 1	C00406
DATE: 11/11/97	REV: 1	TRANSCEIVER FINE LOOP 200 SERIES
DATE: 11/11/97	REV: 1	80B0160200A

6

5

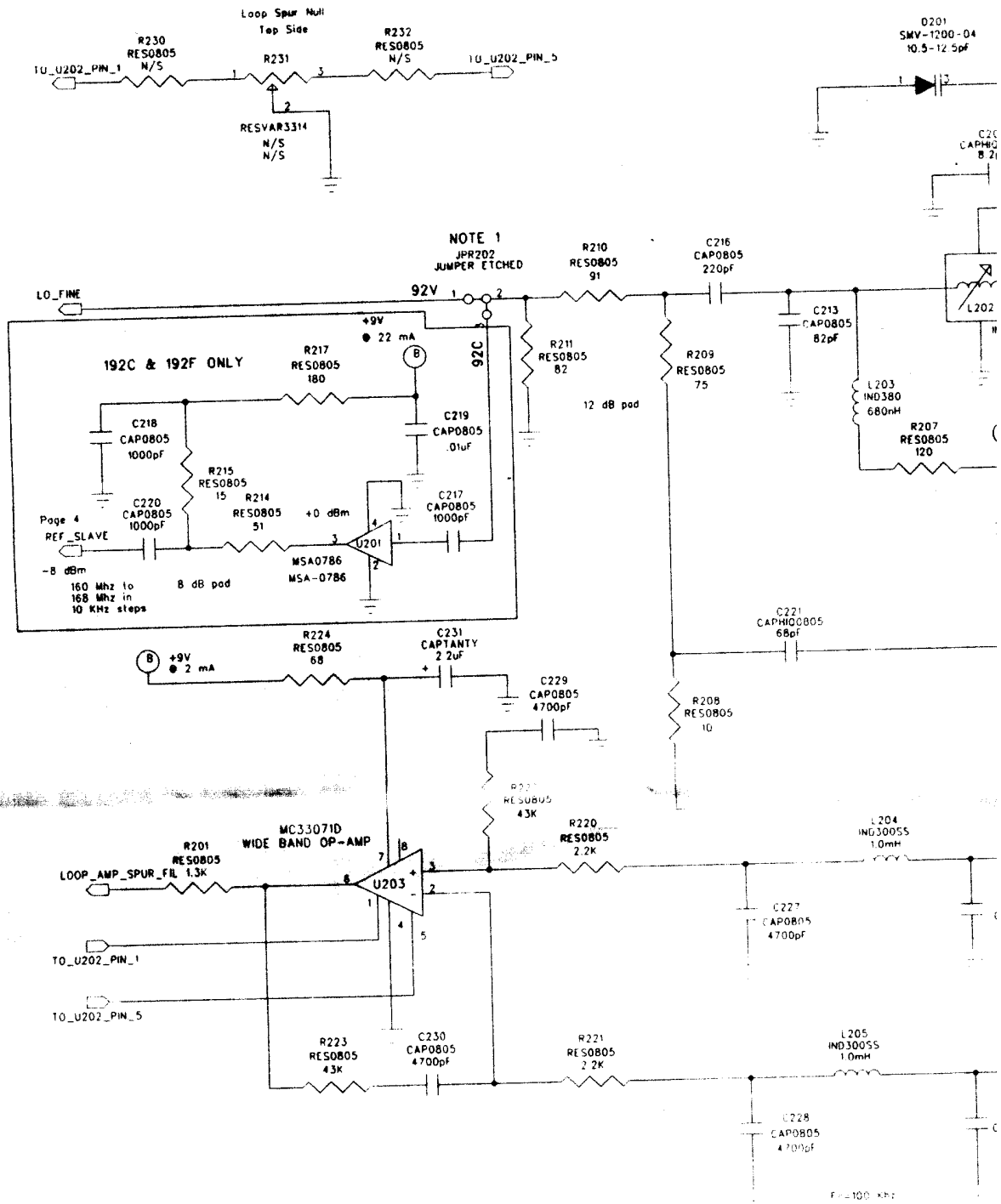
4

D

C

B

A



Missing Des		Highest Des	
R212	R213	R232	C240
R216		L205	U204
C211	C235	O201	Q202
		Y201	JPR2

Note:
 1. CUT JPR201-2&3, JPR202-2&3 FOR 192V
 CUT JPR201-1&2, JPR202-1&2 FOR 192C

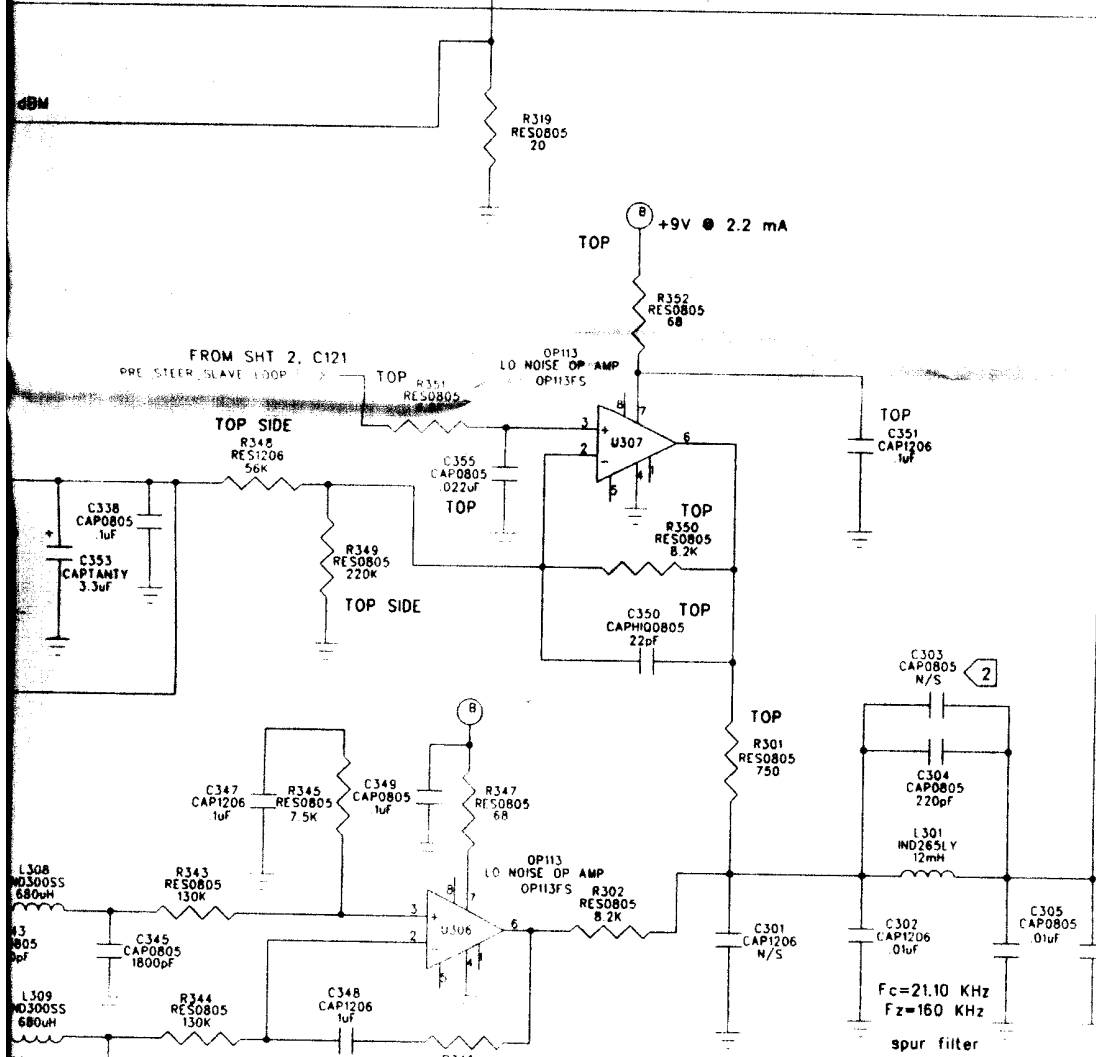
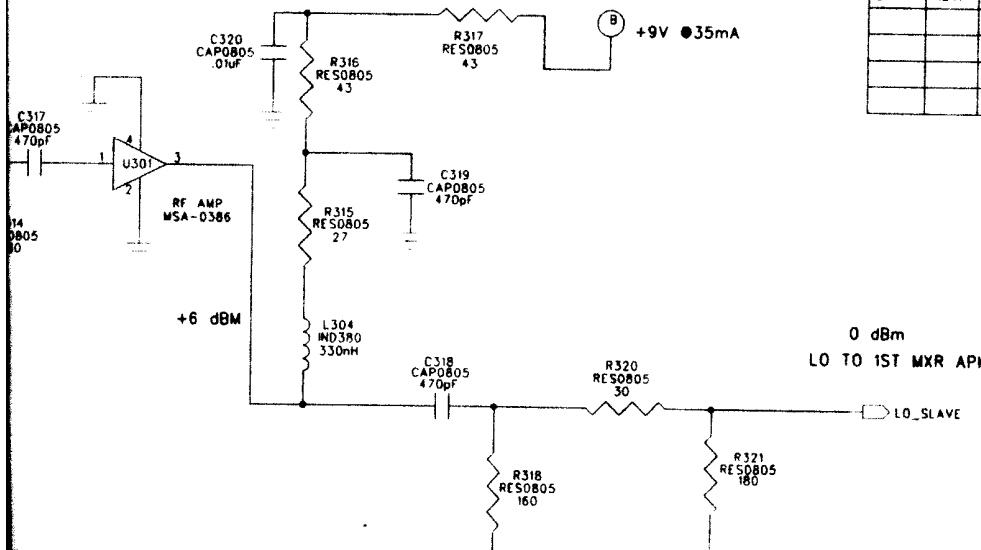


3

2

1

DRAWN	REV.	ECO #	DESCRIPTION	CHECK	DATE



200 KHz approx. 18 KHz

VCO sens. = 9.6 MHz/Volt
Loop sens. = 738 KHz/Volt

FCC ID: ENPESTEEM192M

DESIGNED BY DIAFB ENG	DATE 8-11-97	PHOTO X
DRAWN BY D. CROSS	CHECKED BY B. STRECKER	DUAL X
CHECKED BY B. STRECKER	PHOTO X	PHOTO X
UNLESS OTHERWISE SPECIFIED TOLERANCES:	DECIMAL .XX +/- .010	PERCENT +/- .005
FRACTIONAL +/- 1/64	ANGULAR +/- 1/2	ANGLE +/- .004
HOLE +/- .002		
NET ASSEMBLY NUMBER 80B016	SYSTEM 192M WIRELESS MODEM	
DATE NUMBER C00406	TITLE SLAVE SYNTHESIZER	
	300 SERIES	

ELECTRONIC SYSTEMS TECHNOLOGY

192M WIRELESS MODEM
162 MHz

SLAVE SYNTHESIZER
300 SERIES

D

C

B

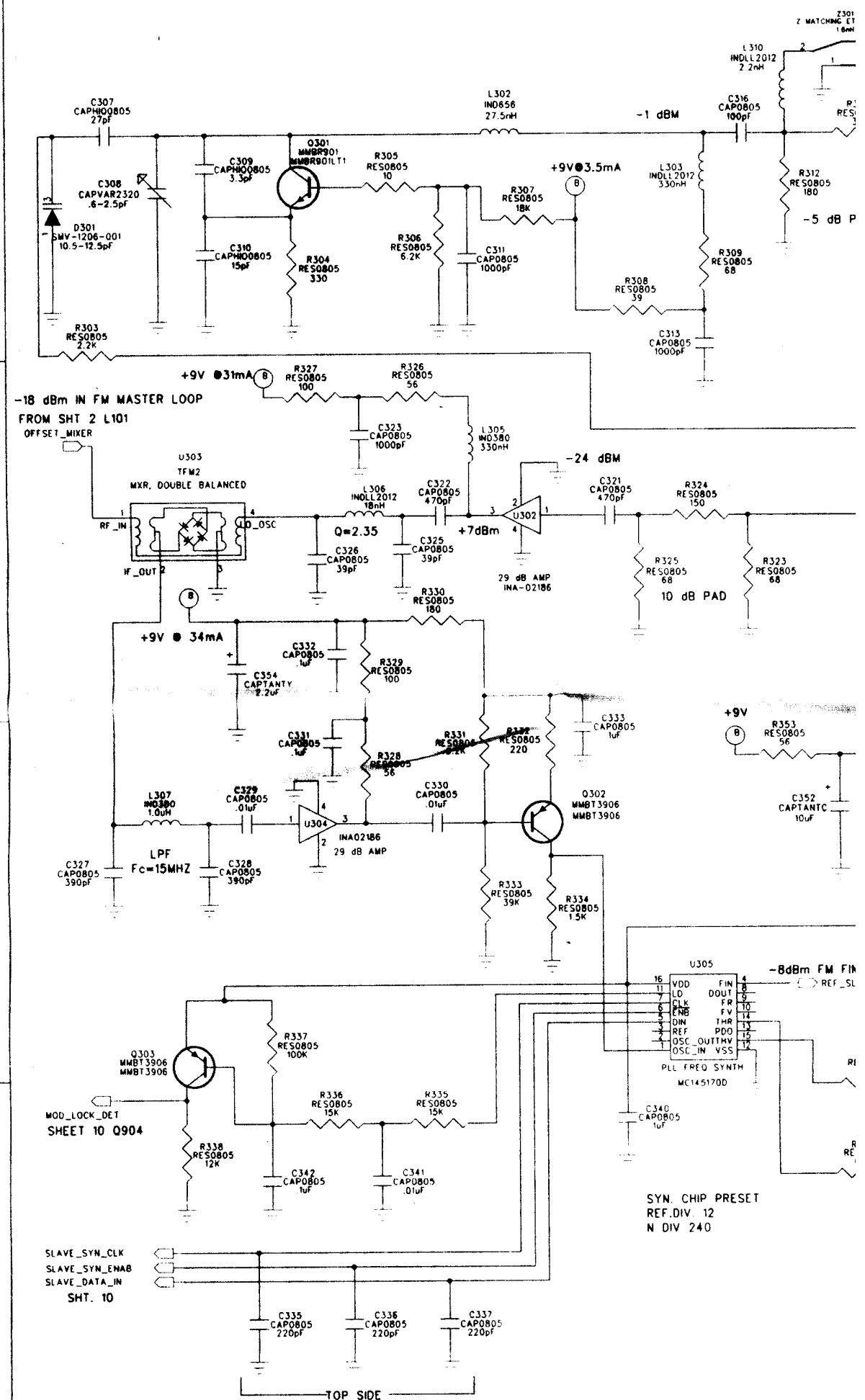
A

D

C

B

A

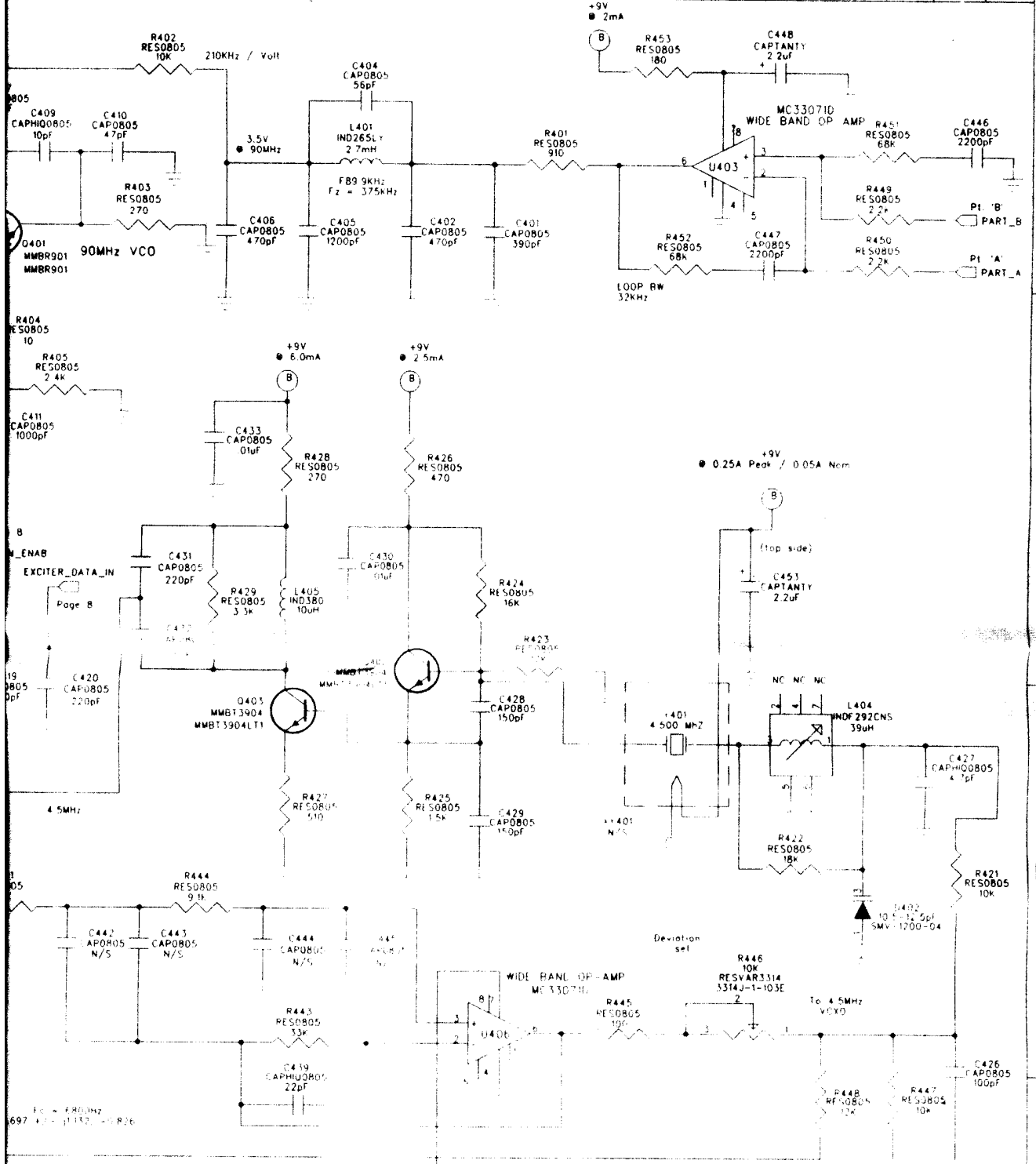


3

2

1

DRAWN	REV	ECO #	DESCRIPTION	CHECK	DATE



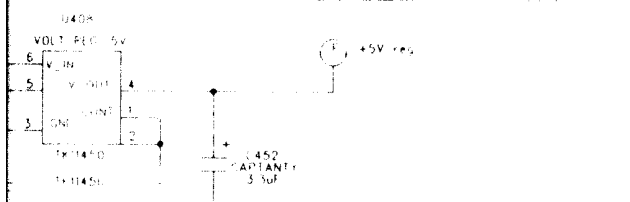
D

C

B

A

FCC ID: ENPESTEEMI92M



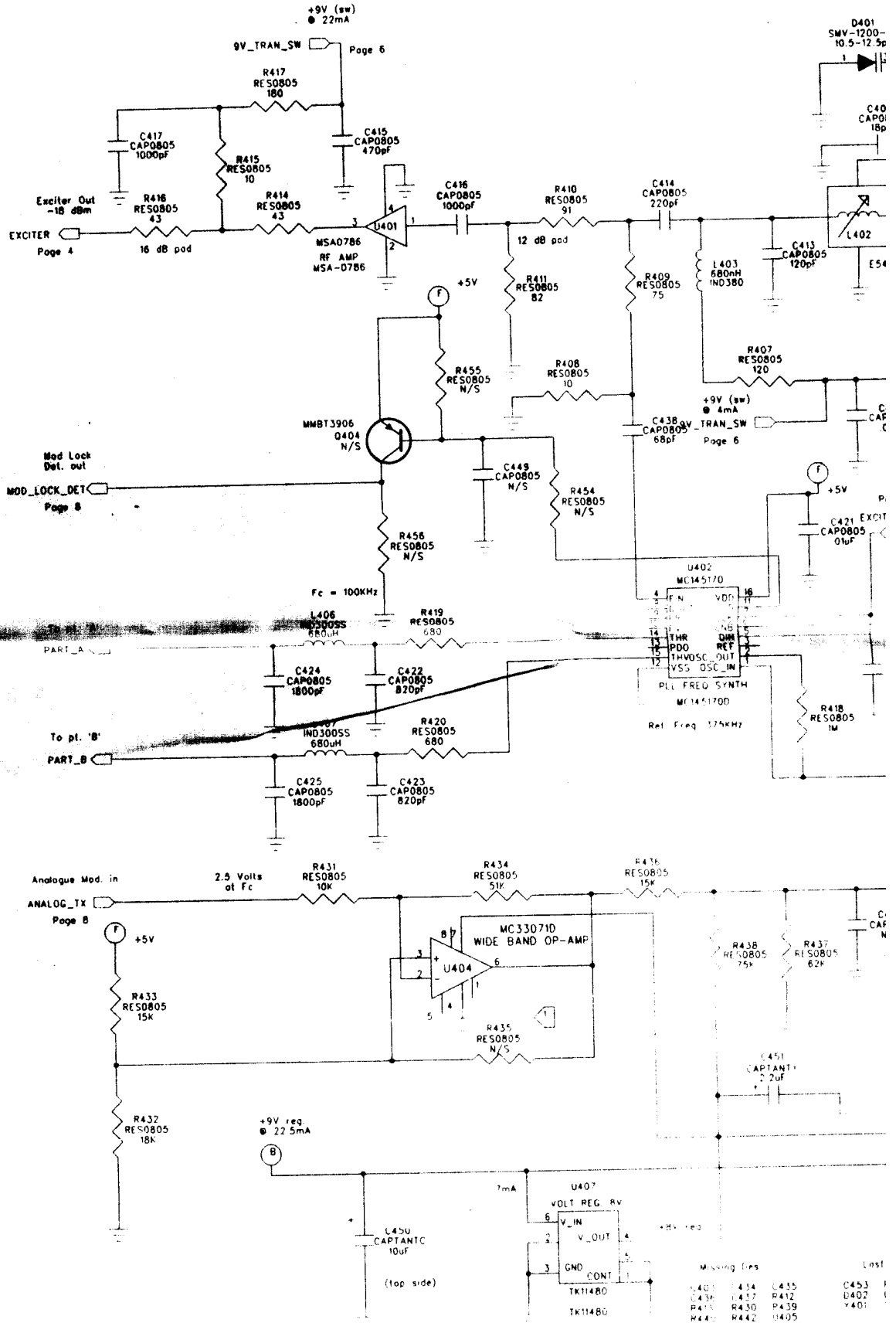
DESIGN	DATE	REV	4/97		ELECTRONIC SYSTEMS TECHNOLOGY
DRAWN	DATE	REV			
CHECKED	DATE	REV			
APPROVED	DATE	REV			
80B016				192M WIRELESS MODEM	
C00406				90 MHz EXCITER 400 SERIES	

D

C

B

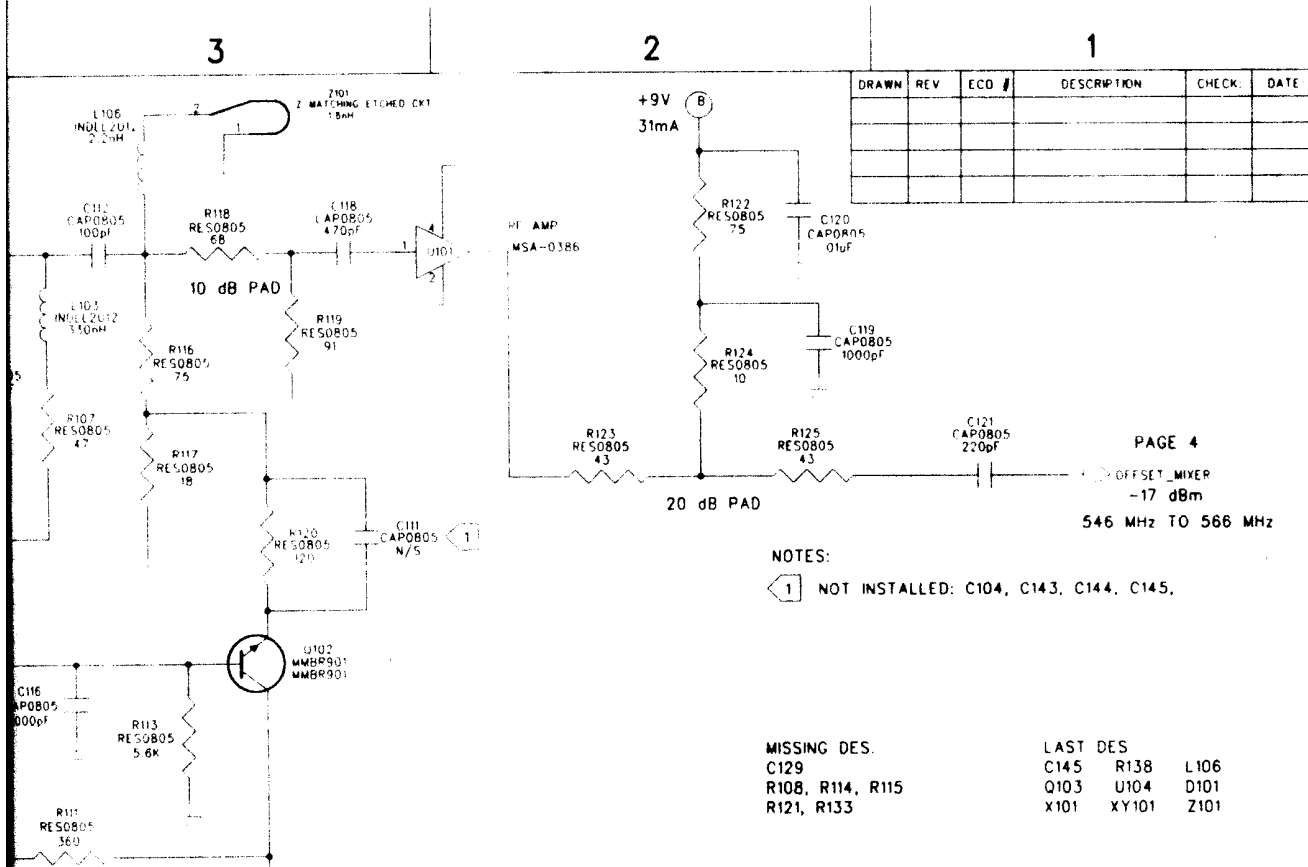
A



Missing Part	Part	Value	Part	Value
U401	454	C415	C453	F
R418	R432	R412	U402	F
R419	R430	R439	U403	F
R420	R442	U405		

Analogue Configuration

Factory select as required

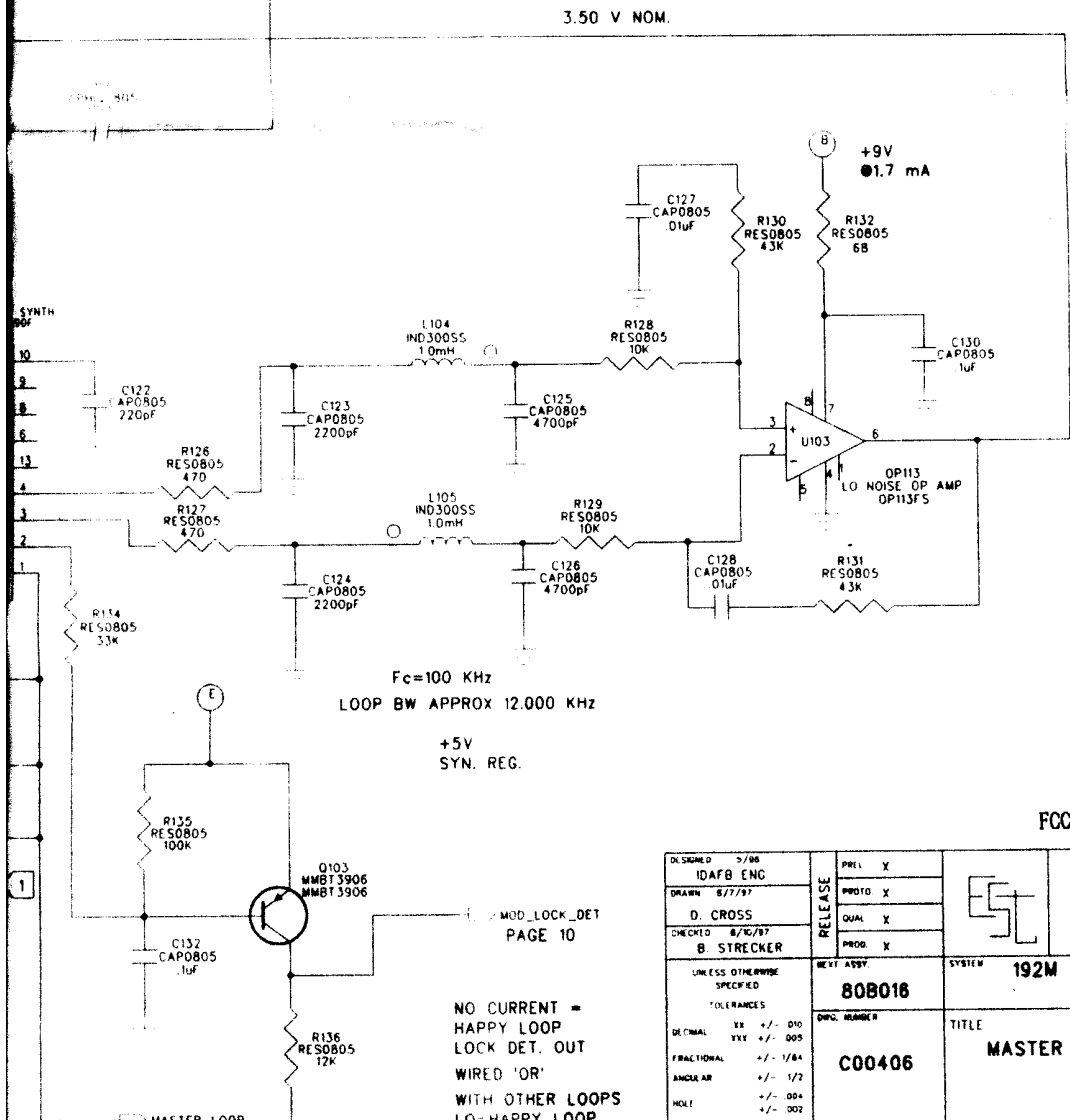


DRAWN	REV	ECO #	DESCRIPTION	CHECK	DATE

PAGE 4
 OFFSET MIXER
 -17 dBm
 546 MHz TO 566 MHz

NOTES:
 1 NOT INSTALLED: C104, C143, C144, C145.

MISSING DES. LAST DES
 C129 C145 R138 L106
 R108, R114, R115 Q103 U104 D101
 R121, R133 X101 XY101 Z101



Fc=100 KHz
 LOOP BW APPROX 12,000 KHz
 +5V SYN. REG.

NO CURRENT =
 HAPPY LOOP
 LOCK DET. OUT
 WIRED 'OR'
 WITH OTHER LOOPS
 LO=HAPPY LOOP

FCC ID: ENPESTEEM192M

DESIGN D 2/88 IDAFB ENG	PREL X		ELECTRONIC SYSTEMS TECHNOLOGY
DRAWN 8/77/97	PROTD X		
D. CROSS	QUAL X		
CHECKED 8/16/97 B. STRECKER	PROD. X		
UNLESS OTHERWISE SPECIFIED	TEST ASSY	SYSTEM 192M WIRELESS MODEM 162 MHz	
TOLERANCES	80B016	DWG. NUMBER	
DECIMAL XX +/- 010	C00406	TITLE	
FRACTIONAL XYE +/- 005		MASTER SYNTHESIZER LOOP	
ANGULAR +/- 1/64		100 SERIES	
HOLE +/- 004			

6

5

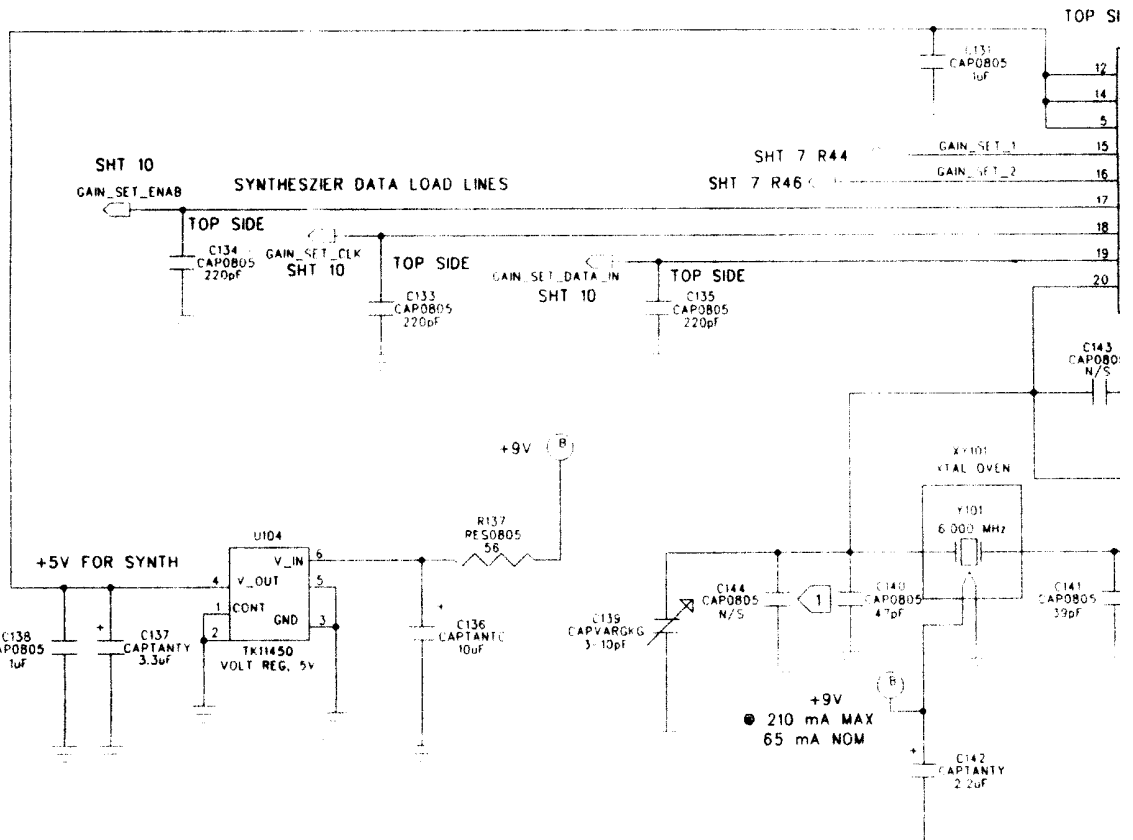
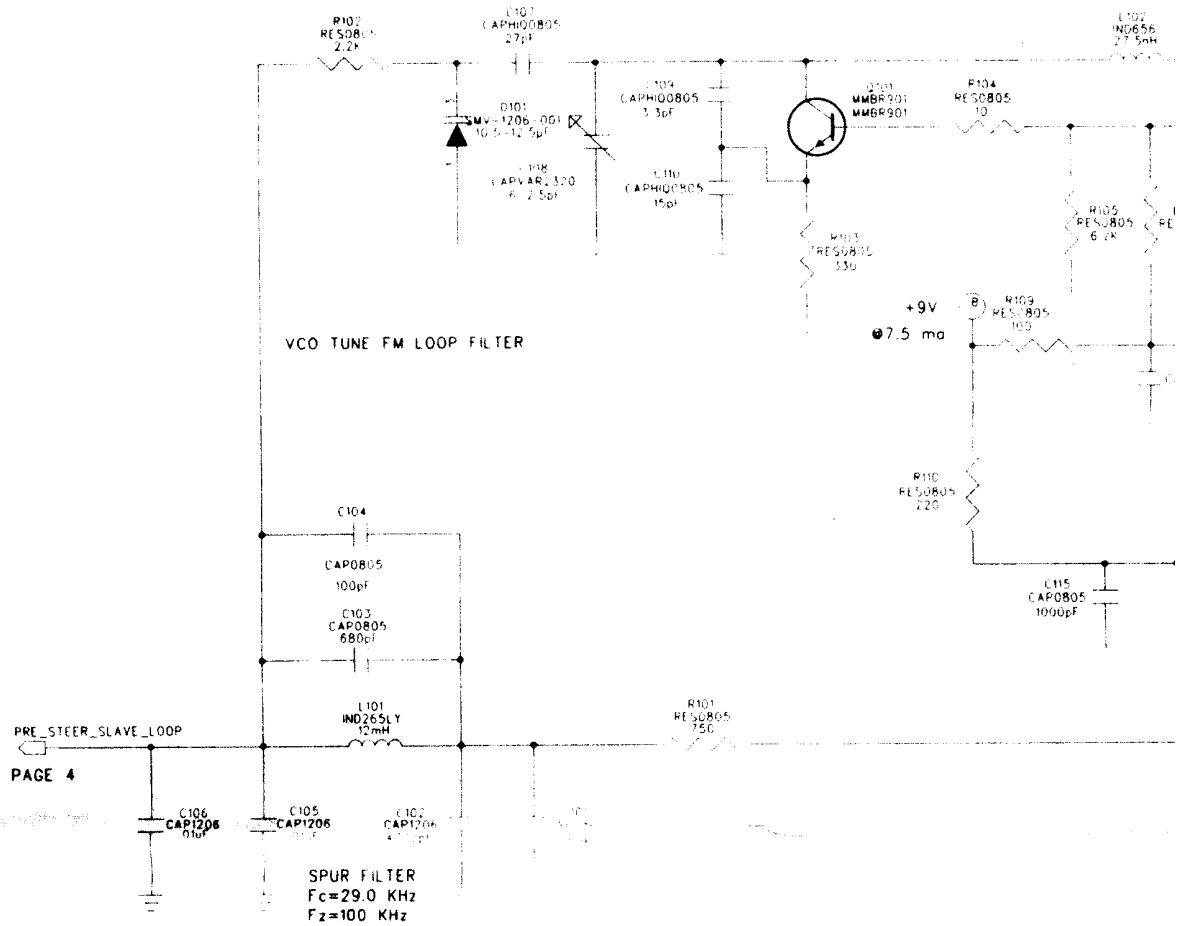
4

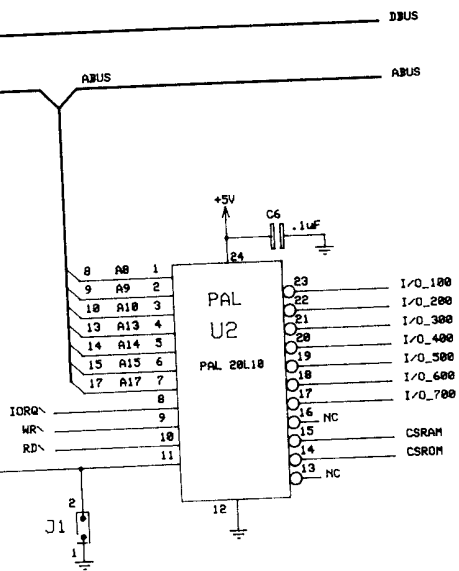
D

C

B

A





RESET

FCC ID: ENPESTEEM192M

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES ARE:

FRACTIONS	DECIMALS	ANGLES
±1/64	.XX ± .01	± 2°
	.XXX ± .005	

RELEASES	DATE	CHECKED
PREL.		
PROTO.		
QUAL.		
PROD.		

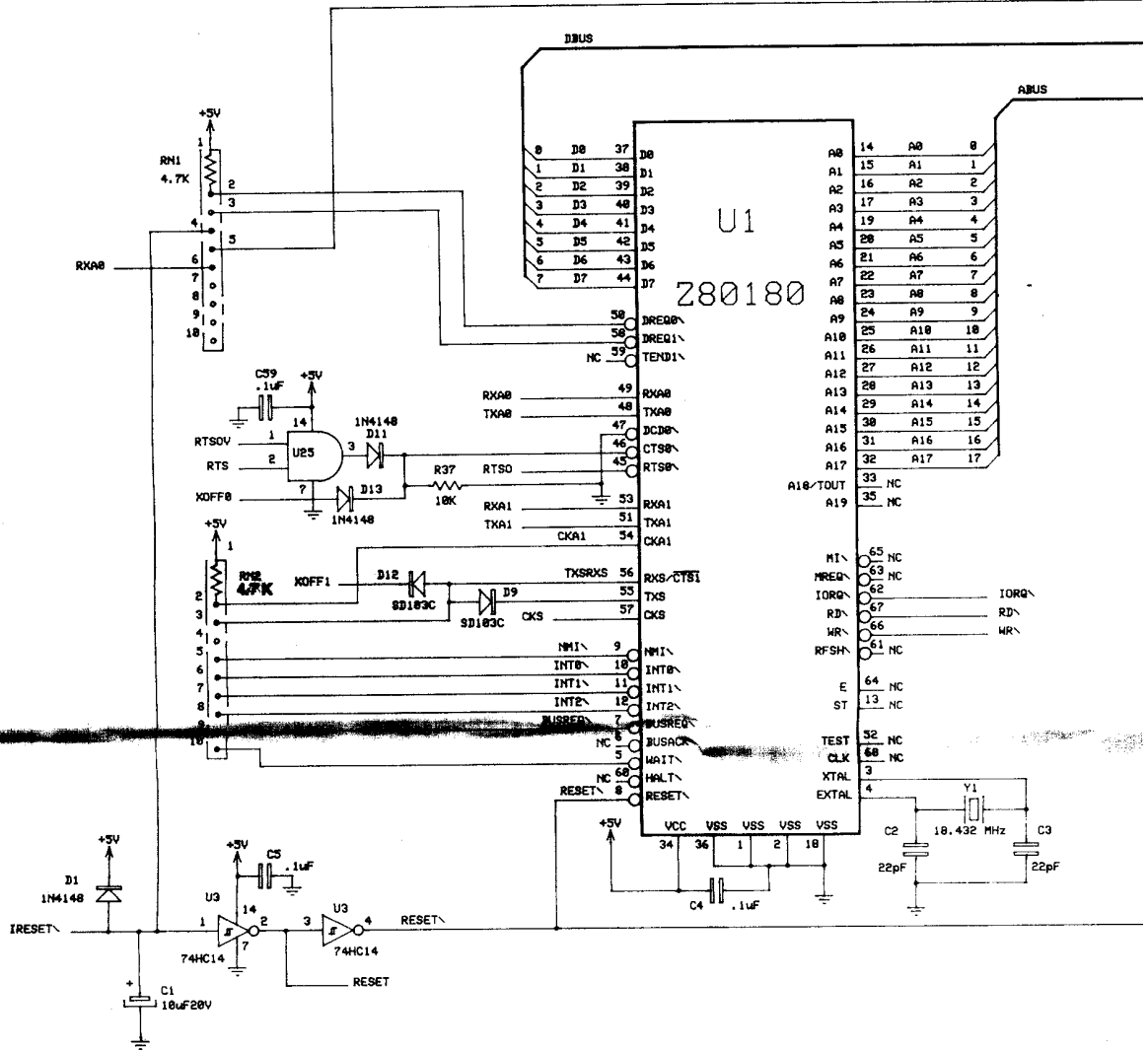
CONTRACT NO.	
APPROVALS	DATE
DRAWN J. C. Fredrickson	10/6/95
CHECKED	
ENGR.	
DRAWING NO.	
C00370	

ELECTRONIC SYSTEMS TECHNOLOGY

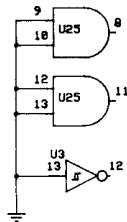
EST 192 CPU

CPU, RESET & I/O DECODE LOGIC

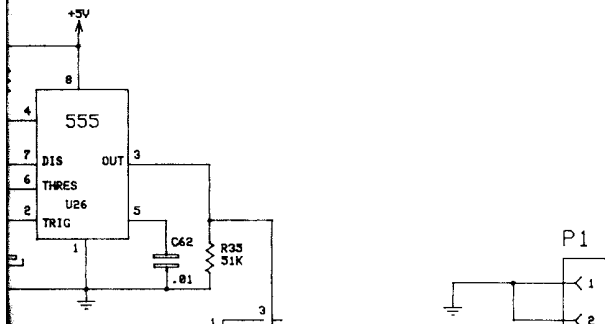
SIZE	CODE	INDENT NO.	PART NO.	REV.
C			25A100300	2



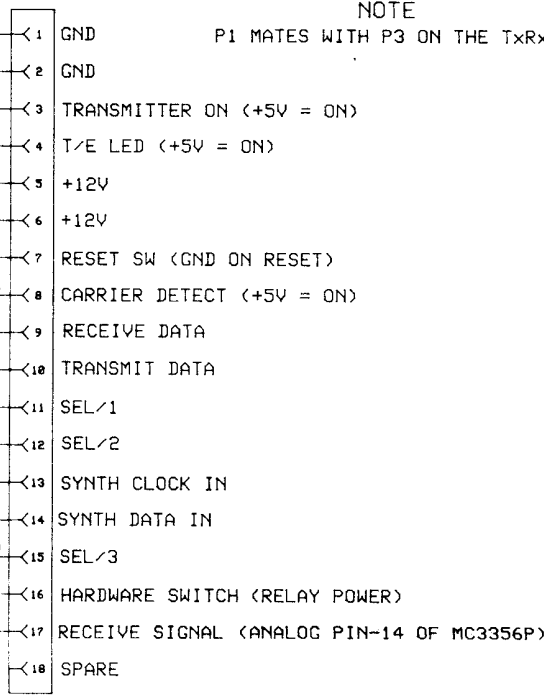
UNUSED GATES



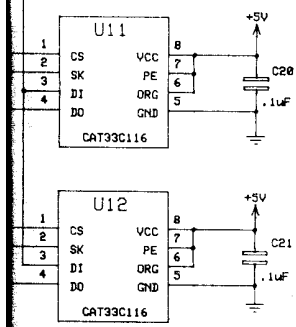
REVISION						REVISIC	
ZONE	LTR		APPROVED	DATE	ZONE	LTR	DESCRIP.
		ADDED U25, C59		1/26/96			



P1

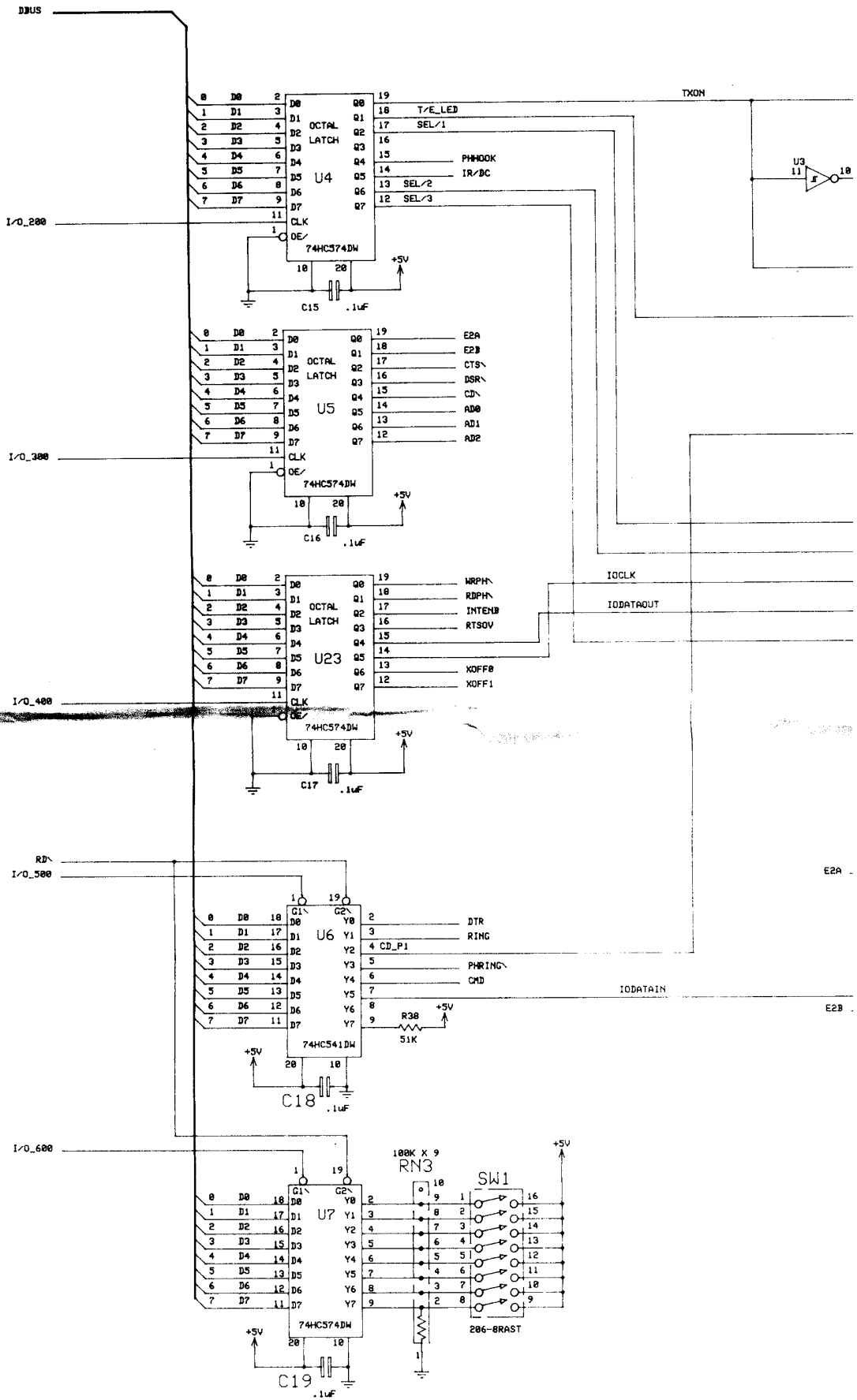


NOTE
P1 MATES WITH P3 ON THE TxRx CARD.

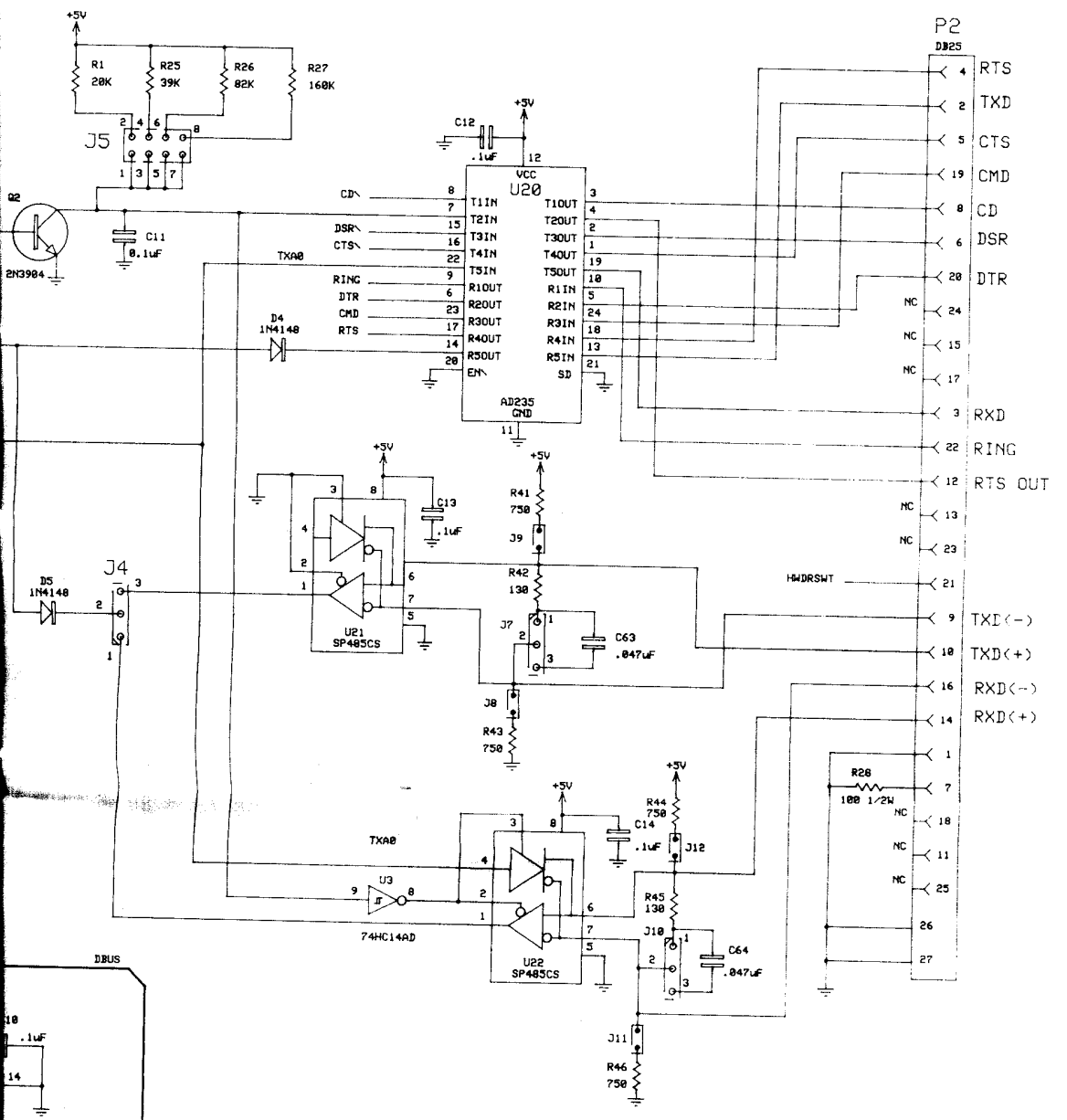


FCC ID: ENPESTEEM192M

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE:		CONTRACT NO.		ELECTRONIC SYSTEMS TECHNOLOGY			
FRACTIONS DECIMALS ANGLES 1/64 .XX ± .01 ± 2° .XXX ± .005		APPROVALS		DATE		EST 192 CPU	
RELEASES		DATE		DRAWN J. C. FREDRICKSON		18/6/95	
PREL.		CHECKED		PFC.		LOGIC / BOARD INTERCONNECT	
PROTO.		ENGR.		DRAWING NO.		SIZE	
QUAL.		PROD.		C00370		C	
APPROVED		DATE		CODE INDENT NO.		PART NO.	
				25A100300		REV. 2	
DO NOT SCALE DRAWING				SCALE		SHEET 3 OF 5	



REVISION						REVISION					
ZONE	LTR	DESCRIPTION	APPROVED	DATE	ZONE	LTR	DESCRIPTION	APPROVED	DATE		
		ADDED R32 (390), R38 (51K)		6/14/96							

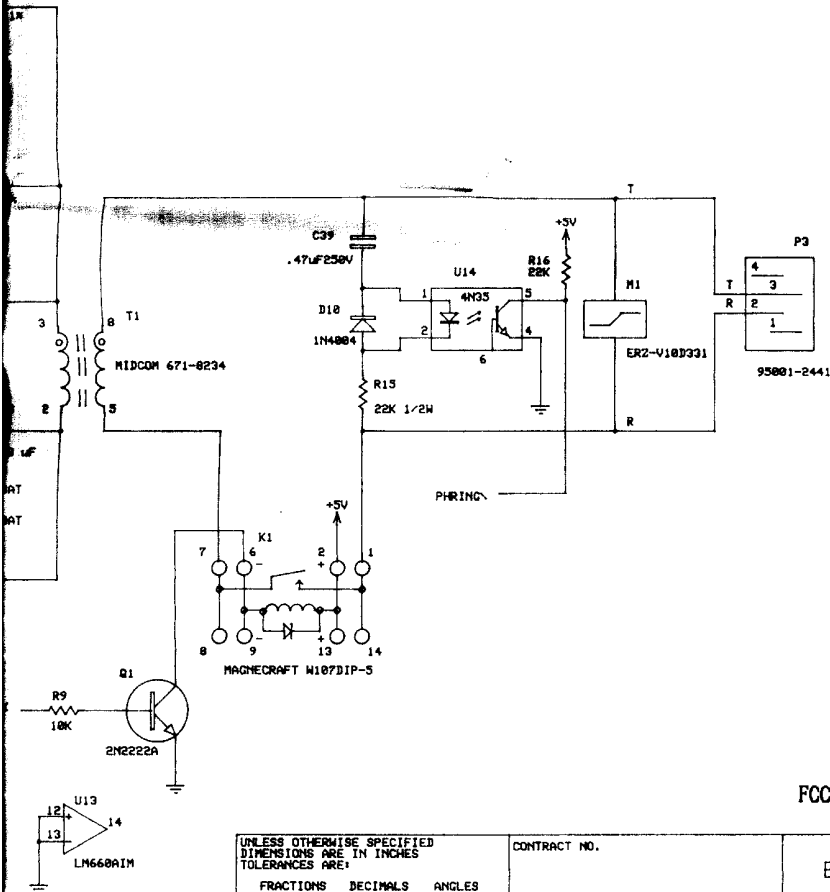
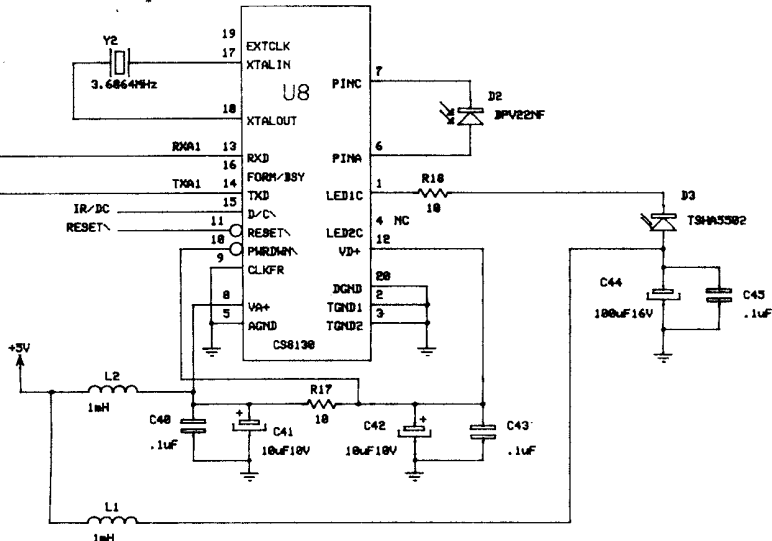


P2	DB25	Signal
4		RTS
2		TXD
5		CTS
19		CMD
8		CD
6		DSR
20		DTR
24	NC	
15	NC	
17	NC	
3		RXD
22		RING
12		RTS OUT
13	NC	
23	NC	
21		H4DRSWT
9		TXI(-)
18		TXD(+)
16		RXD(-)
14		RXD(+)
1		
7		R28 100 1/2W
18	NC	
11	NC	
25	NC	
26		
27		

19	D7	7
18	D6	6
17	D5	5
16	D4	4
15	D3	3
13	D2	2
12	D1	1
11	D0	0

FCC ID: ENPESTEEM192M

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE:		CONTRACT NO.		ELECTRONIC SYSTEMS TECHNOLOGY	
FRACTIONS	DECIMALS	ANGLES	APPROVALS		DATE
±1/64	.XX ± .01	± 2°	DRAWN	J. C. FREDRICKSON	10/6/95
RELEASES	DATE	CHECKED	EST 192 CPU		
PREL.		PFC.	MEMORY / RS-232 / RS-485		
PROY.		ENGR.	DRAWING NO. C00370		
QUAL.			SIZE	CODE	INDENT NO. PART NO.
PROD.			C		25A100300
APPROVED	DATE	REV.	2		
DO NOT SCALE DRAWING					



FCC ID: ENPESTEEM192M

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES ARE:

FRACTIONS	DECIMALS	ANGLES
±1/64	±.01	± 2°
	±.005	

RELEASES	DATE
PREL.	
PROTO.	
QUAL.	
PROD.	

CONTRACT NO.	
APPROVALS	DATE
DRAWN J. C. FREDRICKSON	10/6/95
CHECKED	
MFG.	
ENGR.	
DRAWING NO.	
C00370	

ELECTRONIC SYSTEMS TECHNOLOGY			
EST 192 CPU			
PHONE/IR			
SIZE	CODE INVENT NO.	PART NO.	REV.
C		25A100300	2

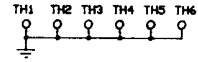
APPROVED	DATE

DO NOT SCALE DRAWING

SCALE

SHEET 4 OF 5

TOOLING HOLES



FCC ID: ENPESTEEM192M

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE:		CONTRACT NO.		ELECTRONIC SYSTEMS TECHNOLOGY			
FRACTIONS DECIMALS ANGLES ±1/64 .XX ±.01 ± 2°		APPROVALS DATE		EST 192 CPU RF MODEM / RECEIVE SIGNAL			
RELEASES DATE		DRAWN J. C. FREDRICKSON 10/6/95					
PREL.		CHECKED					
PROG.		MFG.					
QUAL.		ENGR.					
PROD.		DRAWING NO.		SIZE	CODE INDENT NO.	PART NO.	REV.
APPROVED DATE		C00370		C		25A100300	2
DO NOT SCALE DRAWING				SCALE		SHEET 5 OF 5	

