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REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
X	PROD REL PER ECO NEW		

D

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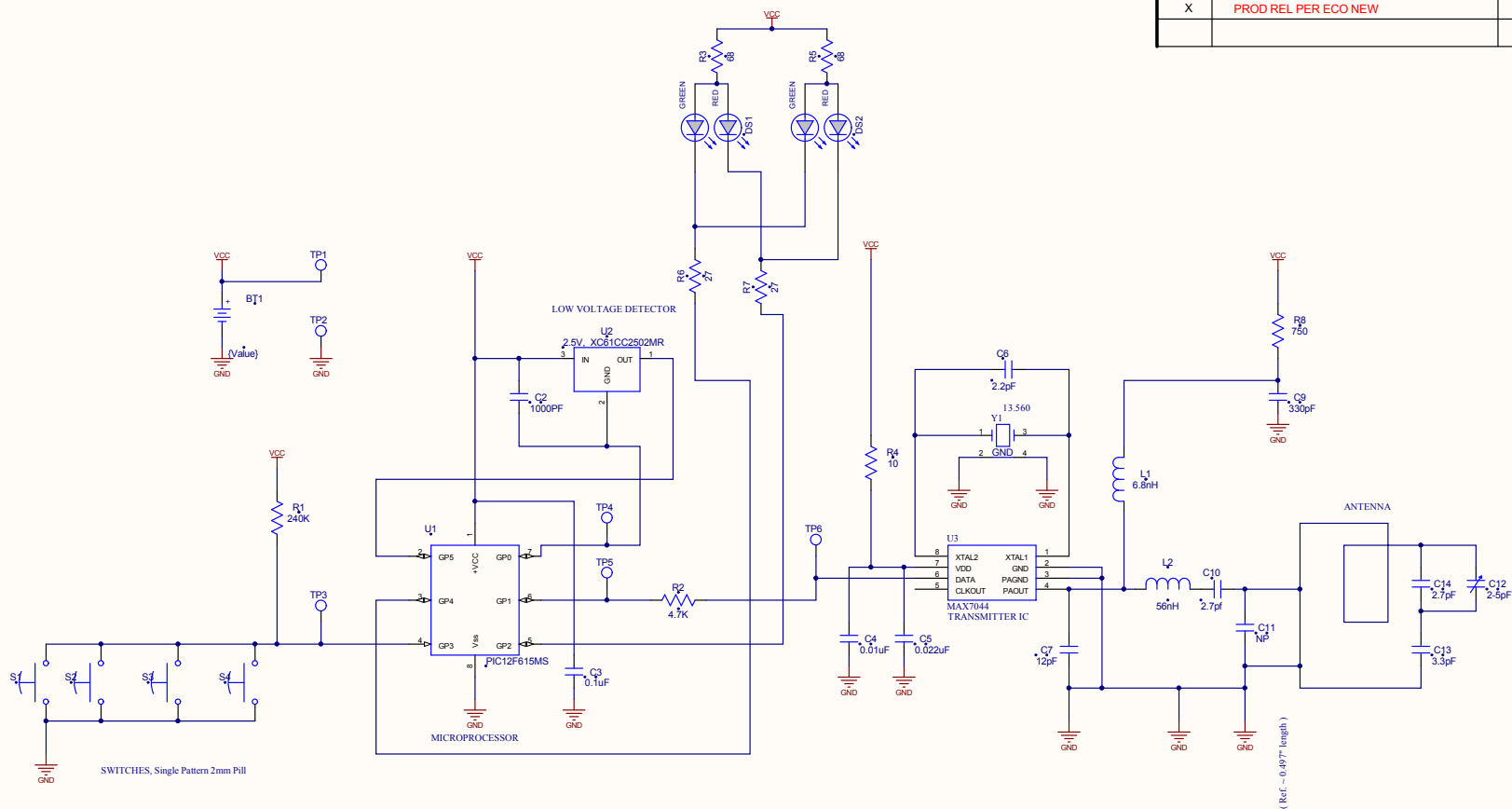
C

B

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4. NP = COMPONENT NOT PLACED ON PCB .
3. ALL RESISTOR VALUES ARE IN OHMS.
2. ALL CAPACITOR VALUES ARE IN MICROFARADS.
1. THIS SCHEMATIC TO BE USED IN CONJUNCTION WITH APPROPRIATE ASSEMBLY DRAWING AND PARTS LIST.

NOTES: (UNLESS OTHERWISE SPECIFIED)

IN-CIRCUIT PROGRAMMING NOTES.

TP1 = VCC, 3.0VDC, INPUT VOLTAGE
 TP2 = GND, CIRCUIT GROUND
 TP3 = VPP, PROGRAMMING VOLTAGE
 TP4 = ICSPCLK, PROGRAMMING CLOCK
 TP5 = ICSPDAT, PROGRAMMING DATA
 TP6 = GND, TO PREVENT TRANSMITTER OPERATION DURING PROGRAMMING

Linear
 CARLSBAD, CA

		Linear. CARLSBAD, CA			
CAD OPERATOR: D. Sanderson	DATE 07/29/2013	TITLE: SCHEMATIC, CX-65			
CHECKED:	DATE				
APPROVED:	DATE	SIZE: B	DRAWING NO.: 235002		REV.: X3
APPROVED:	DATE	SCALE: NONE		SHEET: 1 OF 1	

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