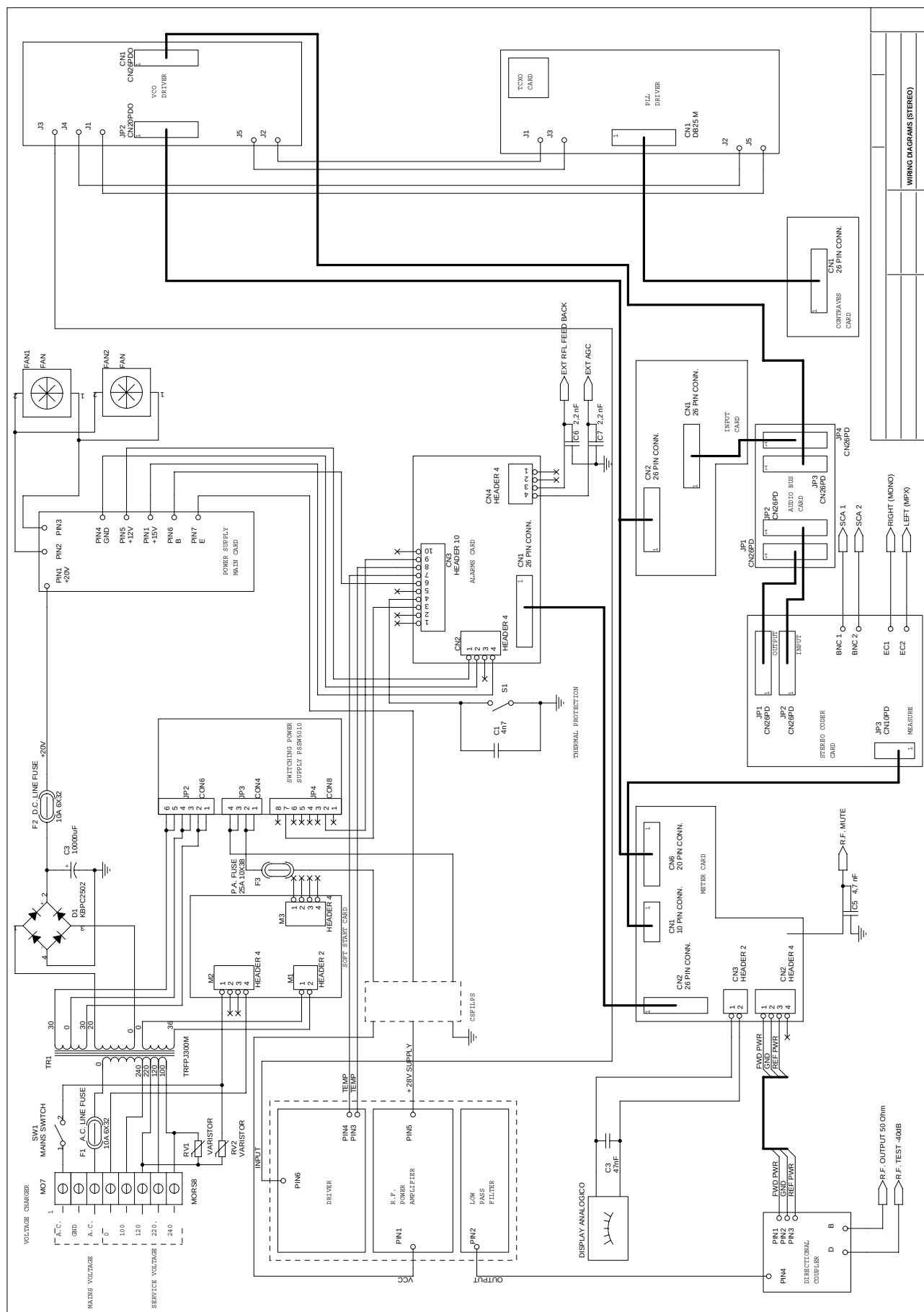
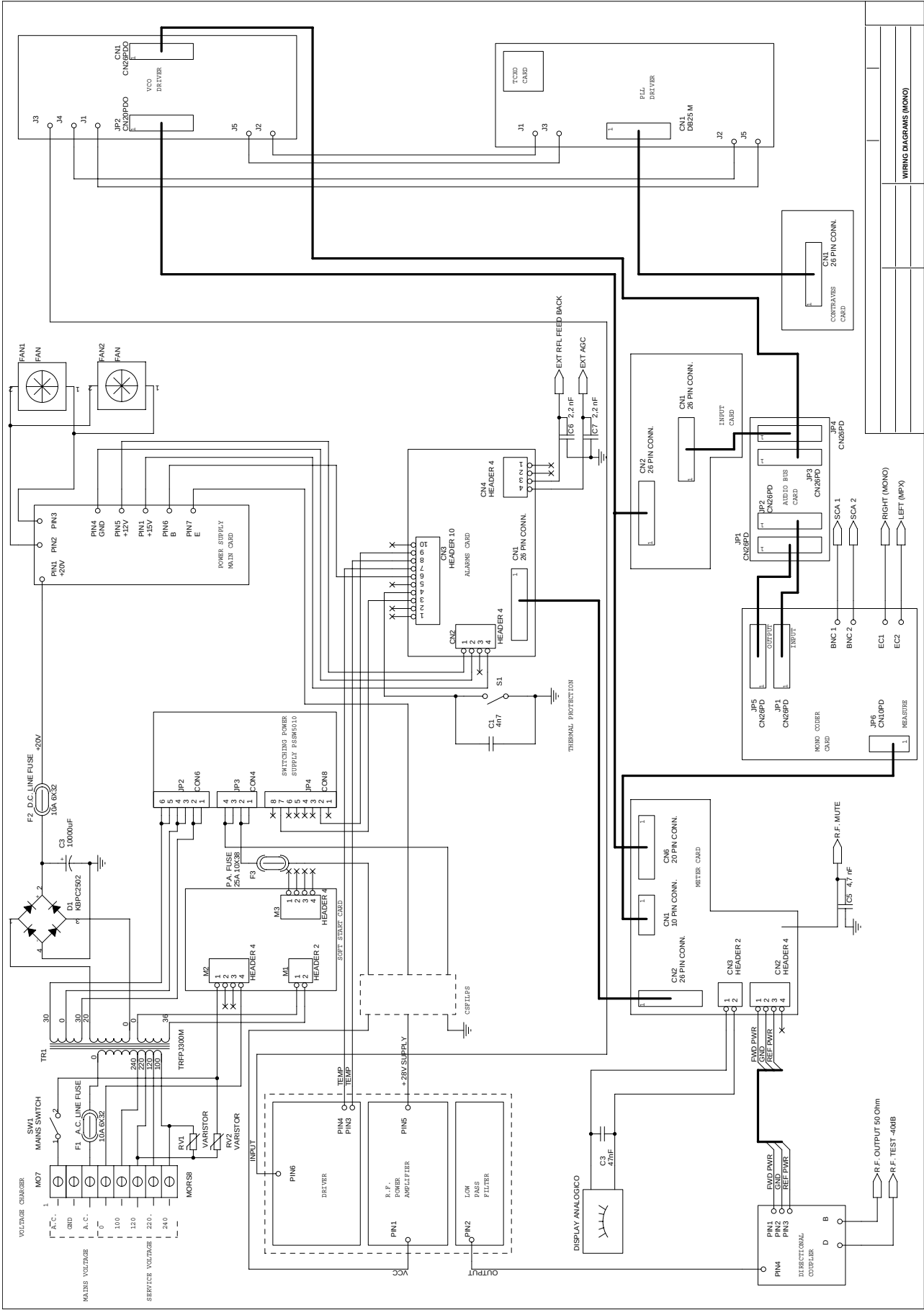
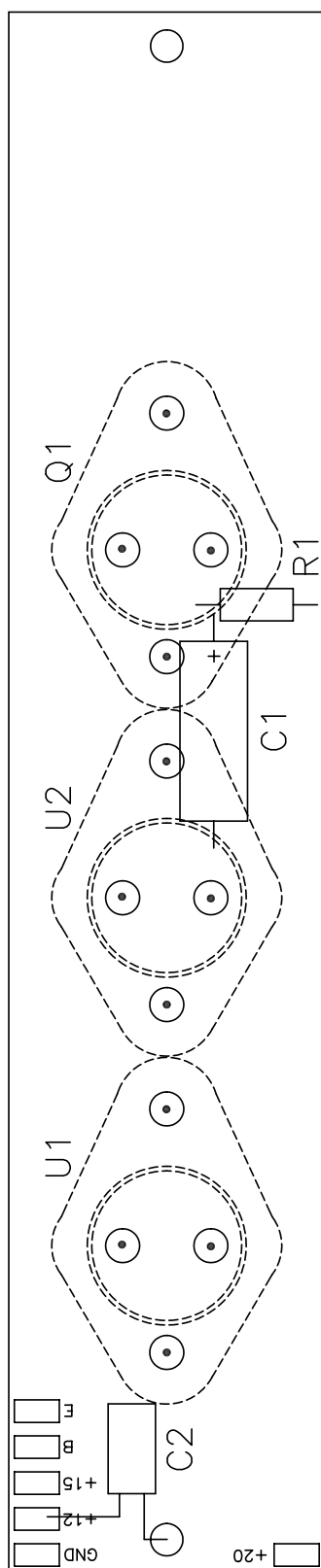
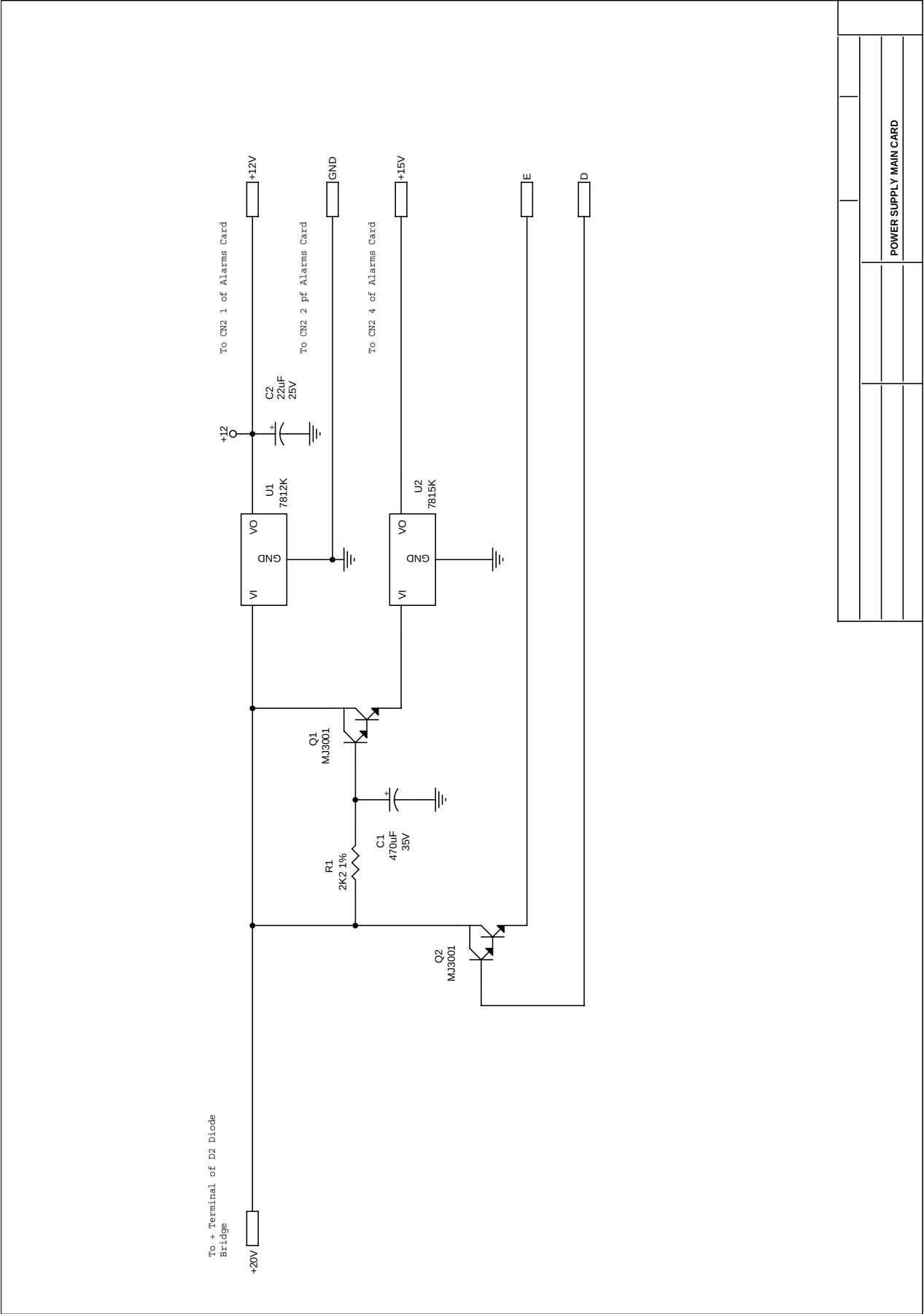


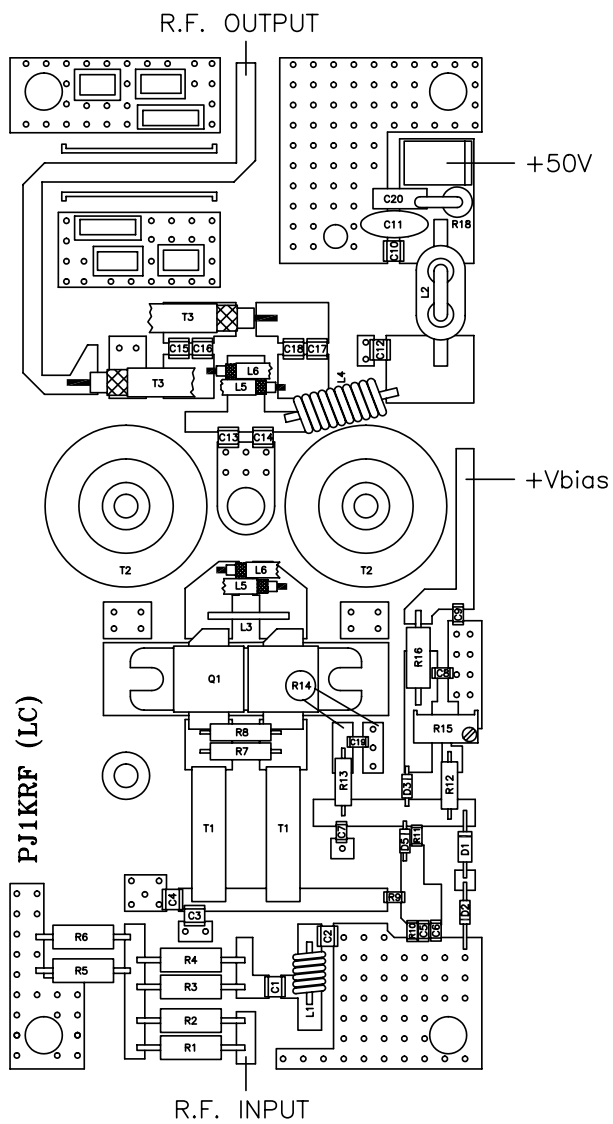


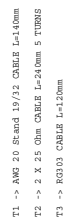
WIRING DIAGRAMS (STEREO)

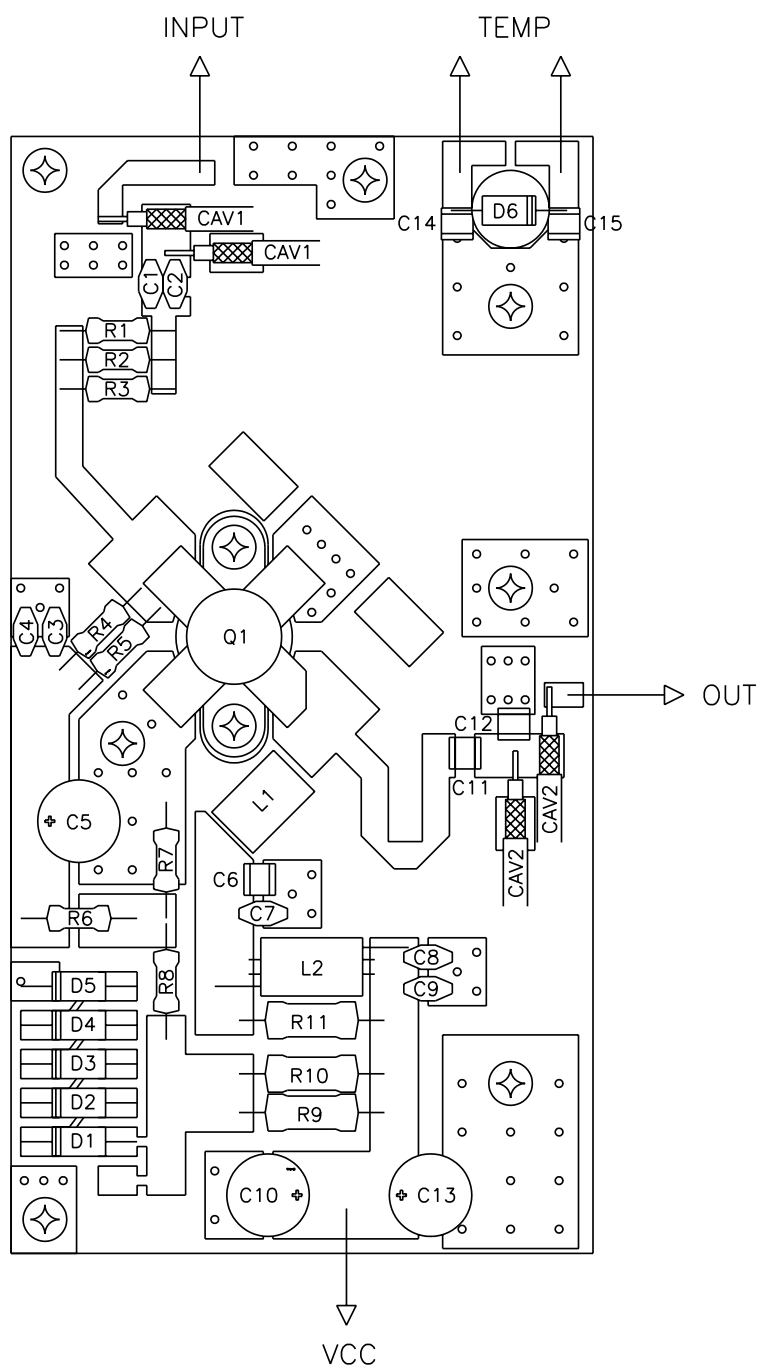


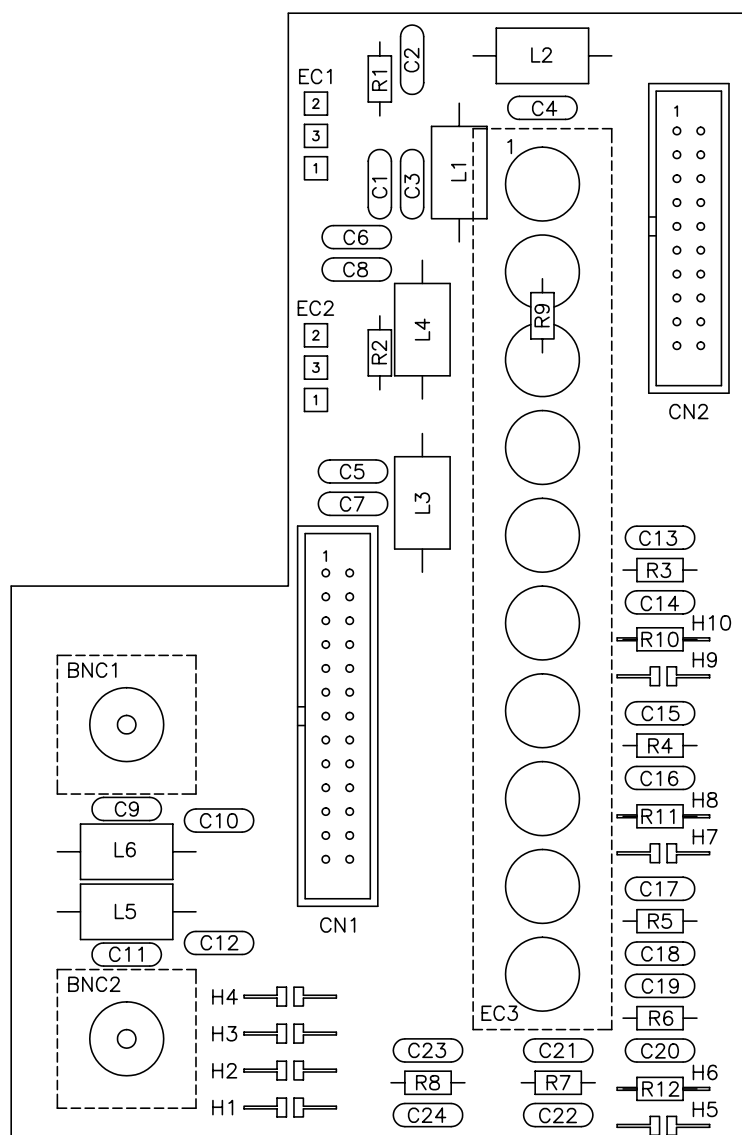


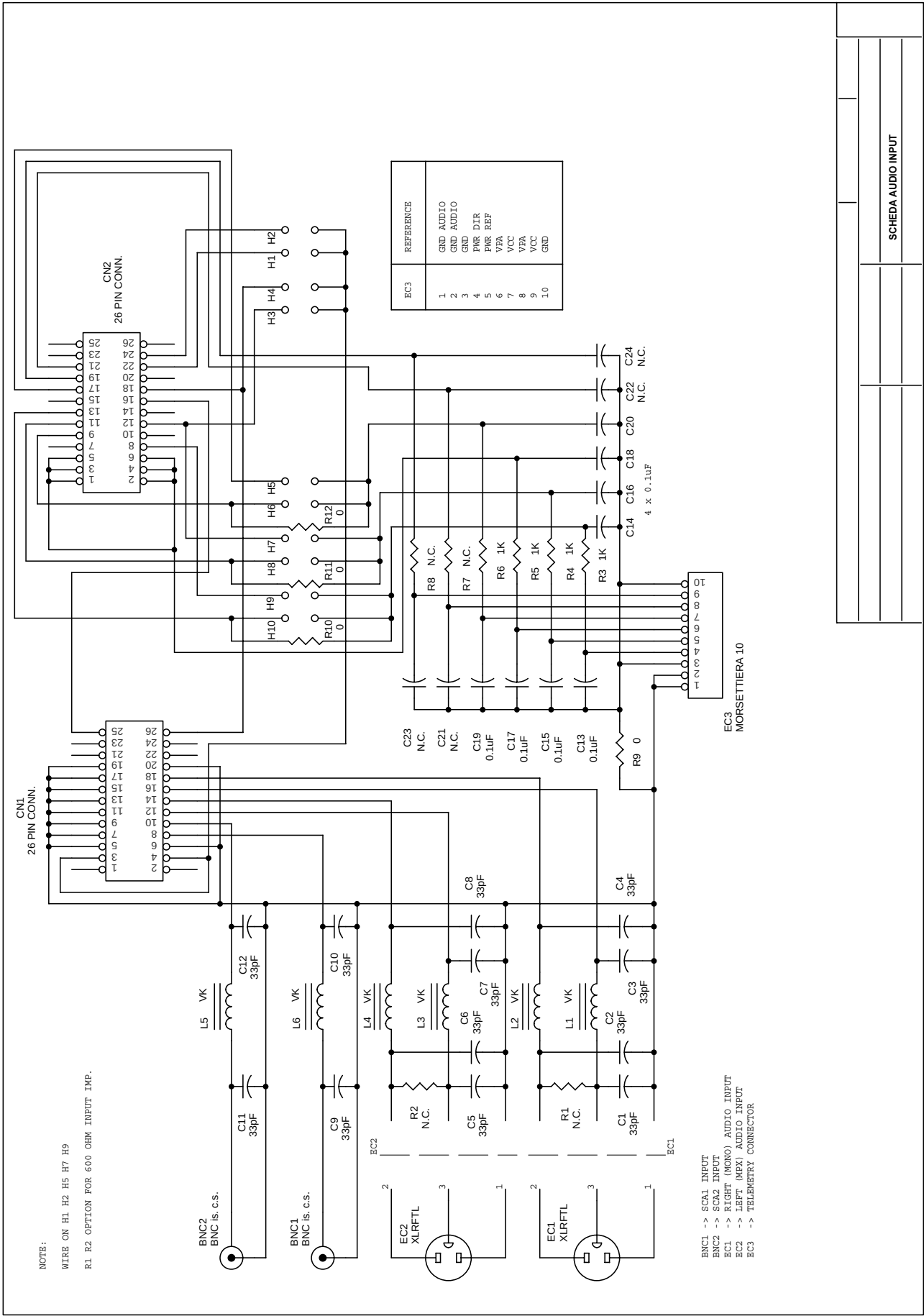


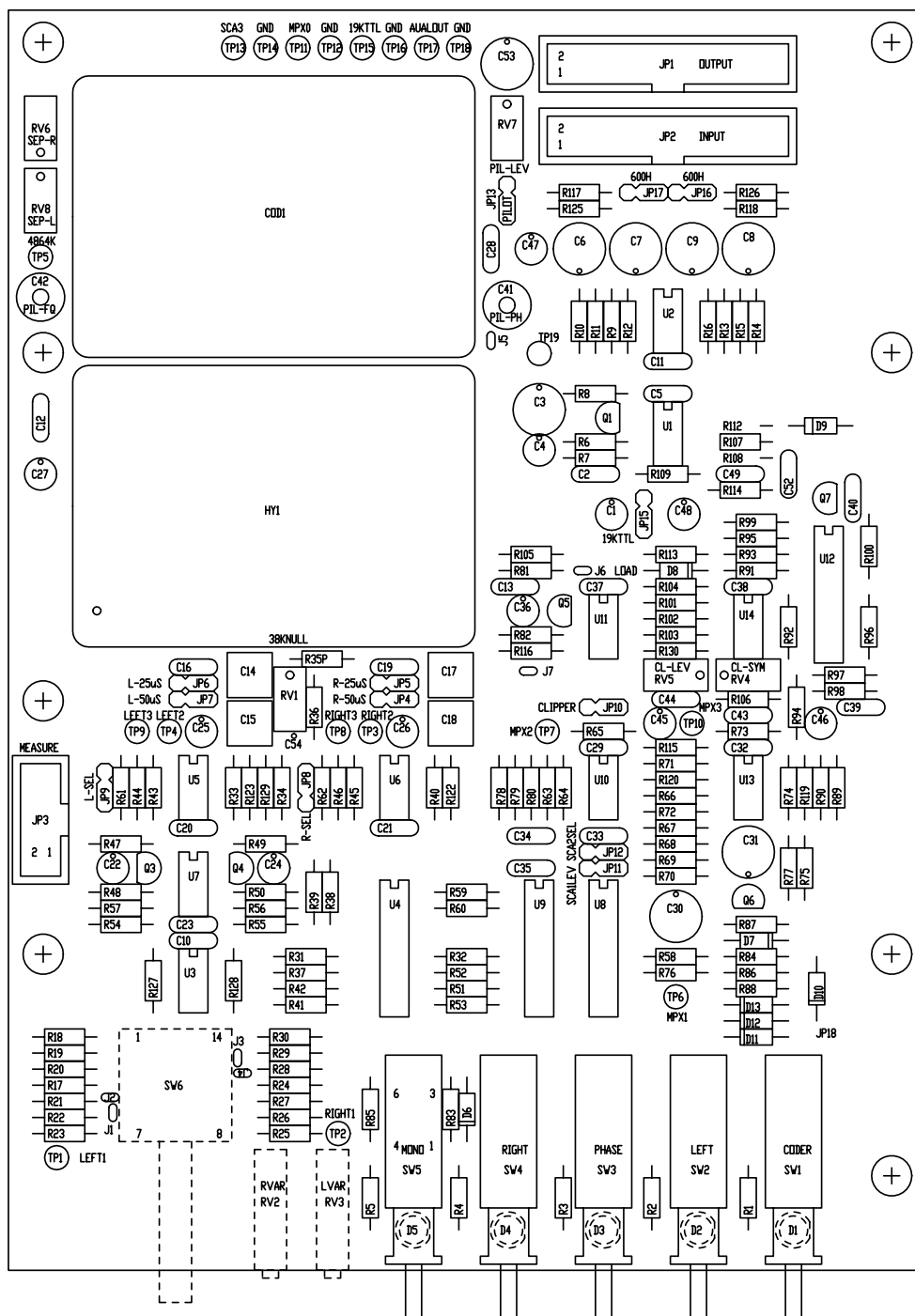


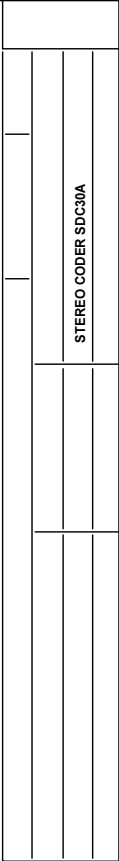




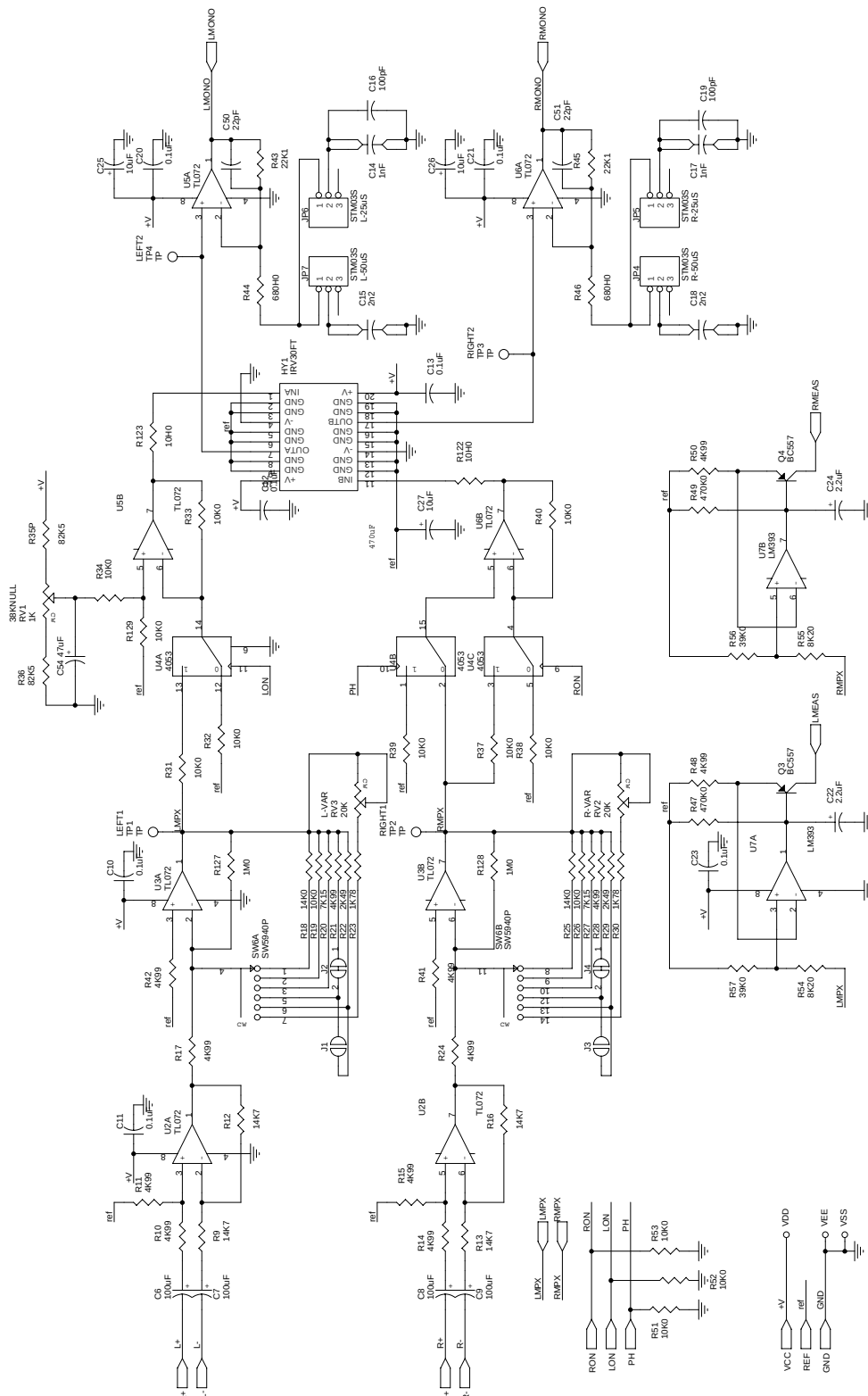






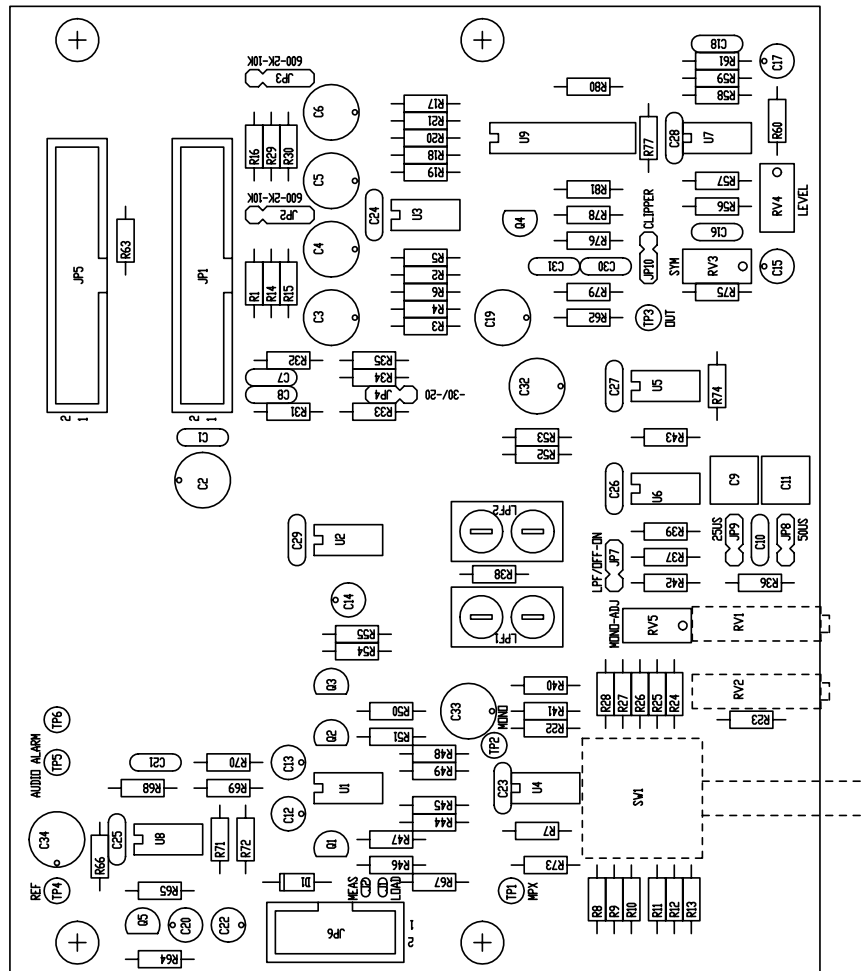


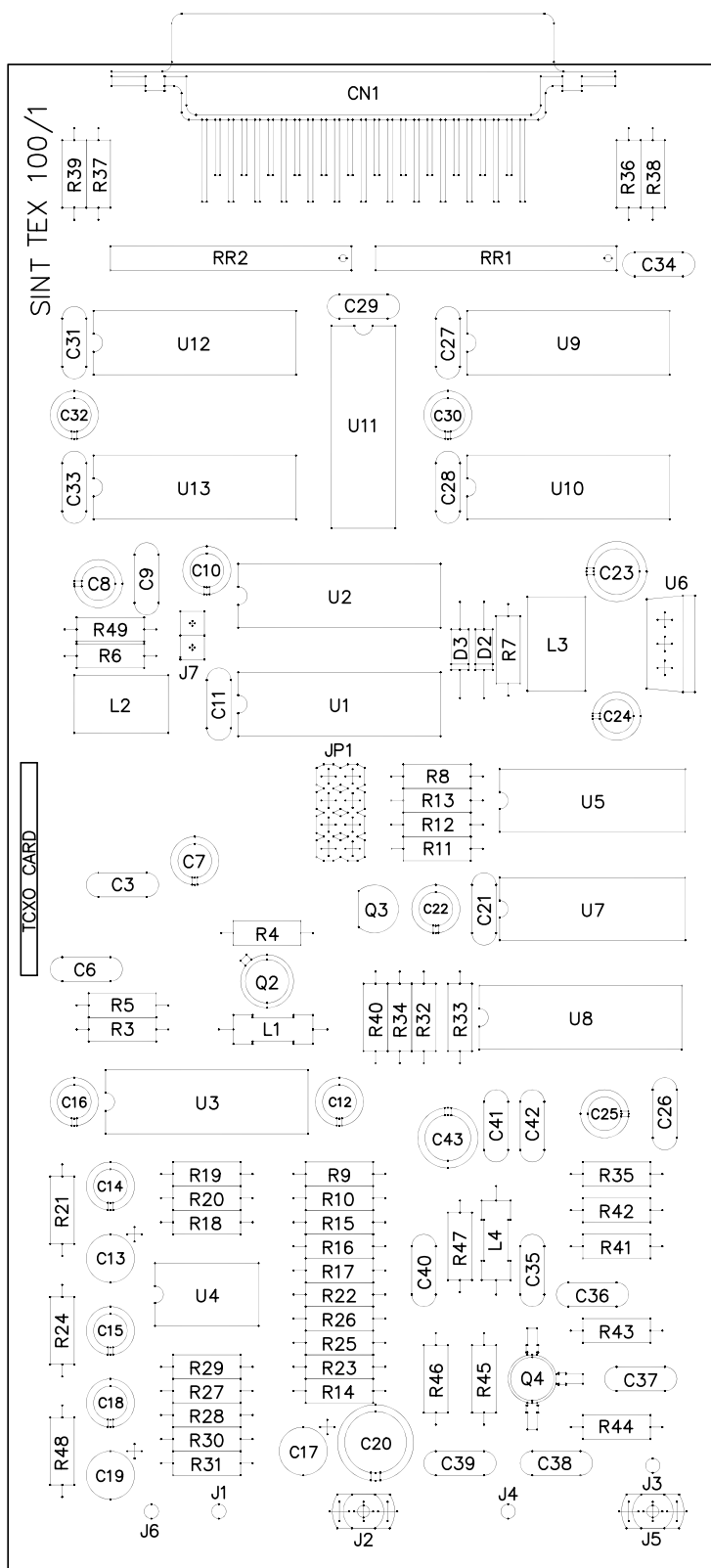
S96, RV2, RV3 soldered on the side
J1, J2, J3 and J4

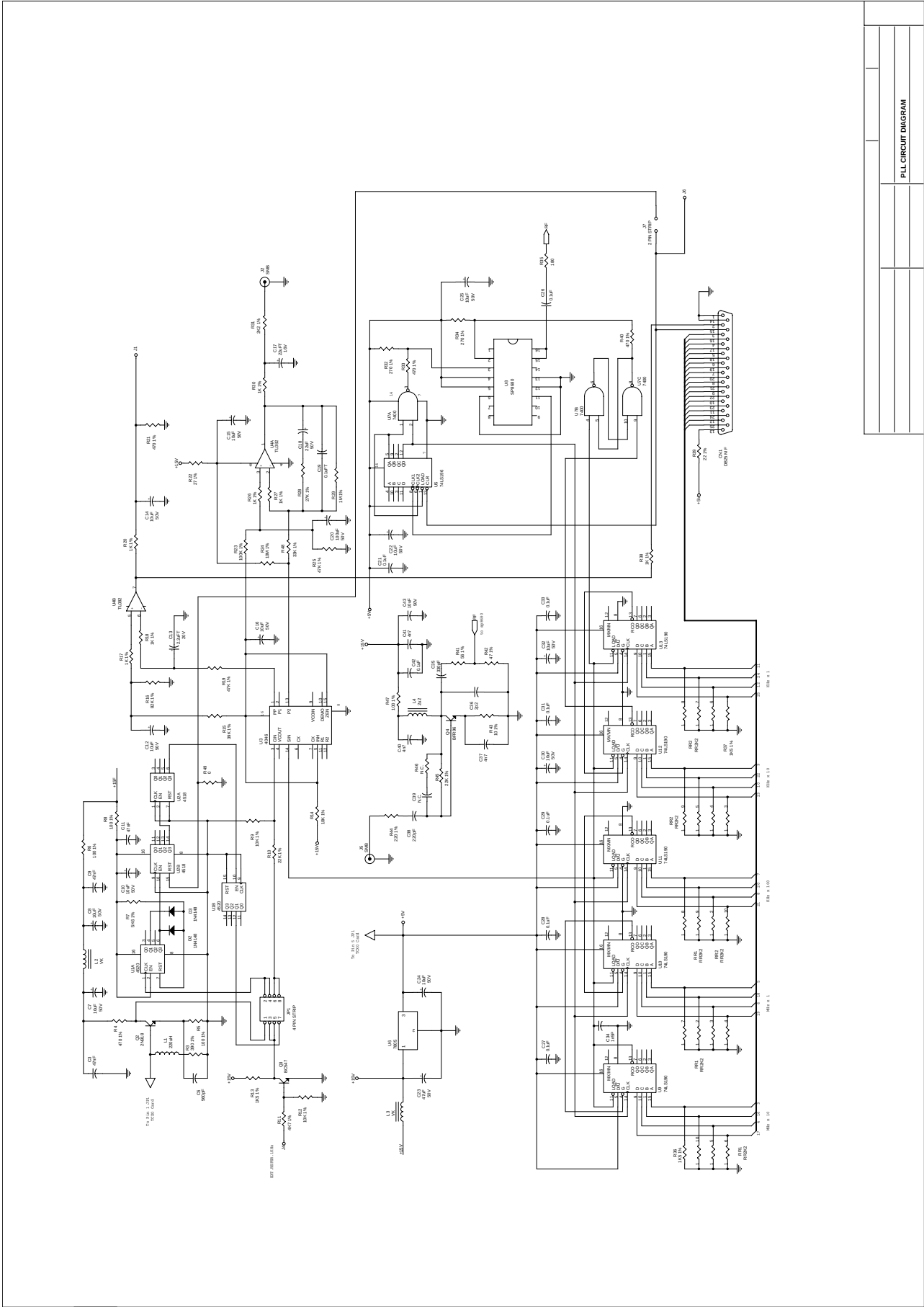


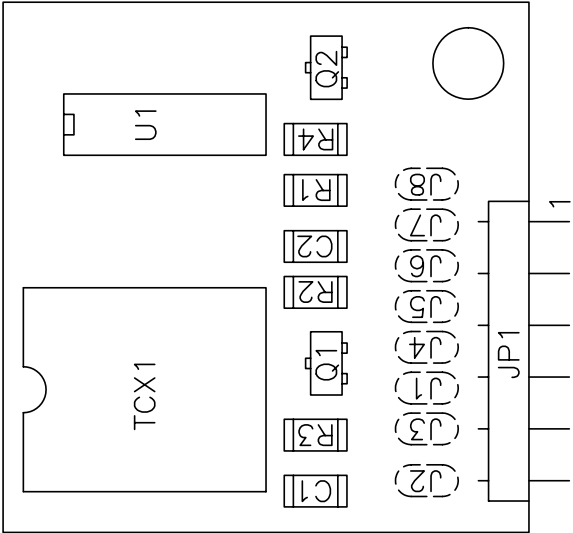
INPUT FILTER PREDIAPHASYS

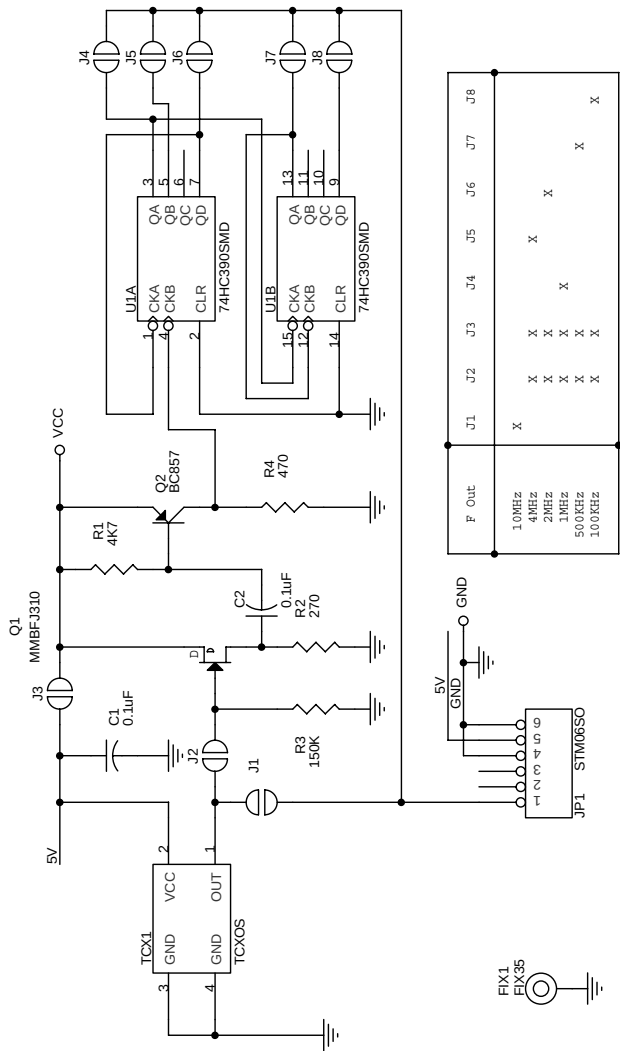






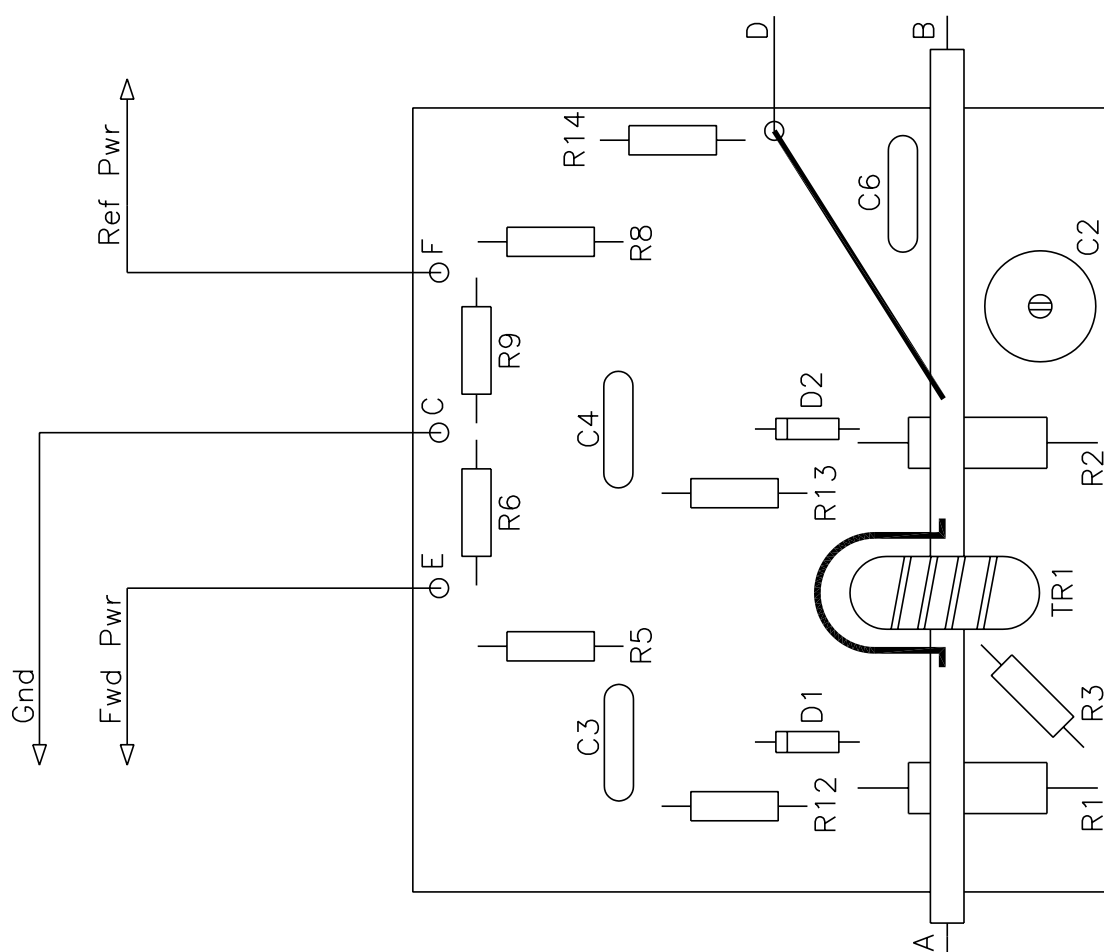


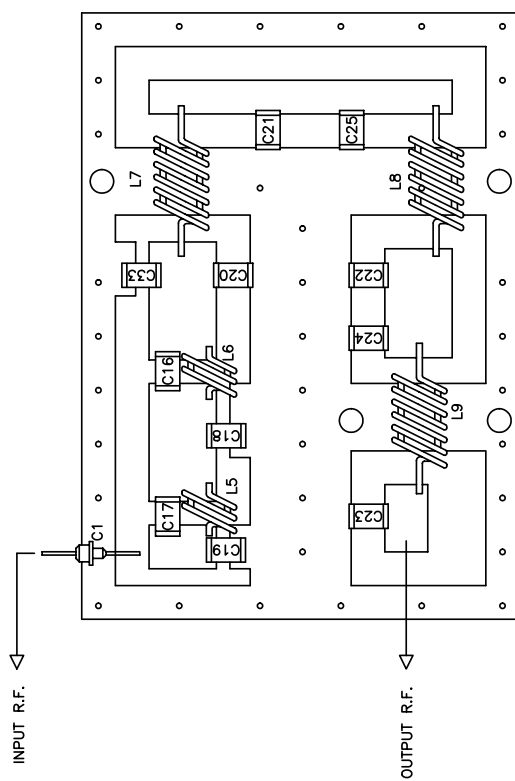


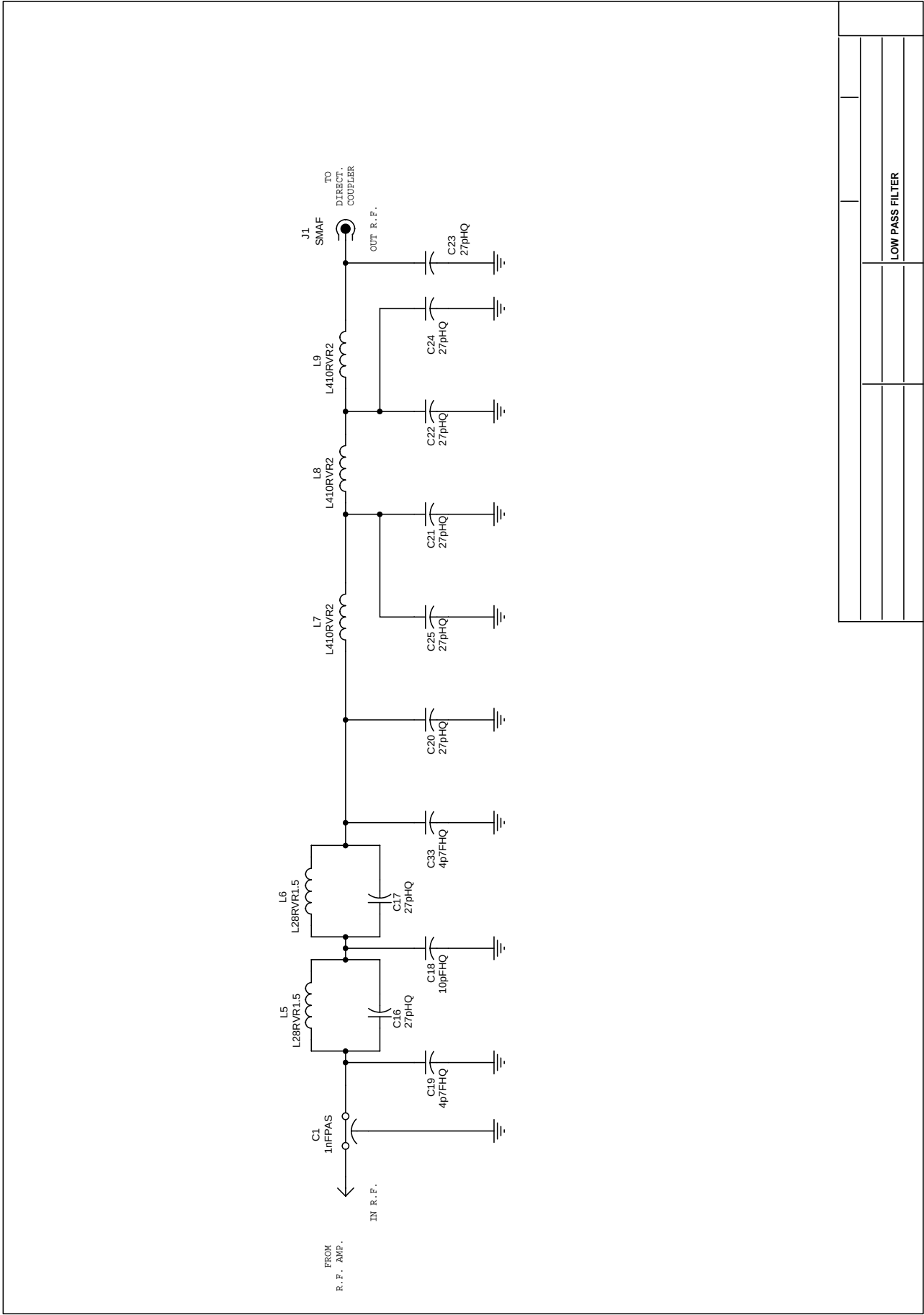


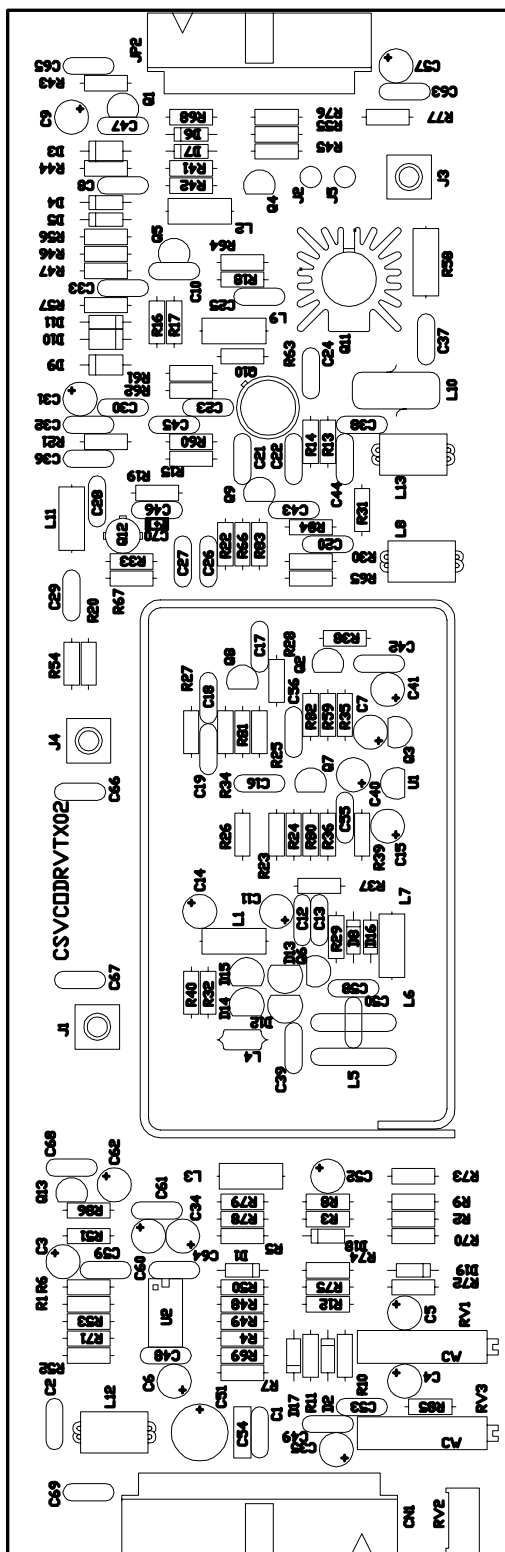
P Out	J1	J2	J3	J4	J5	J6	J7	J8
100KHz	X							
40KHz		X	X		X			
20KHz		X	X					
1MHz		X	X	X				
500KHz		X	X			X		
100KHz		X	X				X	

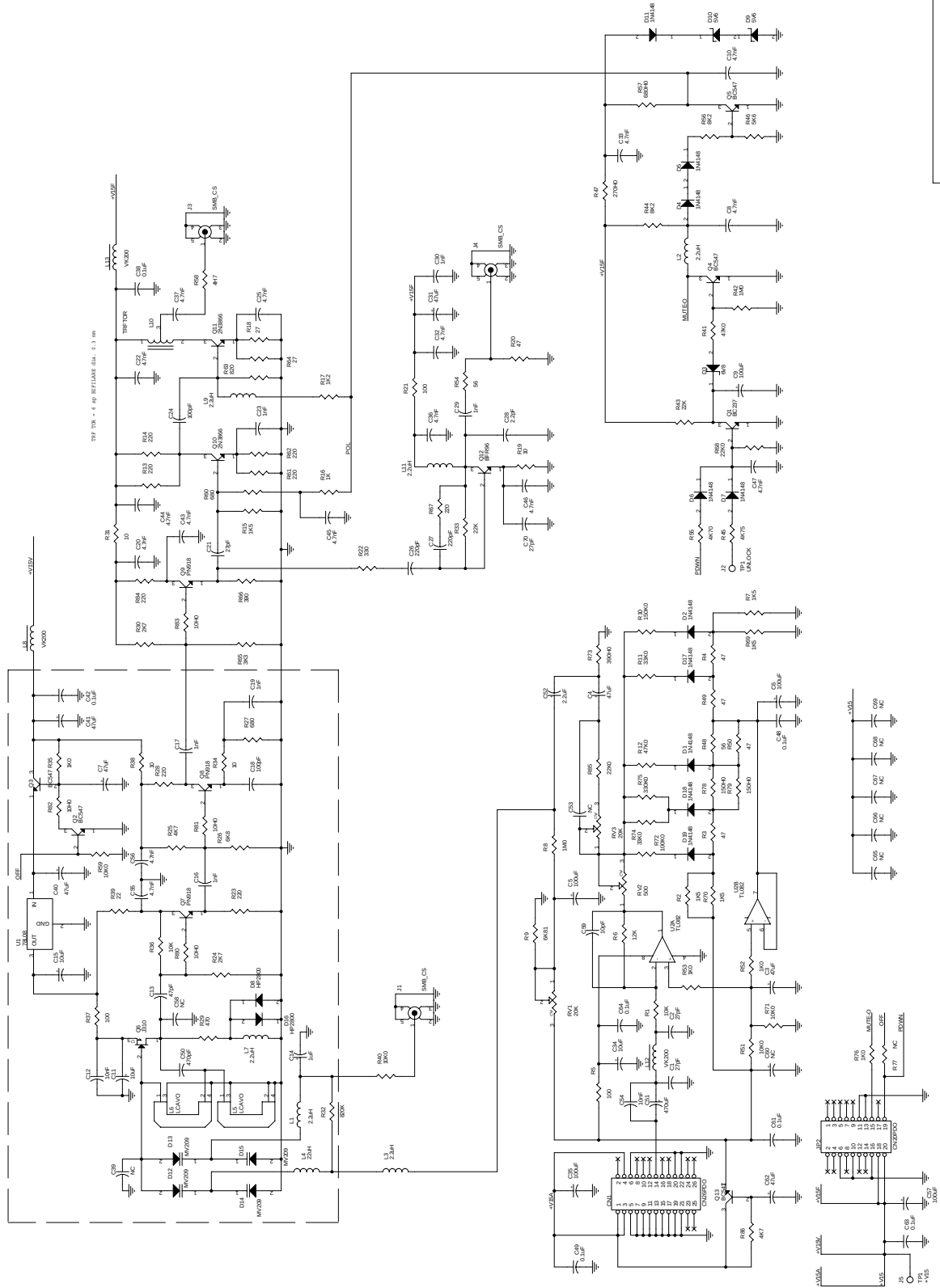
[illegible]



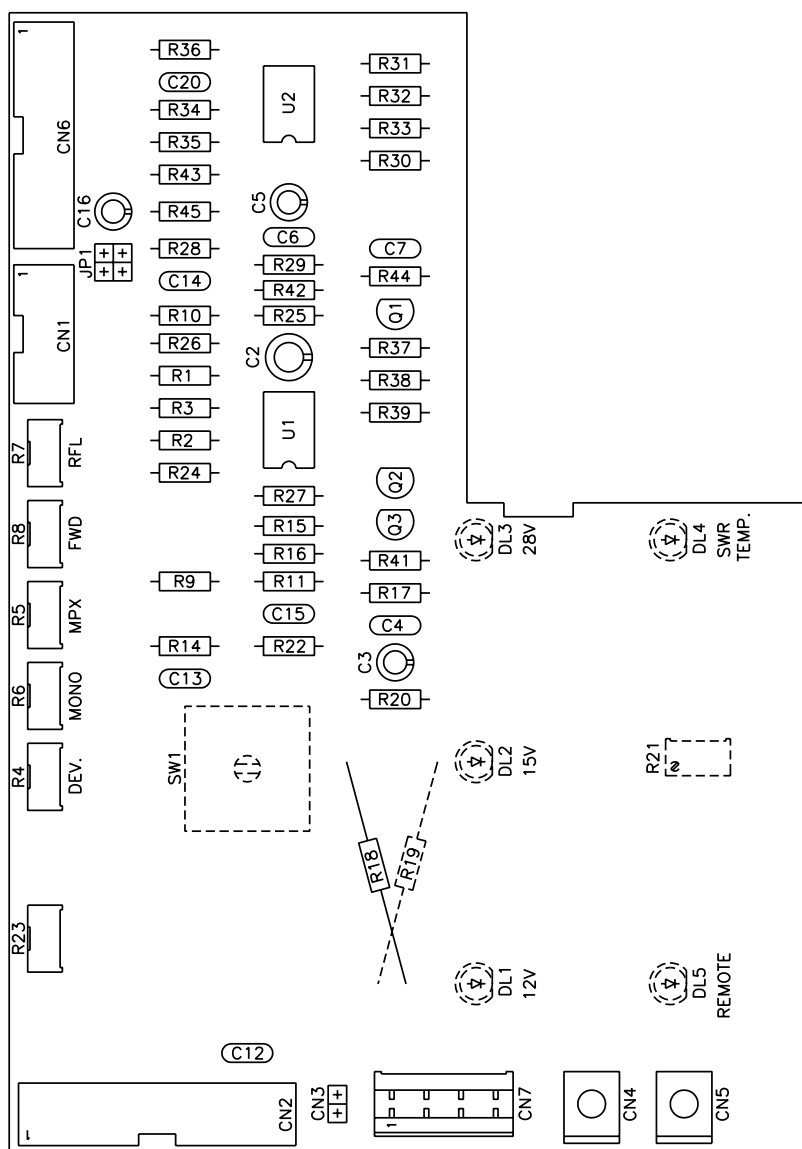


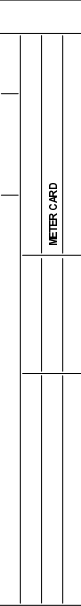


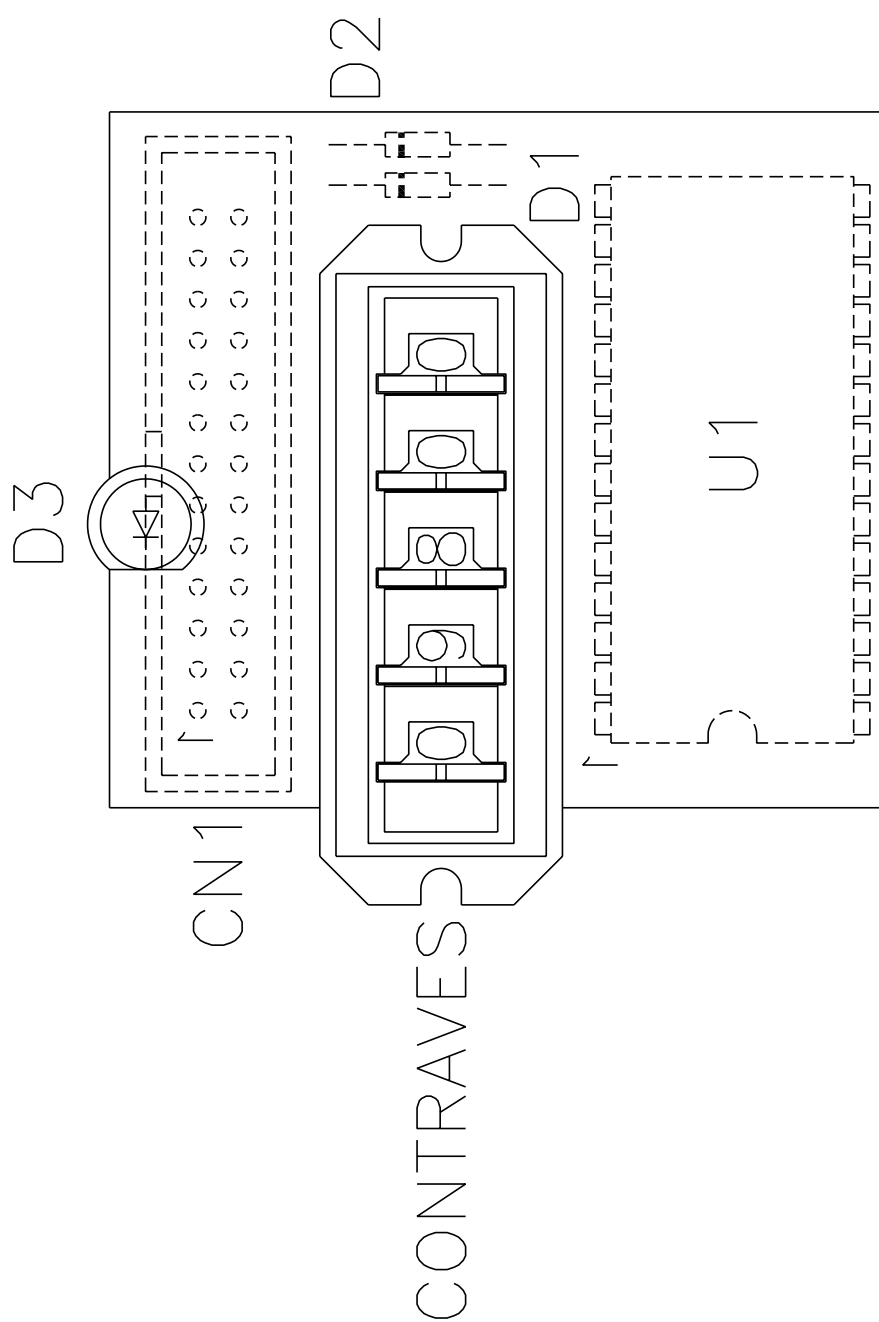


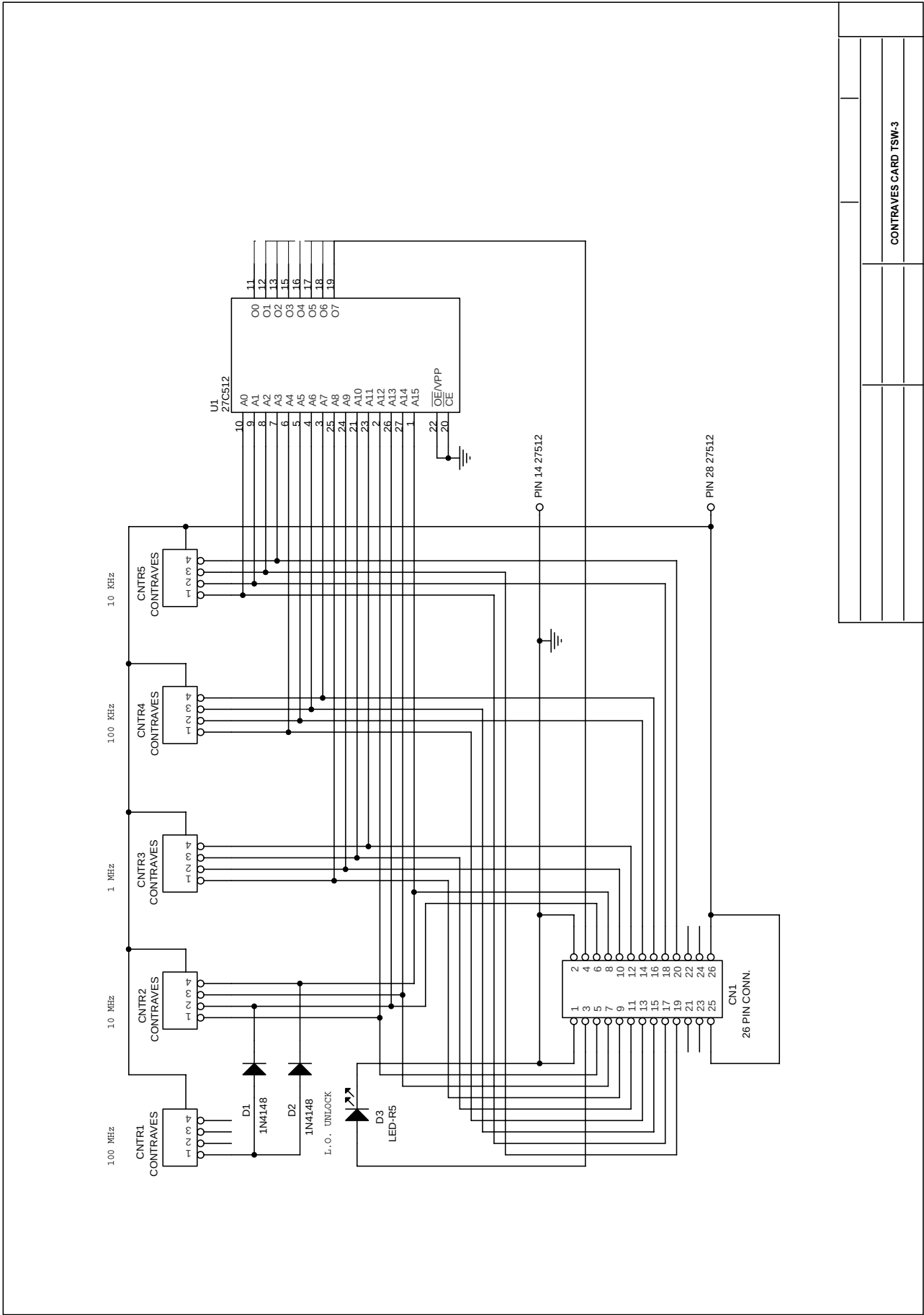


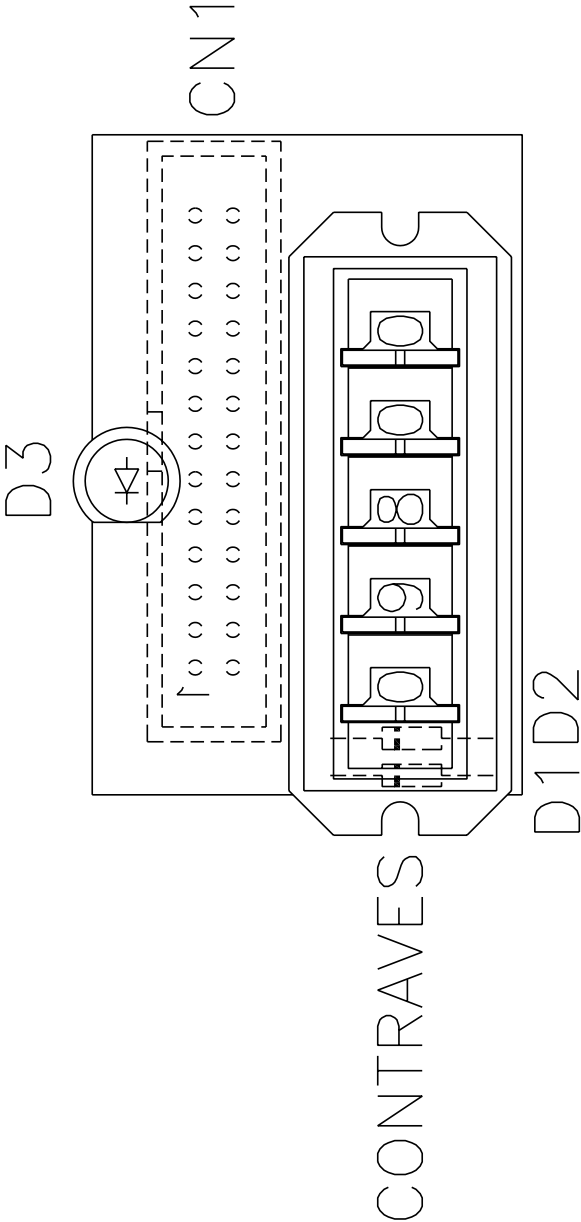
		VCO DRIVER		

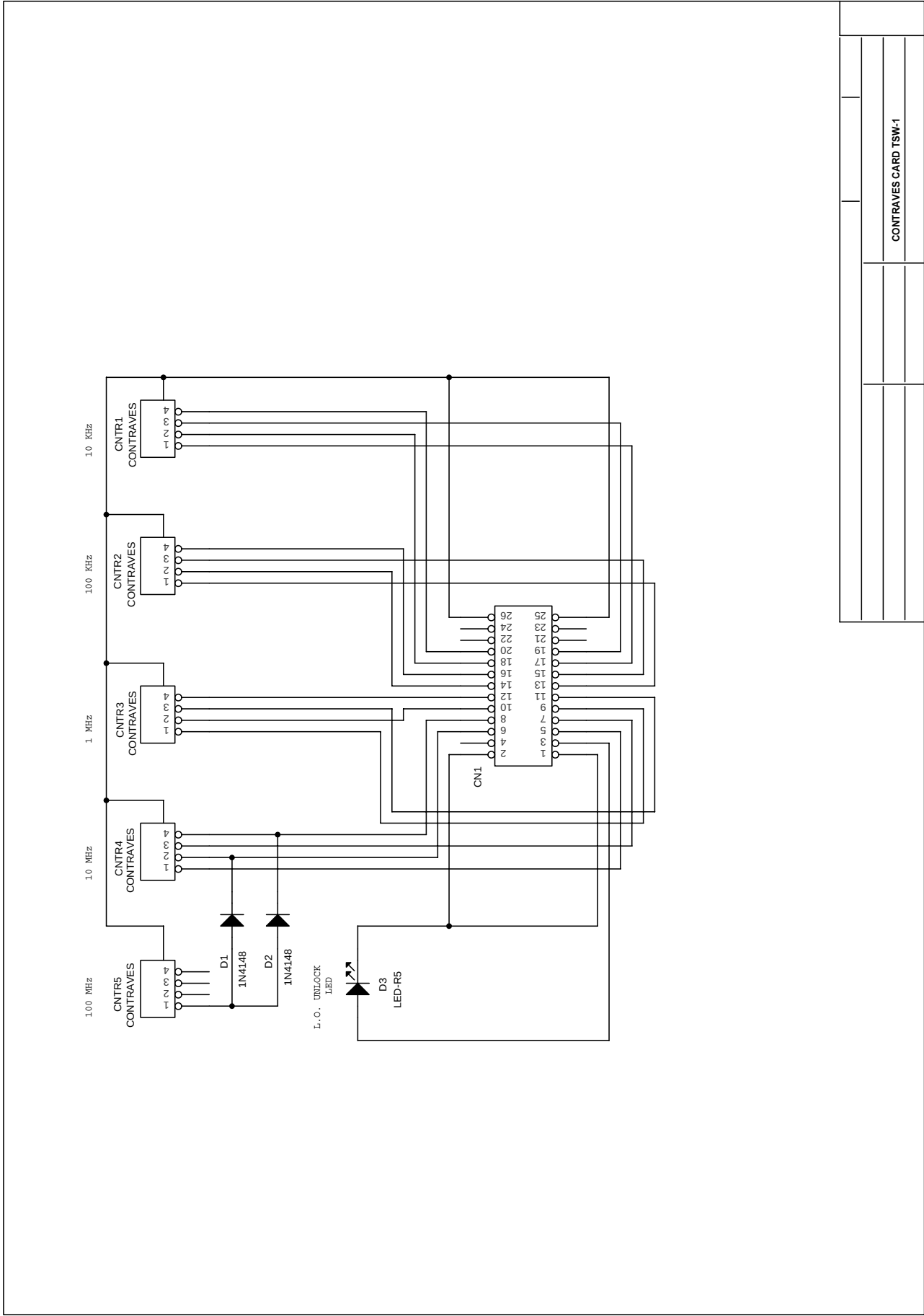


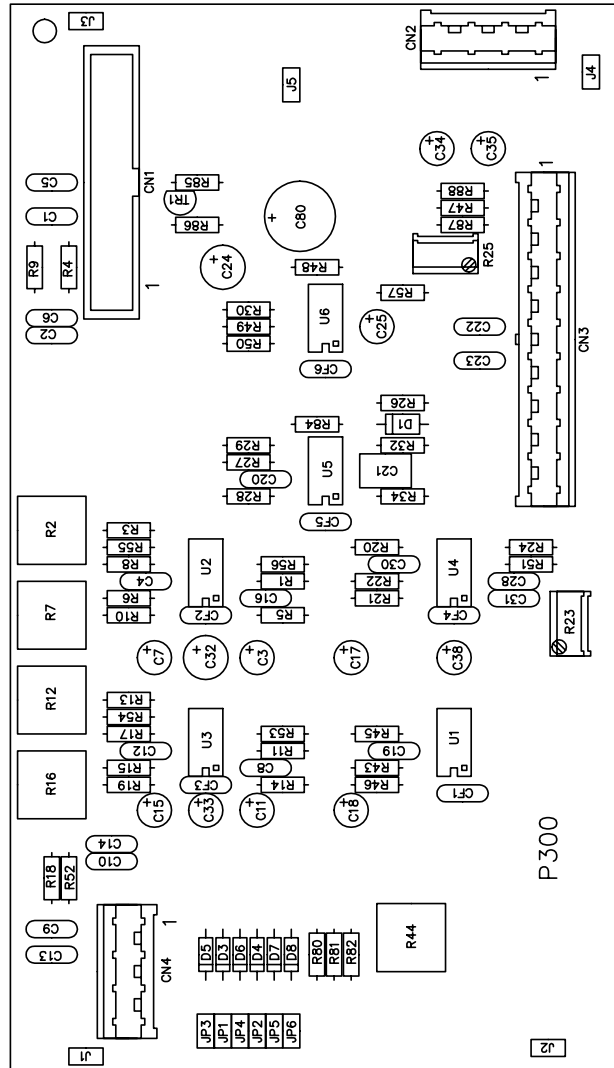


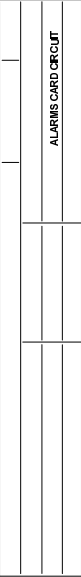


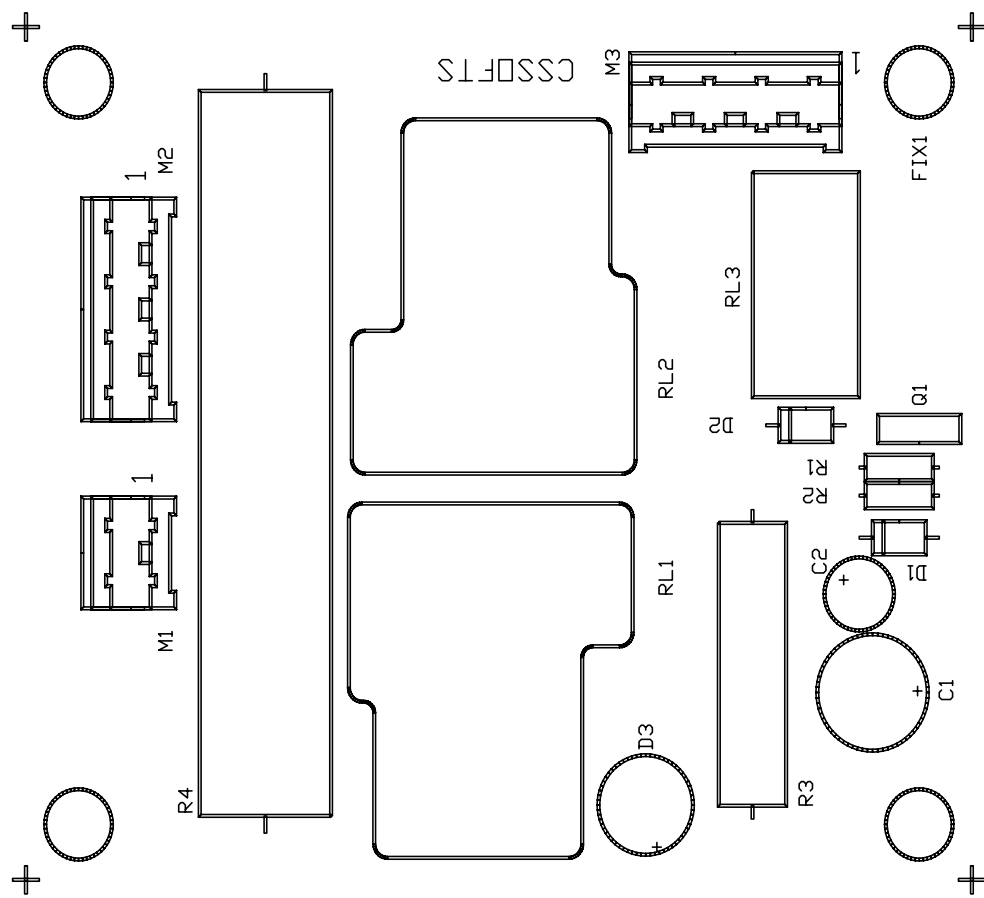


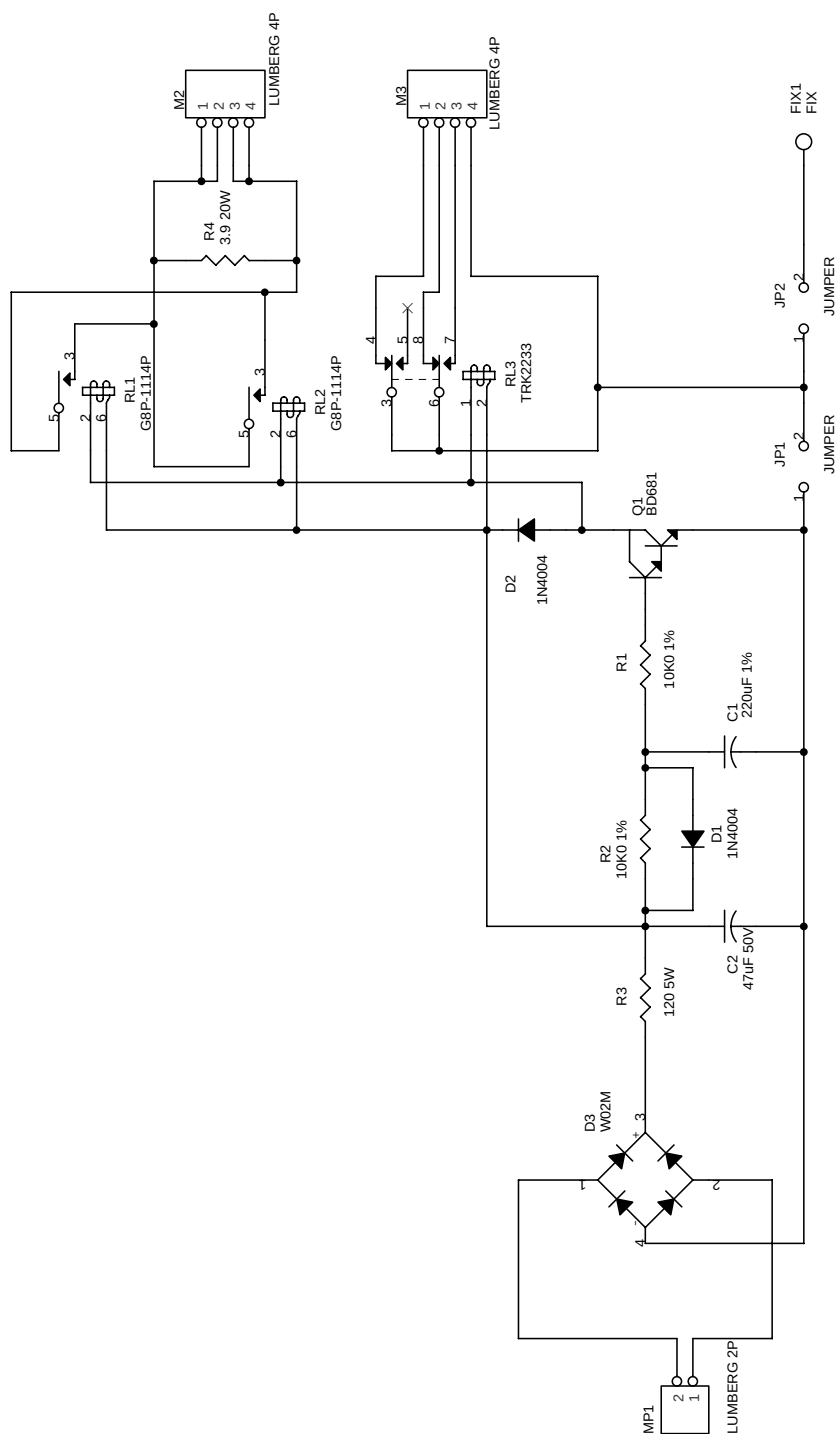






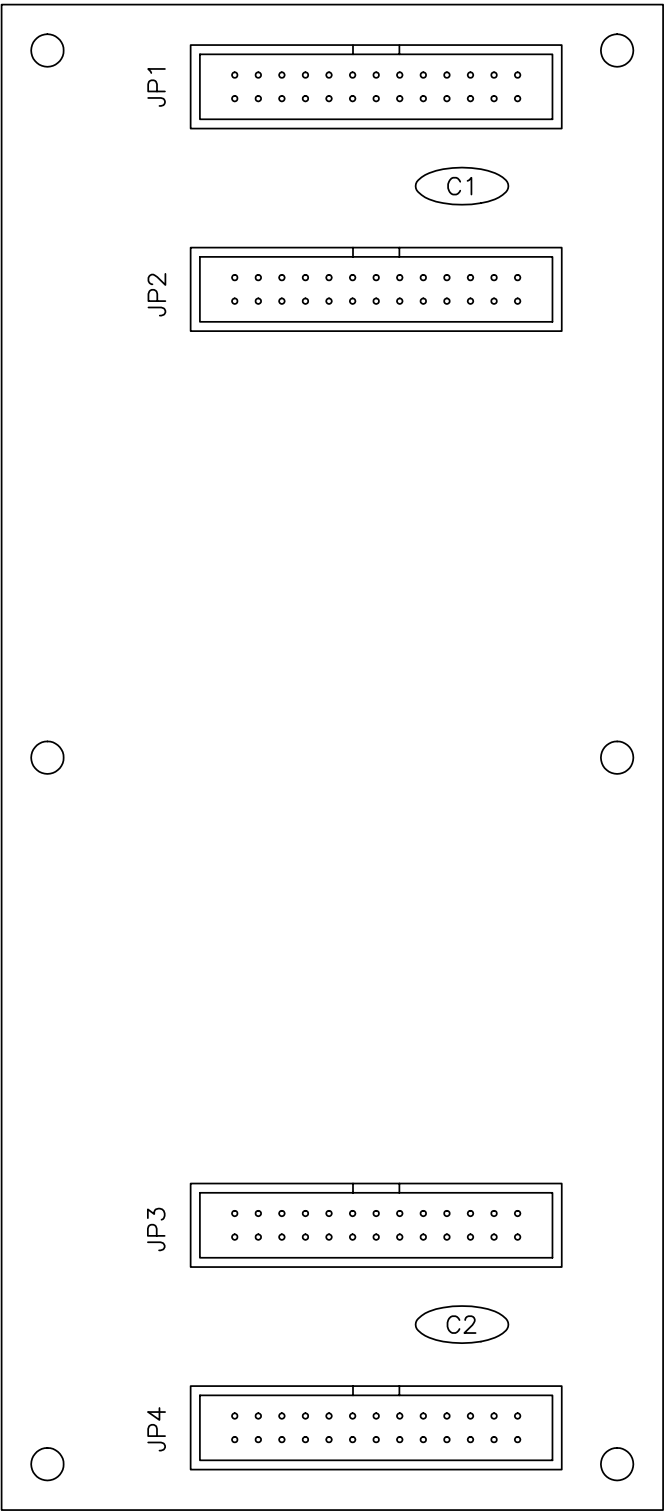


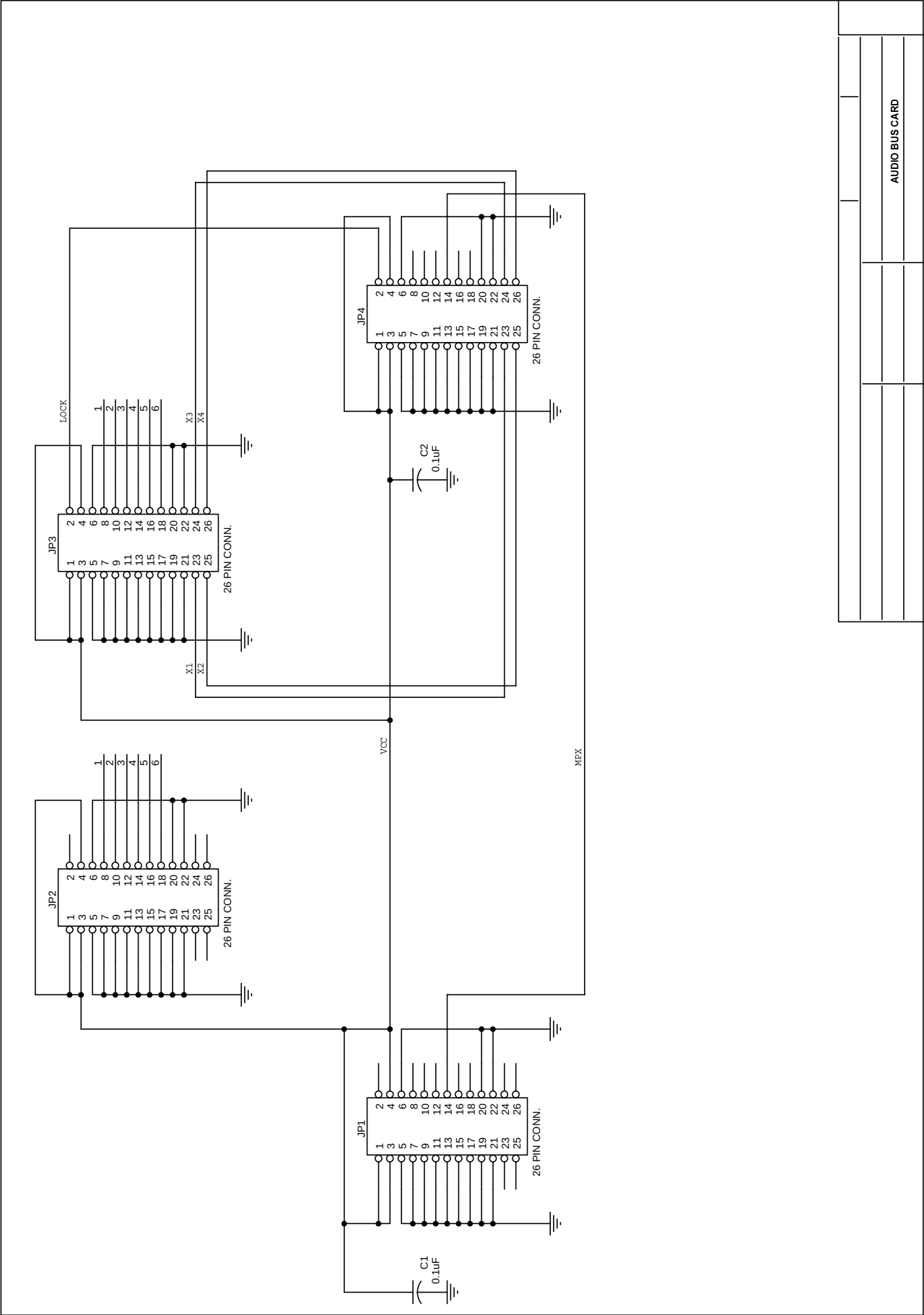


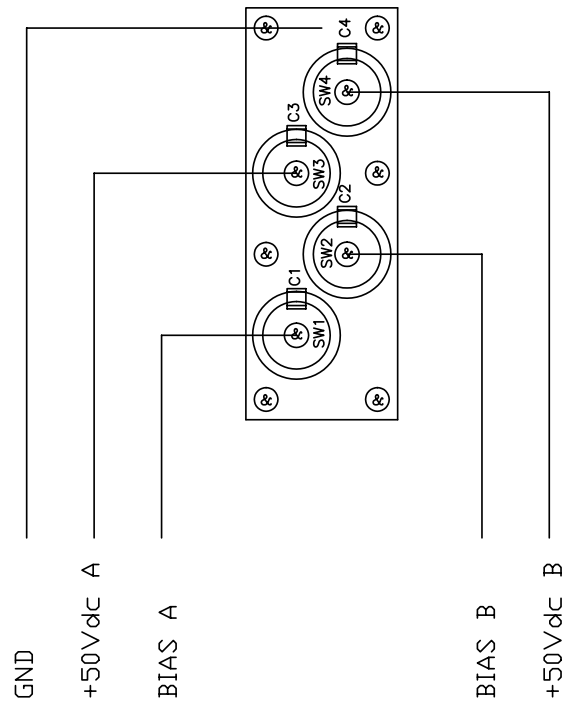


JP1,JP2: JUMPERS SMD CORTOCIRCUITATI

[illegible]







[illegible]