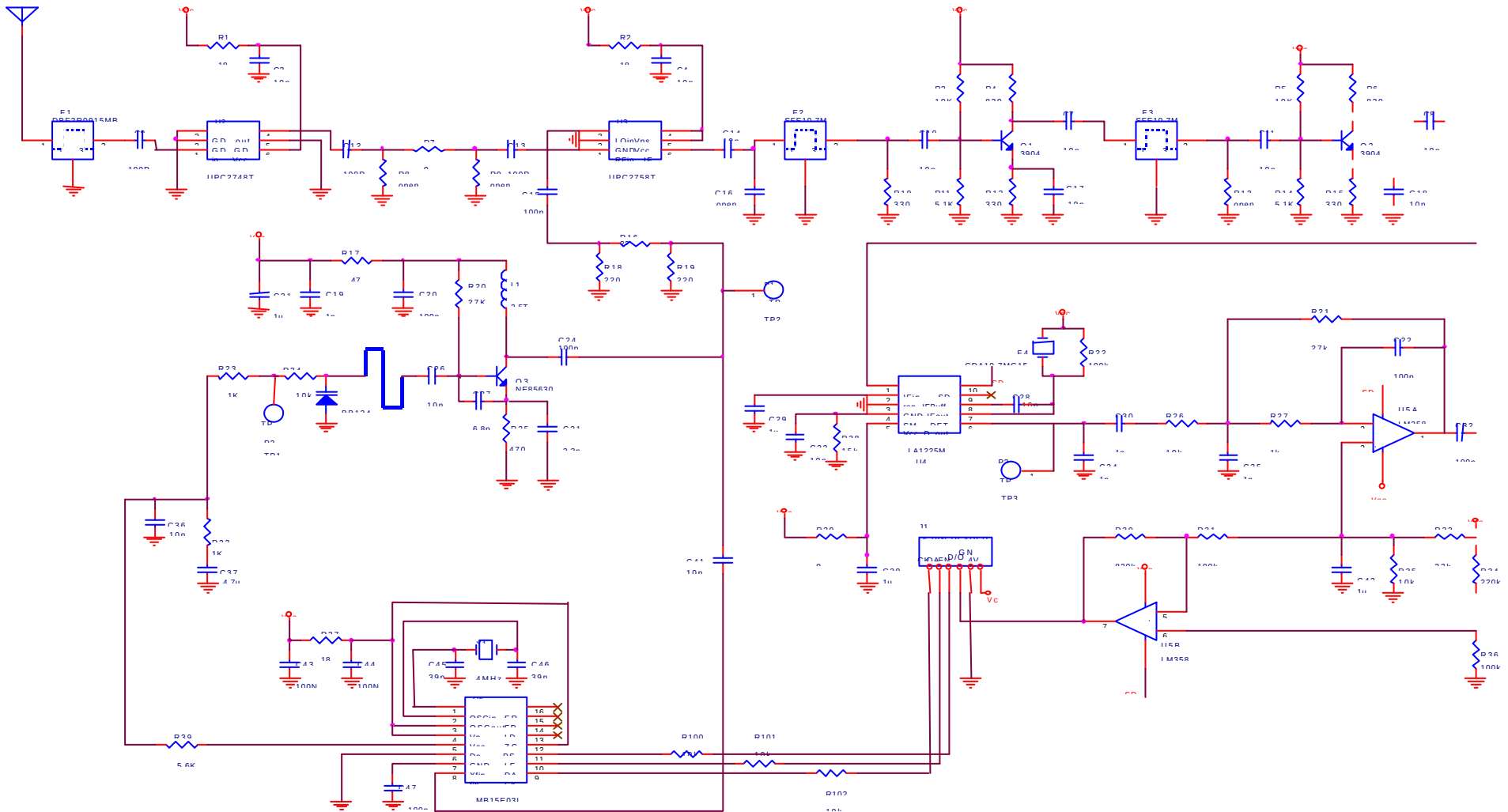
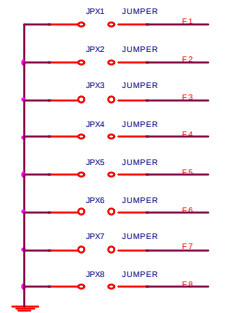
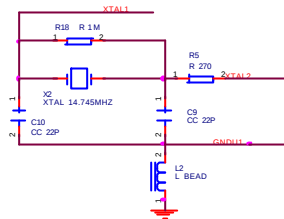
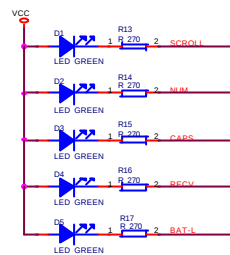


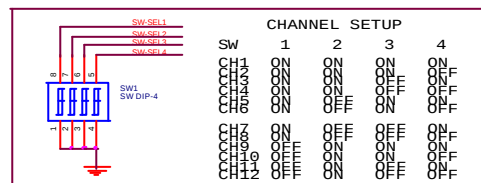
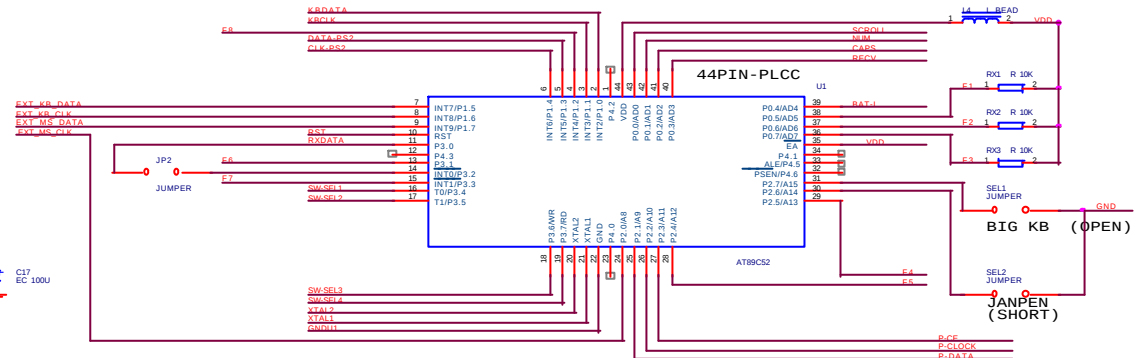
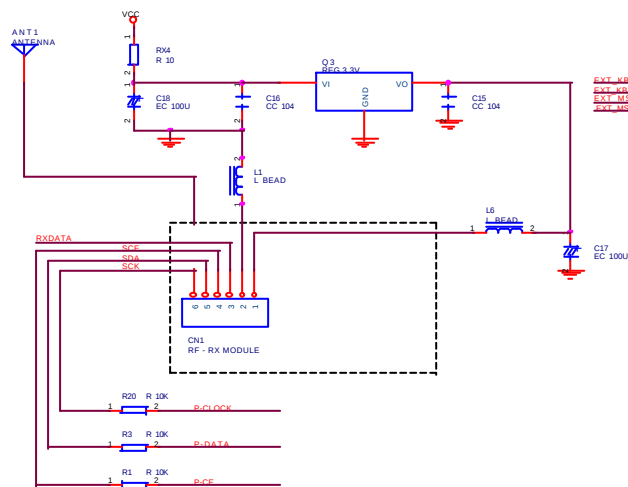
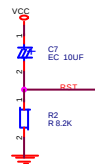
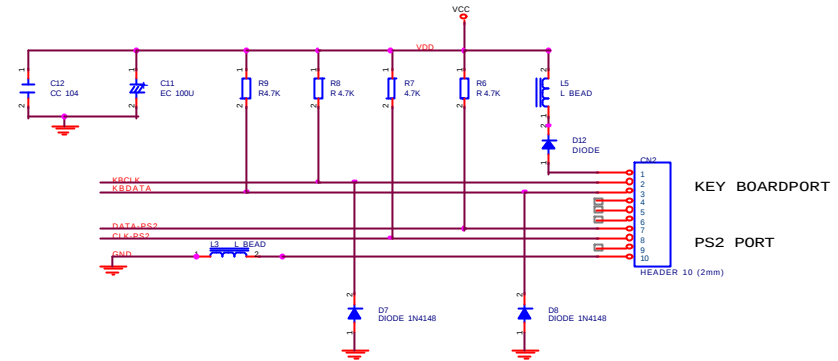
Receiver Schematic



5090-RX1.SCH

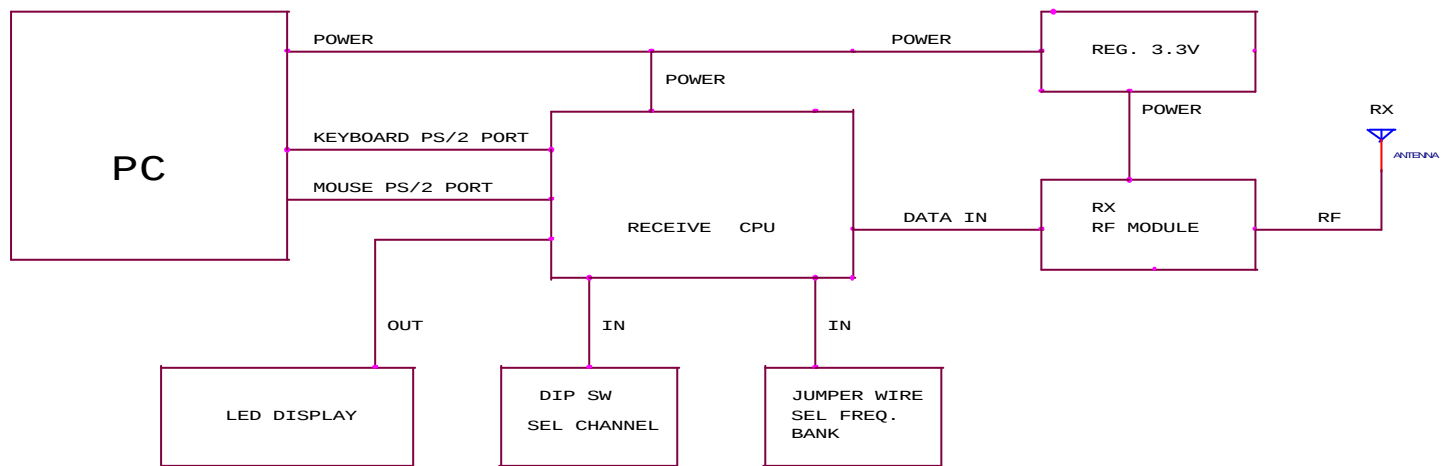
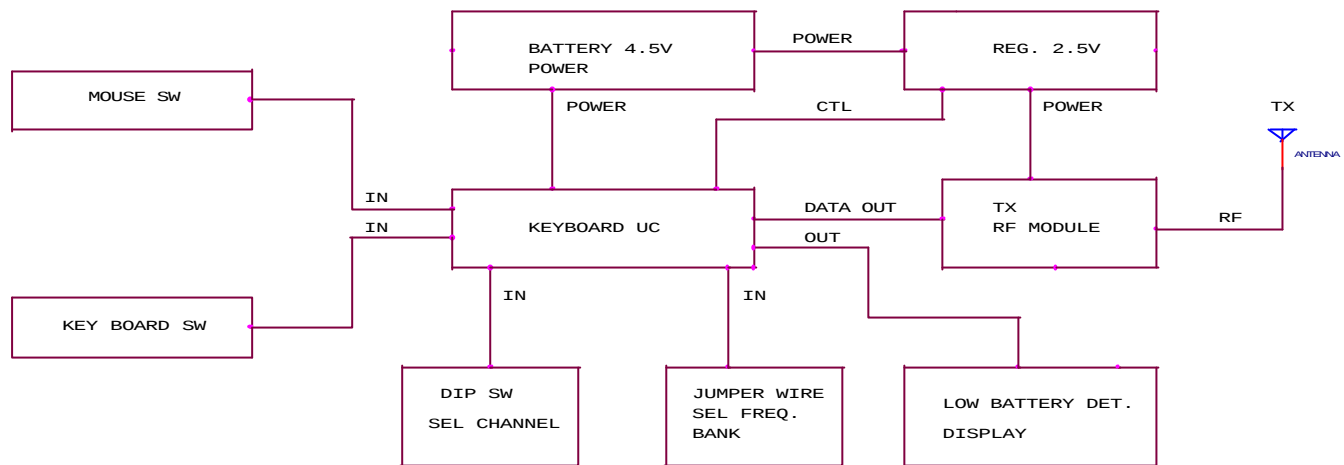


FREQ.	JPX1	JPX2
900MHZ	OPEN	OPEN
433MHZ	SHORT	OPEN
300MHZ	OPEN	SHORT
2.4GHZ	SHORT	SHORT

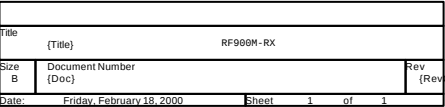


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* PS. ---> R , CC USE "SMD"
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Size Custom	Document Number 5090-RX1.SCH	Rev 2.0
Date:	Monday, November 15, 1999	



Title			
BTC			
Size	Document Number	5000 BLOCK DIAGRAM	Rev
Custom			Rev. Code
Date	Tuesday, February 22, 2000		
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RF-LINK Systems INC.
Receiver Block diagram

Rx

