



GE MDS

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April 23rd, 2010

FEDERAL COMMUNICATIONS COMMISSION

7435 Oakland Mills Road
Columbia, MD 21046
U.S.A.

Subject: C2PC justification changes

Applicant: GE MDS LLC
Product: Data Transceiver MDS LCT450
Model: MDS LCT450
FCC ID: E5MDS- LCT450

Dear Sir/Madam,


The LCT450 rev 05 board was changed to reduce PLL phase noise. This was required in receive so that we could meet the rail customer's 70dB adjacent channel rejection spec. It also improved the TX phase noise.

The change consisted of replacing the Analog Devices AD4252 PLL with the National LMX2541 PLL. The new PLL includes a built in VCO.

In both the old board and the new board the PLL output is mixed with a digitally generated 82.2MHz signal to derive the final desired frequency. Then it is filtered and amplified to the final output level. The TCXO reference frequency did not change.

If you have any queries, please do not hesitate to contact me at 585 242-8440:

Yours truly,

Signed:  Name: Dennis McCarthy

Dennis McCarthy
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