

Figure 9.4 Bottom of Circuit Board

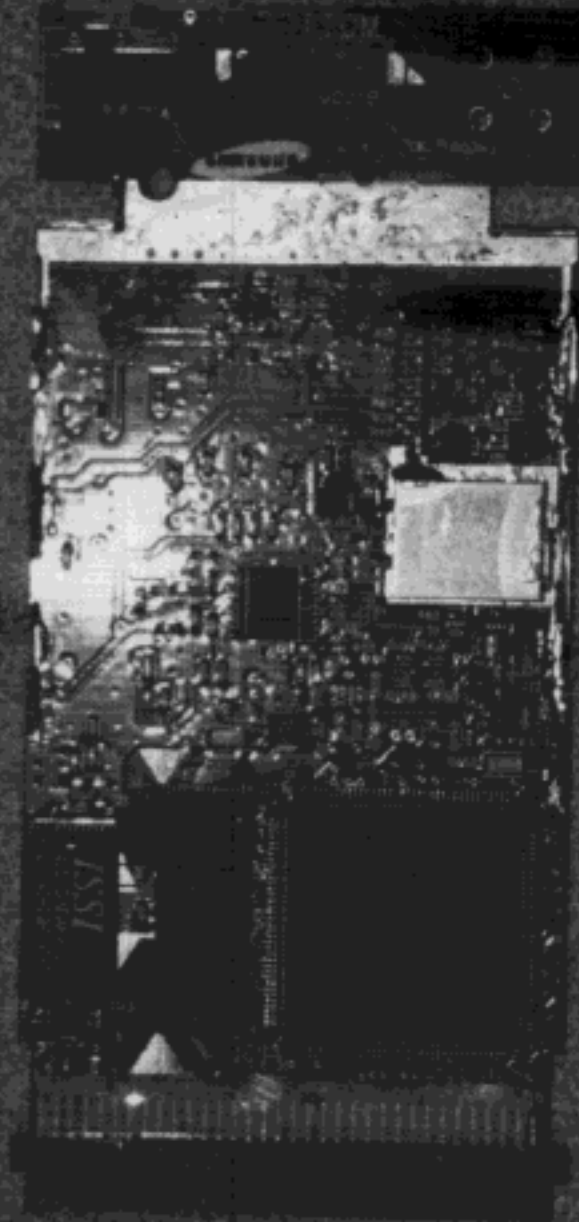


Figure 9.3 Top of Circuit Board

