

2. TX UNIT

- **Tx Match Filter is a filtering and matching circuit, which includes the capacitors C1, C4, C5 and filter of FL1**
- **RF Amp is achieved by using a transistor Q1. The RF signal is applied to the base of this transistor of Q1 for amplification only. The signal will be sent to the antenna via TX Match Filter.**
- **The Local Oscillator and Modulator is a combination of circuit components of Q2, VD1 & L1 which are constructed to form a modulator circuit.**
- **OSC. & Phase Lock Loop are formed by X1 & U1.**

- **Audio Amp is achieved by U2-A, B to provide amplified audio signal to the modulator circuit. The AGC is controlled by Q3, Q4 for automatic level control**
- **CPU (U4) provides a system control on several parts, like providing the channel A & B selector setting, providing the PLL (Phase Lock Loop) control.**
- **Regulator Circuit includes the Q5 and U3.**