



Checkpoint Systems Int. GmbH, Brentanostr. 27, 69434 Hirschhorn

Hirschhorn, den 23.02.2015

RFID POS READER V2 Modification History

During development based on REV00 following modifications were done:

Modifications for REV R01 (released RN2644A)

- Introduction of cooling bracket
- CPU with 256kbyte RAM
- Documentation for Omnify

Modifications for REV R02 (not released)

- Layout Optimizations for Manufacturing at BB
- Swap from SPI1 to SPI0 for serial SRAM
- PTA11 CPU PIN connected to GND for hardware detection

Modifications for REV R03 (not released)

- Additional SPI Flash memory U21 PN 10048122
- Additional Capacitor C69 PN 7212833
- Additional Resistor R195 PN 7371337
- Additional Resistor R196 PN 7371337
- LED DS3-B connected to 12V from Power over Ethernet
- R57 modified from PN 7550507 to PN 7332883
- Additional Resistor R197 PN 7332883

Modifications for REV R04

- Additional Inductor L19 PN 10086744
- Additional Capacitor C70 PN 10038863
- Layout Modifications: adding L19 and C70, new position for C43, D5, C1
- Documentation for Omnify
- Modification label
- Addition to type reference "CPiD RFID 400 RDR"

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Type / Model Name: RFID POS READER V2
(CPiD RFID 400 RDR)

Product Description: UHF RFID Reader
Applicant: Checkpoint Systems
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