Vehicle Nam C210			G MOTOR COMPANY SPECIFICATION		PAGE :	1 / 11
DEPARTMENT: R&D CENTER		STANDARD	SPECIFICATION		PART NU MT-C2	MBER : 10-0107
TITLE :	TRA	NSMITTER ASSY - S	tandard Speci	ficati	on	
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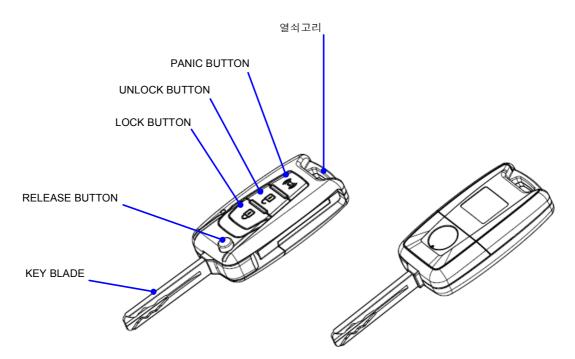


1. APPLICATION

THIS SPEC APPLIES TO C210 IMMO & BCM Transmitter ASS' Y. BUT, IF CONTENT WHAT IS DIFFERENT THIS SPEC EXIST, CONTENTS OF DRAWING SHEET IS FIRST OF ALL

1.1 SUMMARY

WHEN USER CONTROL SWITCH OF TRANSMITTER ASS' Y, DATA FORMAT OF "VEHICLE CONTROL MESSAGE" IS TRANSMITTED TO BCM BY TRANSMITTER' S CPU, AND THEN WHEN BCM RECEIVE THAT MESSAGE, BCM' S CPU CONSIDER TO CONTROL VEHICLE



PICTURE 1. Transmitter

EACH SWITCH FUNCTIONS IS SAME TO TABLE 1.1

TABLE 1.1 EACH SWITCH FUNCTIONS OF TRANSMITTER						
FUN	CTION	SWITCH FUNCTION				
LOCK	DOOR LOCK	SHORT PRESSING LOCK BUTTON UNDER 0.5s - LED flicker only once as long time				
BUTTON		LONG PRESSING LOCK BUTTON OVER 0.5s - LED flicker twice as short time				
UNLOCK BUTTON		SHORT PRESSING UNLOCK BUTTON UNDER 0.5s - LED flicker only once as long time				
PANIC BUTTON	PANIC	LONG PRESSING PANIC BUTTON OVER 2s - LED flicker twice as short time				

TABLE 1.1 EACH SWITCH FUNCTIONS OF TRANSMITTER



2. ELECTRICAL PERFORMANCE

2.1 GENERAL ELECTRICAL PERFORMANCE OF TRANSMITTER ASS' Y

LIST	REQUIREMENTS	NOTE
RATED VOLTAGE	DC 3.0V	
OPERATING VOLTAGE RANGE	DC 2.7V ~ DC 3.3V	NORMAL OPERATING FOR OPERATING
OPERATING TEMPERATURE RANGE	-10°C ~ +60°C	NORMAL OPERATING FOR OPERATING TEMPERATURE RANGE
STORED TEMPERATURE RANGE	-20°C ~ +70°C	
INSULATION RESISTANCE	IT DOESN'T EXISTED FEBRILITY OR IGNITION BY LEAKAGE CURRENT	PCB MOISTUREPROOFING AND COATING DEFINE WITH INSULATION REQUIRED ELECTRONIC COMPONENTS
USING DATE	USING ONE DAY PER 10 TIMES, OVER 2 YEARS	
DARK CURRENT	UNDER 1uA	

2.2 WIRELESS CHARACTERISTIC

- 2.3.1 DOMESTIC (DOM)
 - (1) TRANSMIT FREQUENCY: 433.92MHZ \pm 10KHZ(@ ROOM TEMPERATURE)
 - (2) CLEAR FREQUENCY BAND WIDTH: 13kHz \pm 5kHz
 - (3) OUTPUT: 3.981 mW UNDER (6dBm)
 - (4) MODULATION METHOD: FM MODULATION
 - (5) COMMUNICATION METHOD: HALF DUPLEX METHOD
- 2.3.2 GENERAL EXPORT (EXP)
 - (1) TRANSMIT FREQUENCY: 433.92MHZ \pm 10KHZ(@ ROOM TEMPERATURE)
 - (2) CLEAR FREQUENCY BAND WIDTH: 13kHz \pm 5kHz
 - (3) OUTPUT: 3.981 mW UNDER (6dBm)
 - (4) MODULATION METHOD: FM MODULATION
 - (5) COMMUNICATION METHOD: HALF DUPLEX METHOD

FCC (Federal Communications Commission)

WARNING: This equipment may generate or use radio frequency energy.

Changes or modifications to this equipment may cause harmful interference unless the modifications are expressly approved in the instruction manual.

The user could lose the authority to operate this equipment if an unauthorized change or modification is made.

This device complies with Part 15 of the FCC's Rules. Operation is subject to the following two Conditions:

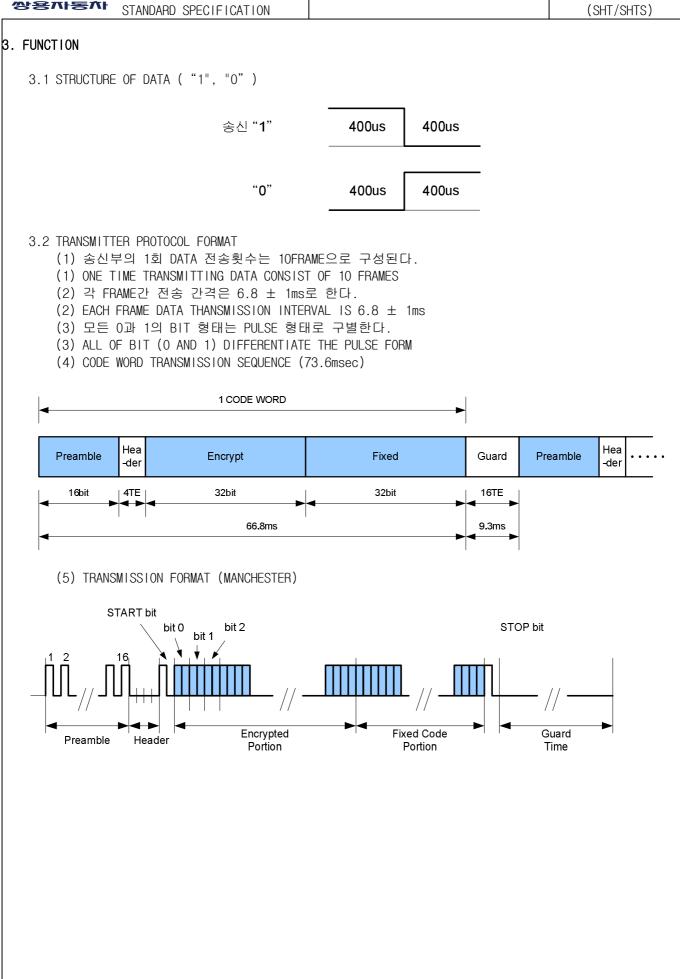
1. This device may not cause harmful interference, and

2. This device must accept ant interference received, including interference that may cause undesirable operation.



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SPEC NO: ES-





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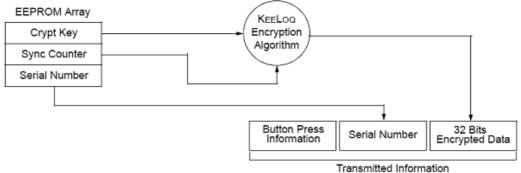
((6) CO	DE WOF	RD TRANSMISS	ION DATA FORMAT							
			Encrypted Po	ortion (32bits)				Fixed C	Code Porti	on (32 bits	3)
	BUT.	Not Ox5A Synchronization Counter DISC 16 bits BUT. Not used				SER	IAL NUMBER	AL NUMBER			
	4bits	4bits	4bits	15	0	4bits	4bits	5		24bits	
S2 S1 S0 S3											
				,							
	(7) RF	Data	Format (756		rame						>
	(7) RF I		Format (756	10 F	rame	1 Frame	1	Frame	1 Frame	1 Frame	► 1 Frame
	1 Frame 1 2 3 4	│ 1 F Prea Heade Start Encry	rame 1 Fram mble: 16BIT er: CONSIST t Bit: 1Bit ypted Portio	10 F e 1 Frame 1 Fr	rame } 4TE(T 2Bit (T	iming ABLE 3	 Eleme −1)		1 Frame	1 Frame	► 1 Frame
	1 Frame 1 2 3 4	☐ 1 Fi F Prea Heade Start Encry Fixed	rame 1 Fram mble: 16BIT er: CONSIST t Bit: 1Bit ypted Portio	e 1 Frame 1 Fr CONSIST OF "O" OF Low Level FOF CONSIST OF "1" n: CONSIST OF 32	rame 3 4TE(T 2Bit (T 32Bit (7pted F	iming ABLE 3 TABEL	Eleme -1) 3-2)	ent)	<u> </u>		► 1 Frame

Bit Address	Field	Description	Values
0 ~ 15	Count	Synchronization Counter	Synchronization Counter Value
16 ~ 17	BSEL_0, BSEL_1	Bit rate select	00- TE= 100 us 01- TE= 200 us 10- TE= 400 us * 11- TE= 800 us
18 ~ 19	MTX_0, MTX_1	Minimum number of code words	00- 1 01- 4 10- 10 * 11- 16
20 ~ 21	GUARD_0, GUARD_1	Guard time select	00- 0 ms (1 TE) 01- 6.8 ms + 2 TE * 10- 25.6 ms + 2 TE 11- 76.8 ms + 2 TE
22	MOD	Modulation Format	0 = PWM 1 = MANCHESTER *
23	HEADER	Header Length	0 = Short Header, TH = 4 x TE * 1 = Standard Header, TH = 10 x TE
24 ~ 27			Not Used
28 ~ 31	S3, S0, S1, S2	Button Status	S3, S2, S1, S00001=DOOR LOCK0101011001001001

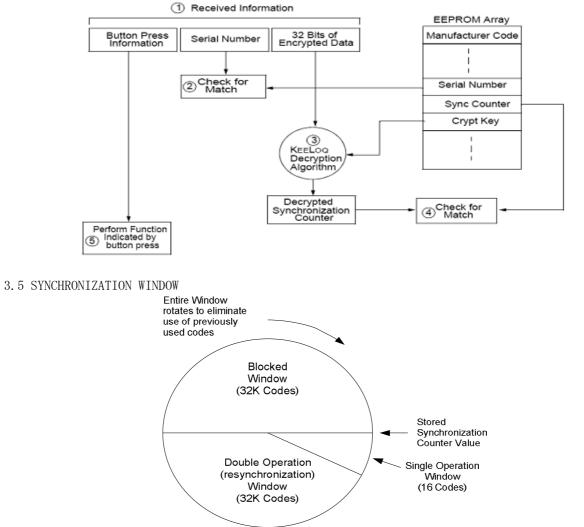


Bit Addre	SS	Fi	eld	Desci	·iption	Values			Values	
32 ~ 55	I DO	~	D23	Serial	Number					
56 ~ 59						0 0	1 0			
60 ~ 63	S3,	SO,	S1, S2	Button	Status	S3, 0 0 0 0	S2, 0 0 0 1	0	S0 1 0 1 0	= DOOR LOCK = DOOR UNLOCK = PANIC = ESCORT

3.3 BUILDING THE TRANSMITTED CODE WORD (ENCODER)



3.4 BASIC OPERATION OF RECEIVER (DECODER)

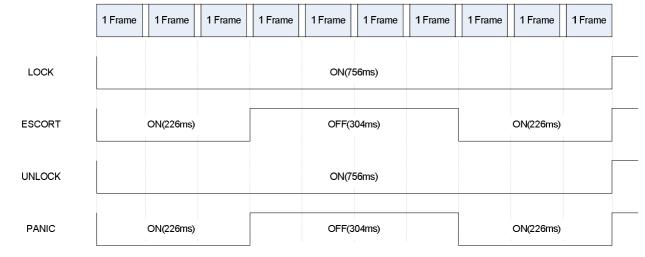




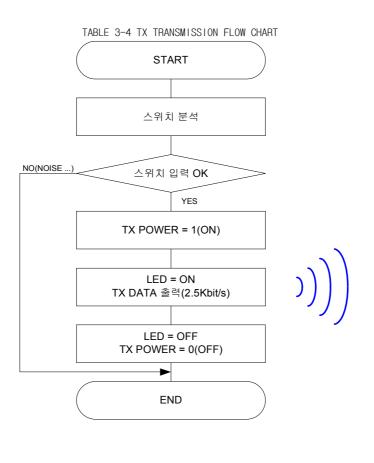
3.6 LED FLICKERING PROCEDURE

LED FLICKERING PERFORM ON/OFF PROCEDURE

TABLE 3-3 LED TURNED THE LIGHT ON TIME BY EACH FUNCTION



3.7 RADIO FREQUENCY TRANSMISSION PROCEDURE





3.8 REMOCON FUNCTION OF EACH SIGNAL

	TABLE 3-8 REMOCON FUNCTION OF EACH SIGNAL									
SYMBOL	L SIGNAL NAME (PIN NO)		CONTENTS	OPERATING	NOTE					
VSS	GND	POWER (14)	Ground							
RA0	LOCK	INPUT (13)	HIGH: PRESSING	- SHORT PRESSING - DOOR LOCK - LONG PERSSING - ESCORT						
RA1	UNLOCK	INPUT (12)	HIGH: PRESSING	- SHORT PRESSING - DOOR UNLOCK						
RA2	Not Used	(11)			Spare IO Port					
RC0	POWER CON	OUTPUT (10)								
RC1	Not Used	(9)			Spare IO Port					
RC2	TX DATA	OUTPUT (8)	RF DATA OUTPUT	DATA OUTPUT ABOUT SWITCH INPUT	625Hz					
RC3	PA CON	OUTPUT (7)	RF PA OUTPUT	POWER AMP CONTROL	736ms					
RC4	LED	OUTPUT (6)	LOW: LED ON	Data OUTPUT Display						
RC5	Not Used	(5)			Spare IO Port					
/MCLR	RESET	INPUT (4)	WHEN POWER TURN ON, IT RESET							
RA4	PANIC	INPUT (3)	HIGH: PRESSING	- LONG PRESSING - PANIC	Spare IO Port					
RA5	Not Used	(2)			Spare IO Port					
VDD	BAT	POWER (1)	BAT POWER SUPPLY	- POWER SUPPLY TO MICOM	NORMAL : 2.1V < BAT < 3.5V					

3.9 POWER ON INITIALIZATION

(1) DEFINE ABOUT ALL OF I/O (INPUT/OUTPUT) PINS

(2) WHEN CPU CHANGE FROM POWER DOWN MODE TO ACTIVE MODE, DEFINE EACH PORT'S CONDITION

SYMBOL	PIN NO	1/0	CONDITION	CONTENTS	SYMBOL	PIN NO	1/0	CONDITION	CONTENTS
VDD	1	POWER	HIGH		RC2	8	OUTPUT	_	DATA
RA5	2	-	_	Not Used	RC1	9	-	_	Not Used
RA4	3	INPUT	HIGH	HIGH PERCEIVING	RC0	10	OUTPUT	HIGH	HIGH ON
/MCLR	4	INPUT	HIGH	LOW PERCEIVING	RA2	11	Ι	-	Not Used
RC5	5	_	_	Not Used	RA1	12	INPUT	HIGH	HIGH PERCEIVING
RC4	6	OUTPUT	LOW	LOW ON	RA0	13	INPUT	HIGH	HIGH PERCEIVING
RC3	7	OUTPUT	HIGH	HIGH ON	VSS	14	POWER	LOW	Ground

TABLE 3-8 REMOCON FUNCTION OF EACH SIGNAL



SSANGYONG MOTOR COMPANY STANDARD SPECIFICATION

SPEC NO: ES-

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	STANDARD SPECIFICATION	N						(SHI/SHIS)
4. OPERATING								
(1) AFTER AND T (2) DURIN IT PE (3) IF SW (4) WHEN	CH OF TRANSMITTER PRESS SWITCH PRESSING (PRESS RANSMIT DATA G MICOM TRANSMIT DATA, RFORM EXISTING OPERATIN ITCH PRESSING TIME IS U ALL OF OUTPUT COMPLETIN ER DOWN MODE"	SING TI IF BUT NG JNDER C	IME = (ITON IN).1s, N).1 ~ (NPUT EN NICOM).5s) ITER, GNOR	, IMME MICOM E THAT	DIATE IGNO SIGN	GNAL ± 5ms, IT WILL BE ENTERED
SWITCH	0.1~0.5s							0.1s 미만무시
TX PWR							****	
TX DATA								
LED –				LED O	N			
(1) AFTER AND T (2) DURIN IT PE (3) IF SW (4) WHEN	CH OF TRANSMITTER PRESS SWITCH PRESSING (PRESS RANSMIT DATA G MICOM TRANSMIT DATA, RFORM EXISTING OPERATIN ITCH PRESSING TIME IS U	SING TI IF BUT NG JNDER (IME = (FTON).1s,).1 ~ (NPUT EN NICOM).5s) NTER, IGNOR	, IMME MICON E THAT	EDIATE I IGNO SIGN	
TX PWR								
TX DATA]
LED -				LED O	N			
(1) IF SW TRANSM (2) DURING IT PE (3) WHEN /	CH OF TRANSMITTER PRESS ITCH IS PRESSED LONGER MIT DATA G MICOM TRANSMIT DATA, RFORM EXISTING OPERATIN	THAN 2 IF BUT NG	s, AT TON IN	THE SA IPUT EN	ME T TER,	IME, TH MICOM	RANSM I GNO	MITTER IS POWER ON AND

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SSANGYONG MOTOR COMPANY STANDARD SPECIFICATION SPEC NO: ES-

	TANDAND SILCTI TOATT				(311/3113)
SWITCH	2.0s ~	7			0.1s 미만 무시
		 2s 이상			
	-	•			
TX PWR					
TX DATA					
LED		LED ON	LED OFF	LED ON	
(1) IF SWITC AND TRAI (2) DUR IT PERF((3)WHEN ALL "POWER DO	OF TRANSMITTER PRE CH IS PRESSED LONGEN NSMIT DATA ING MICOM TRANSMIT ORMS EXISTING OPERA	R THAN 0.5s DATA, IF BU TING.	, WHEN THE TIME	E BECOME 1s, TR ER, MICOM IGNO	ANSMITTER IS POWER ON
TX PWR					
TX DATA					
LED		LED ON	LED OFF	LED ON	
ID CODE AND TO I , THEREI THE USEI (2) ID CODE ID CODE 4.6 REMOCON RES (1) WHEN SW (2) DURING CONTINU	G THE SAME ID CODE IS CONTROLED VEHIC PREVENTED MALFUNCTI FORE IT MUST BE CHO R' S SATISFACTION A COMPOSITION IS WHAT CONBINE " PONSE OF OPERATING ITCH IS PRESSING, I TRANSMIT 10 FRAMES, OUS SWITCH AND PERF G SWITCH OPERATING	CLE BY WIREL ON OF VEHIC SEN CAREFUL ND POST-MAN 24 MULTIPL CONTINUOUS T BASICALLY IF THE SW ORM EXISTIN	LESS, SO TO MAI CLE BYINTERFERE LLY IN TERM OF NAGEMENT IER OF 2" CODE SWITCH Y TRANSMIT 10 F ITCH PRESSES SE NG TRANSMISSION	NTAINED ACCURA NCE ARE VERY IN A THEFT OF THE RAME VERAL TIMES, M	VEHICLE AND

	STAN	IUANU SEL							(SH	
SWITCH	I	Γ	SW입력			송신중 SW {	입력 무시		·	
TX PWR		J								
ΤΧ ΔΑΤΑ					ורחר			L		
					UU					
		(0000.00								
5.1 REVIS (1) L	SION SPO1 _IST : 3-2	의 6) CC	DDE WORD TRANSM							
5.1 REVIS (1) L	SION SPO1 _IST : 3-2	의 6) CC			0001 =	> 0010				
5.1 REVIS (1) L	SION SPO1 IST : 3-2 NOT USED F	2의 6) CC RANGE OF	DDE WORD TRANSM		0001 =		ed Code	Portion (32 bits)	
5.1 REVIS (1) L	SION SP01 IST : 3-2 NOT USED F Encr Not used	2의 6) CC AANGE OF ypted Poi	DDE WORD TRANSM Fixed Code Por	rtion : (0001 = BUT.		ed Code	Portion (SERIAL N		
5.1 REVIS (1) L (2) N	SION SP01 IST : 3-2 NOT USED F Encr Not used	2의 6) CC RANGE OF ypted Po	DE WORD TRANSA Fixed Code Por rtion (32bits) Synchroniza Counter	rtion : (Fixe	ed Code		IUMBER	
5.1 REVIS (1) L (2) N BUT. 4bits	SION SP01 IST : 3-2 NOT USED F Encr Not used	2의 6) CC AANGE OF ypted Poi 0x5A DISC	DE WORD TRANSA Fixed Code Por rtion (32bits) Synchronizat Counter 16 bits	rtion : (BUT.	Fixe Not used 4bits	ed Code	SERIAL N	IUMBER	
5.1 REVIS (1) L (2) N BUT. 4bits	SION SP01 IST : 3-2 NOT USED F Encr Not used 4bits	2의 6) CC AANGE OF ypted Poi 0x5A DISC	DE WORD TRANSA Fixed Code Por rtion (32bits) Synchronizat Counter 16 bits	rtion : (BUT. 4bits	Fixe Not used 4bits		SERIAL N	IUMBER	
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5.1 REVIS (1) L (2) N BUT. 4bits	SION SP01 IST : 3-2 NOT USED F Encr Not used 4bits	29 6) CC AANGE OF ypted Por 0x5A DISC 4bits	DE WORD TRANSA Fixed Code Por rtion (32bits) Synchronizat Counter 16 bits	rtion : (BUT. 4bits	Fixe Not used 4bits 0 0	1	SERIAL N	NUMBER	
5.1 REVIS (1) L (2) N BUT. 4bits 0	SION SP01 IST : 3-2 NOT USED F Encr 4bits 0 0 0 0 Encry Not 0 Used	29 6) CC XANGE OF ypted Poil 0x5A DISC 4bits - ypted Poil ypted Poil 0x5A - DISC - 4bits - ypted Poil ypted Poil 0x5A - 0x5A -	DDE WORD TRANSA Fixed Code Por rtion (32bits) Synchronizat Counter 16 bits 15 rtion (32bits) Synchronizat Counter	tion : (BUT. 4bits	Fixe Not used 4bits 0 0	1	SERIAL N 24b	NUMBER bits (32 bits)	
5.1 REVIS (1) L (2) N BUT. 4bits 0	SION SP01 IST : 3-2 NOT USED F Encr Abits 0 0 0 0 Encry Not 0 Used 0	29 6) CC AANGE OF ypted Por 0x5A DISC 4bits	DDE WORD TRANSA Fixed Code Por rtion (32bits) Synchronizat Counter 16 bits 15	tion : (BUT. 4bits	Fixe Not 4bits 0 0 Fixe Not	1	SERIAL N 24b	NUMBER bits (32 bits)	
(1) L (2) M BUT. 4bits 0 BUT. 4bits	SION SP01 IST : 3-2 NOT USED F Encr Abits 0 0 0 0 Encry Not 0 used 1 C	29 6) CC AANGE OF ypted Por 0x5A DISC 4bits	DDE WORD TRANSA Fixed Code Por rtion (32bits) Synchroniza Counter 16 bits 15 rtion (32bits) Synchronizat Counter 16 bits	tion : (BUT. 4bits 0	Fixe Not used 4bits 0 0 Fix Not used 4bits	1	SERIAL N 24b	NUMBER bits (32 bits)	