

# LECTROSONICS TEST & ALIGNMENT PROCEDURE<sup>1</sup>

**MODEL: UT700 Audio Board**

**REV: 17302  
FIRMWARE VER: 1.0**

**DATE: 25 FEB 2004**

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**Setup:** Preamp gain pot (R16) to min. (CCW). 3.5VDC in measured at J4-3.

<u>MEASUREMENT.</u>	<u>TEST CONDITIONS</u>	<u>TYPICAL MEASUREMENT RESULT</u>
<u>Input Voltage:</u>		+3.5 Vdc measured at J4-3 (+/- 0.10)
<u>Current Draw before programming:</u>		MAX 70mA
<u>Reference Voltages:</u> Measure DC voltage at:		U16 Pin 1 (C10) 1.8VDC (+/- 0.1) U15 Pin 6 (C28) -3.2VDC (+/- 0.2) J3 Pin2 +3.5VDC (+/- 0.2S)
<u>Program Processor:</u> Program Microprocessor and apply label. <b>(FACTORY ONLY)</b>		
<u>Current Draw after programming:</u>		75mA (+/- 5ma)
<b>9VDC TO J4-1.</b>		
<u>Power Up LED Test:</u> Power D.U.T. down and back up: Observe operation of LED's D5, D6 and D9 as follows: After approximately 1 second:		All 3 LED's RED. D2 GREEN, D5 and D6 OFF
<u>Noise Floor. Preamp gain pot to Max.</u> Measure noise level @ TP4. Measure noise level @ TP5.		Approx. - 74dBV (+/- 2.0) Approx. - 57dBV (+/- 2.0)
<u>Preamp gain pot to Min.</u> Measure noise level @ TP4. Measure noise level @ TP5.		Approx. - 80dBV (+/- 4.0) Approx. - 80dBV (+/- 4.0)
<u>Reference Voltages:</u> Measure DC voltage at:		TP3 0.0VDC (+/- 0.001) TP4 1.525VDC (+/- 0.004) TP5 1.525VDC (+/- 0.004)
<u>Limiter output test:</u> <b>1kHz , -27BV audio signal coupled into mic jack J1- 2 through a 33? F capacitor ( "+" side of cap toward input).</b>		
<u>-20 -10 LED test:</u>	-27dBV 250Hz into DUT -17dBV 250Hz into DUT -8dBV 250Hz into DUT	D5 GREEN, D6 OFF D5 GREEN, D6 GREEN D5 RED, D6 GREEN
<u>Frequency Response:</u>	<b>Use 250Hz reference. Ratio Analyzer. Measured at TP4</b>	40Khz -5.1dbr (+/-0.5) 20Khz +0.6dBr (+/- 0.2) 10Khz +0.3dBr (+/- 0.2) 1Khz +0.1dBr (+/- 0.2) 100Hz -0.4dBr (+/- 0.2)
<u>Mic Gain Pot Test:</u> <b>-55dBV in at J1. Gain Pot R16 at Minimum. Ratio Audio Analyzer. Measure at TP4.</b>	Mic Gain Pot to Mid and Measure Level Mic Gain Pot to Max and Measure Level	Approx. 29 dBr (+/- 2.0) Approx. 35 dBr (+/- 3.0)
<b><u>NOTE:</u> Return Mic Gain Pot to Minimum after test.</b>		
<b>Set SW1 and SW2 to FF.</b>		

<sup>1</sup> Test condition changes are in **bold** type. Measurement results are typical and do not indicate 'specs' unless min., max., or a range is specified. Small deviations from these 'typical' values does not constitute malfunction. Measurement result values apply only if this procedure (including initial setup) is followed. Void where prohibited by law.

Setup: Connect RF board to audio board, apply 9 vdc to battery contacts. Measure RF output at J2, J4 (J4 is Gnd).  
Turn power switch S3 ON (up position).

<u>MEASUREMENT.</u>	TEST CONDITIONS	TYPICAL MEASUREMENT RESULT
<u>Check current draw:</u>		<110 ma
<u>Check VCO control voltage:</u>	<b>Audio Bd. S1, S2 (F,F)</b> <b>Audio Bd. S1, S2 (0, 0)</b>	<3 vdc >1 vdc
<u>Measure output power:</u>		+17 dbm
To adjust output power: Install jumper from J7-1 to J7-7 on audio board, set S1 to "A" and adjust power with S2. After adjustment, remove jumper.		
<u>Set operating frequency:</u>		Operating freq. +/- 1.5 KHz
To adjust frequency: Install jumper from J7-1 to J7-7 on audio board, set S1 to "F" and adjust frequency with S2. After adjustment, remove jumper.		
<u>Carrier NULL:</u>		>25 db below lower sideband
To adjust carrier NULL: Install jumper from J7-1 to J7-7 on audio board, set S1 to "0" and adjust S2 for best NULL. Set S1 to "1" and adjust S2 for best NULL. Repeat S1 "0 and 1" adjustments until best NULL has been achieved. After adjustment, remove jumper.		

This completes the alignment.