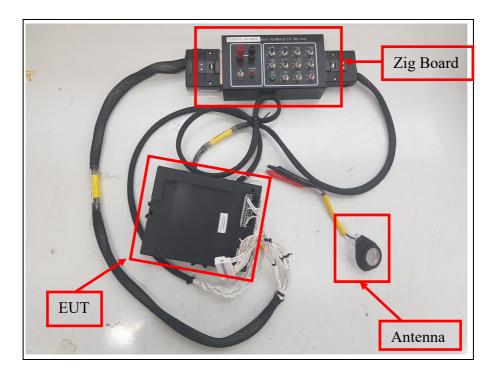


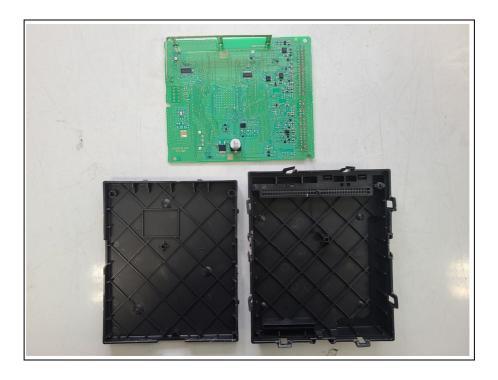
# **Internal photos of EUT**



## View of Test Zig and EUT

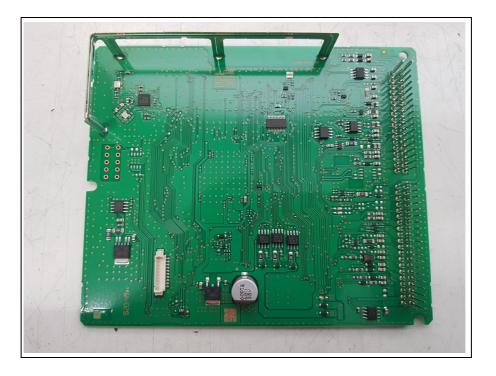


### Internal view of EUT





## Top view of Main PCB



### Bottom view of Main PCB

